

TMS320C55x™ DSP Functional Overview

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About This Manual

This document provides a functional overview of the devices included in the TMS320C55x™ generation of digital signal processors (DSPs). Included are descriptions of the CPU architecture, bus structure, memory structure, and on-chip peripherals. Detailed descriptions of device-specific characteristics such as package pinouts, package mechanical data, and device electrical characteristics are included in separate device-specific data sheets.

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