

*Advanced Micro
Devices, Inc.*

v.

Aquila Innovations Inc.

U.S. Patent No. 6,239,614

IPR2019-01525

Aquila Innovation Inc.'s Demonstratives

Grounds

Ground	References	Type	Challenged Claims
1	Urano & Mutoh021	§ 103	1-3
2	Mutoh & Mutoh021	§ 103	1-3
3	Douseki & Ramus	§ 103	4-5

Urano, Mutoh, and Mutoh021

- Urano, Mutoh, and Mutoh021, like the '614 patent, arrange high-threshold CMOS transistors adjacent to low-threshold CMOS transistors on the same chip. POR at 17.
- The high-threshold transistors can serve as power switch cells. *Id.*

Urano, Mutoh, and Mutoh021

- These references do not discuss any benefits or drawbacks associated with how the power switch cells are arranged. POR at 18.

Ground 1: Urano & Mutoh021

- Petitioner construes “disposed around” as “a power switch comprising a plurality of third MOS transistors wherein a portion of the plurality of the third MOS transistors **encircle said unit cell array.**” POR at 19 (citing Paper No. 1 at 21).
- Urano does not disclose power switch cells “encircling the unit cell array.” POR at 19 (citing Paper No. 1 at 52).

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