UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

ADVANCED MICRO DEVICES, INC.
Petitioner

v.

AQUILA INNOVATIONS, INC.
Patent Owner

Case IPR2019-01525
Patent 6,239,614 B1

PETITION FOR *INTER PARTES* REVIEW

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1005	Mutoh et al., "1-V Power Supply High-Speed Digital Circuit Technology with Multithreshold-Voltage CMOS," *IEEE Journal of Solid-State Circuits*, Vol. 30, No. 8, 847-854 (1995) ("Mutoh")
1006	U.S. Patent No. 6,653,693 to Makino ("Makino")
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1016	Saigo et al., "A 20 K-Gate CMOS Gate Array," *IEEE Journal of*

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	Solid State Circuits, Vol. SC-18, No. 5, 578-584 (1983)
1017	Sato et al., "A Subnanosecond 2000 Gate Array with ECL 10OK Compatibility," Vol. ED-31, No. 2, 139-143 (1984)
1018	Massetti et al., "A CMOS-Based Mixed Analog-Logic Standard Cell Product Family," *IEEE 1988 Custom Integrated Circuits Conference*, 24.1.1 (1988)
1019	Horowitz et al., "Chapter 2: Transitors," *The Art of Electronics*, 2nd Edition, Cambridge University Press (1989)
1020	U.S. Patent No. 4,001,869 to Brown ("Brown")
1021	U.S. Patent No. 4,499,387 to Konishi ("Konishi")
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1023	U.S. Patent No. 6,285,052 to Draper ("Draper")
1024	U.S. Patent No. 6,292,015 to Ooishi *et al*. ("Ooishi")
1025	Baker et al., "CMOS Circuit Design, Layout, and Simulation" *Institute of Electrical and Electronics Engineers, Inc.* (1998)
1026	Sato et al., "A Subnanosecond 2000 Gate Array with ECL 100K Compatibility," *IEEE Journal of Solid-State Circuits*, Vol. SC-19, No. 1, 5-9 (1984)
1027	Smith et al., "A CMOS-Based Analog Standard Cell Product Family," *IEEE Journal of Solid-State Circuits*, Vol. 24, No. 2, 370-379 (1989)
1028	U.S. Patent No. 6,340,825 to Shibata ("Shibata")
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