

United States Patent [19]

CAPACITOR STRUCTURE

SEMICONDUCTOR MEMORY DEVICE

[75] Inventors: Yoichi Tobita; Kenji Tokami, both of

Assignee: Mitsubishi Denki Kabushiki Kaisha,

Hyogo, Japan

INCLUDING STABILIZING CAPACITIVE ELEMENTS EACH HAVING A MOS

Tobita et al.

[11] Patent Number:

5,544,102

[45] Date of Patent:

Aug. 6, 1996

FOREIGN PATENT DOCUMENTS

49-114337 of 1974 Japan . 2-273393 of 1990 Japan .

OTHER PUBLICATIONS

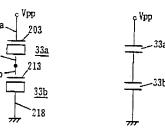
Martino Jr., et al., "An On-Chip Back-Bias Generator for MOS Dynamic Memory", IEEE Journal of Solid-State Circuits, vol. SC-15, No. 5, Oct. 1980, pp. 820-826.

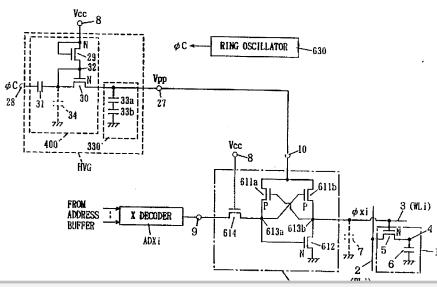
Primary Examiner—David C. Nelms Assistant Examiner—F. Niranjan Attorney, Agent, or Firm—Lowe, Price, LeBlanc & Becker

[57] ABSTRACT

In a semiconductor memory device including a boosting circuit for generating a high voltage constantly, and a word line driving circuit for transmitting a high voltage from the boosting circuit on a selected word line, a capacitor for stabilizing the high voltage generated by the boosting circuit is formed of a series of capacitive elements using a FET having a gate insulating film identical in thickness to that of a insulating gate type field effect transistor in the memory device. A voltage applied across each capacitive element is relaxed, and the capacitor is improved in dielectric breakdown voltage characteristics, to stably supply the high voltage.

6 Claims, 18 Drawing Sheets





AMD EX1022



Find authenticated court documents without watermarks at docketalarm.com.

Tokyo, Japan

[21] Appl. No.: **471,047**

[22] Filed: Jun. 6, 1995

Related U.S. Application Data

[63] Continuation of Ser. No. 151,248, Nov. 12, 1993.

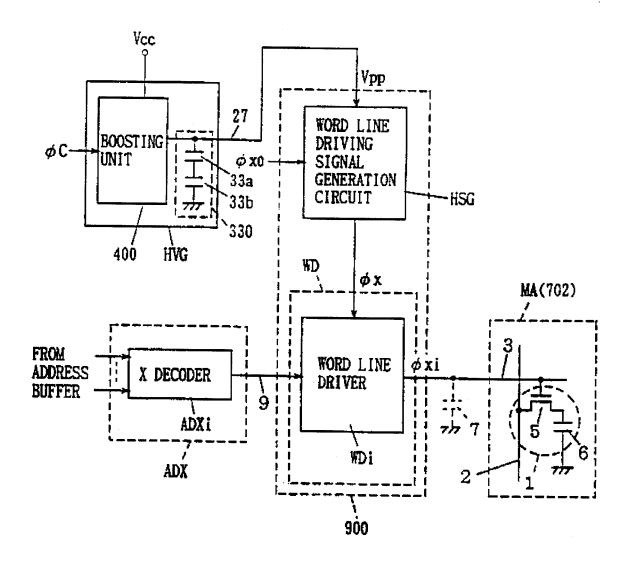
[56] References Cited

U.S. PATENT DOCUMENTS

5,544,102

FIG. 1

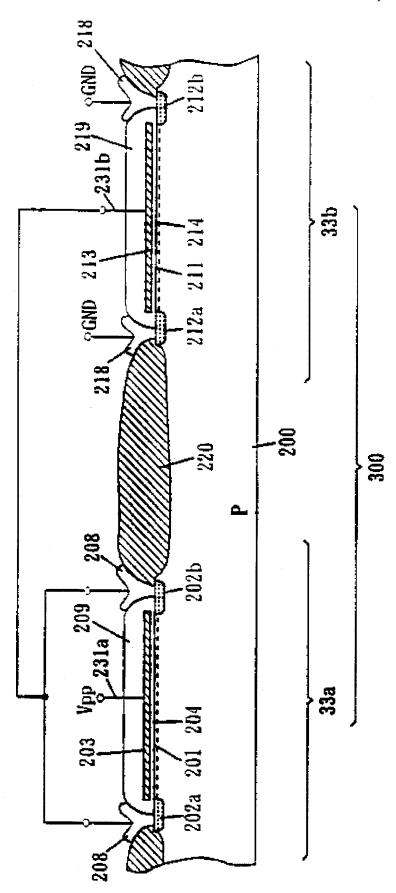
Aug. 6, 1996





Aug. 6, 1996

Ø



5,544,102

FIG. 3 (a) (b) ç Vpp 231a 203 33a <u>33a</u> 208 231ь -33b <u>33b</u> 218

FIG. 4A

FIG. 4B

FIG. 4C

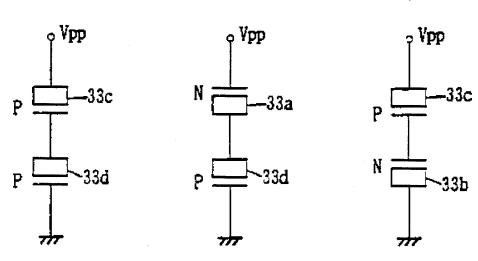
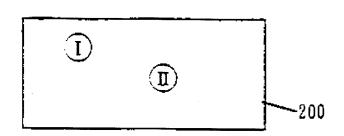


FIG. 5





F I G. 6

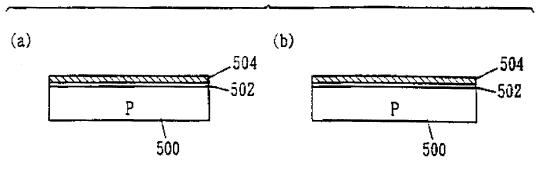


FIG. 7

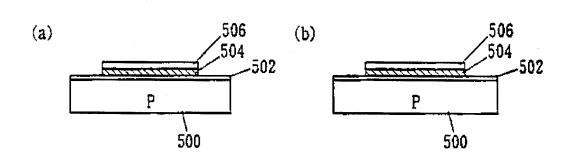


FIG. 8

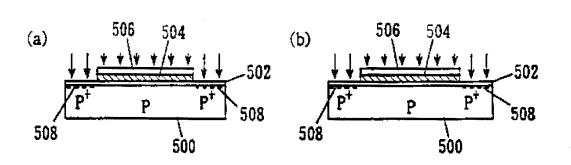
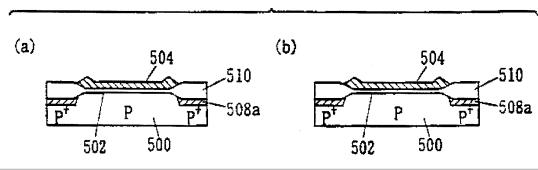


FIG. 9





DOCKET

Explore Litigation Insights



Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

Real-Time Litigation Alerts



Keep your litigation team up-to-date with **real-time** alerts and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

Advanced Docket Research



With over 230 million records, Docket Alarm's cloud-native docket research platform finds what other services can't. Coverage includes Federal, State, plus PTAB, TTAB, ITC and NLRB decisions, all in one place.

Identify arguments that have been successful in the past with full text, pinpoint searching. Link to case law cited within any court document via Fastcase.

Analytics At Your Fingertips



Learn what happened the last time a particular judge, opposing counsel or company faced cases similar to yours.

Advanced out-of-the-box PTAB and TTAB analytics are always at your fingertips.

API

Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

LAW FIRMS

Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

FINANCIAL INSTITUTIONS

Litigation and bankruptcy checks for companies and debtors.

E-DISCOVERY AND LEGAL VENDORS

Sync your system to PACER to automate legal marketing.

