

(12) **United States Patent**  
**Casper et al.**

(10) **Patent No.:** **US 6,687,766 B1**  
(45) **Date of Patent:** **Feb. 3, 2004**

(54) **METHOD AND APPARATUS FOR A FIBRE CHANNEL CONTROL UNIT TO EXECUTE SEARCH COMMANDS LOCALLY**

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(73) Assignee: **International Business Machines Corporation**, Armonk, NY (US)

(\* ) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

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(21) Appl. No.: **09/481,715**

(22) Filed: **Jan. 12, 2000**

**Related U.S. Application Data**

(62) Division of application No. 09/172,695, filed on Oct. 14, 1998, now Pat. No. 6,185,631.

(51) **Int. Cl.<sup>7</sup>** ..... **G06F 13/14**

(52) **U.S. Cl.** ..... **710/20; 710/21; 710/29; 714/720**

(58) **Field of Search** ..... **710/20, 21, 29; 714/720**

(56) **References Cited**

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*Primary Examiner*—Jeffrey Gaffin

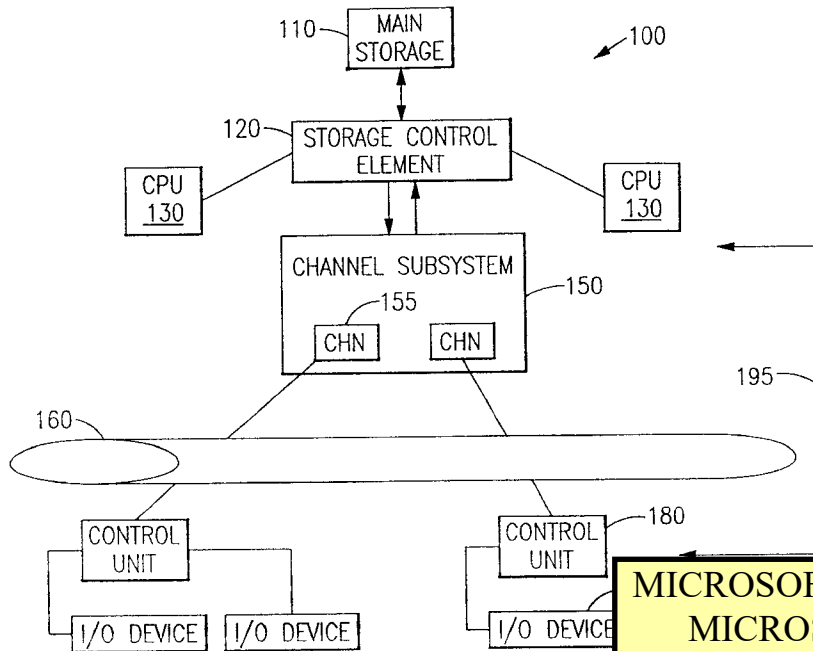
*Assistant Examiner*—Abdelmoniem Elamin

(74) *Attorney, Agent, or Firm*—John E. Campbell; Marc A. Ehrlich; Floyd A. Gonzalez

(57) **ABSTRACT**

The present invention provides a method for fibre channel control units to execute commands locally when a channel sends a repeat execute indicator in conjunction with certain other field settings, wherein the control unit will repeat and chain control words until certain predefined conditions occur.

**24 Claims, 2 Drawing Sheets**



**MICROSOFT - EXHIBIT 1032**  
**MICROSOFT CORP. v.**  
**UNILOC 2017 LLC**  
**IPR2019-01026**

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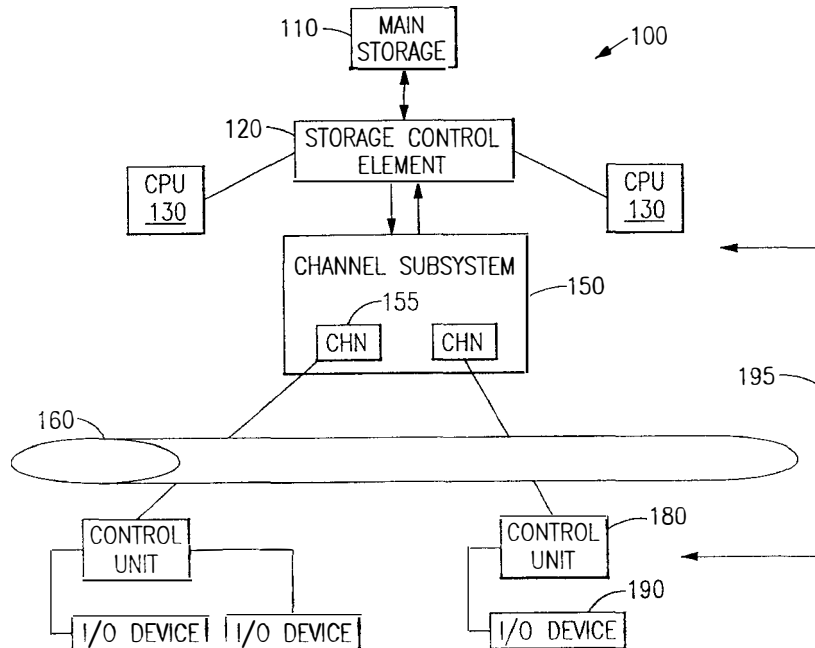
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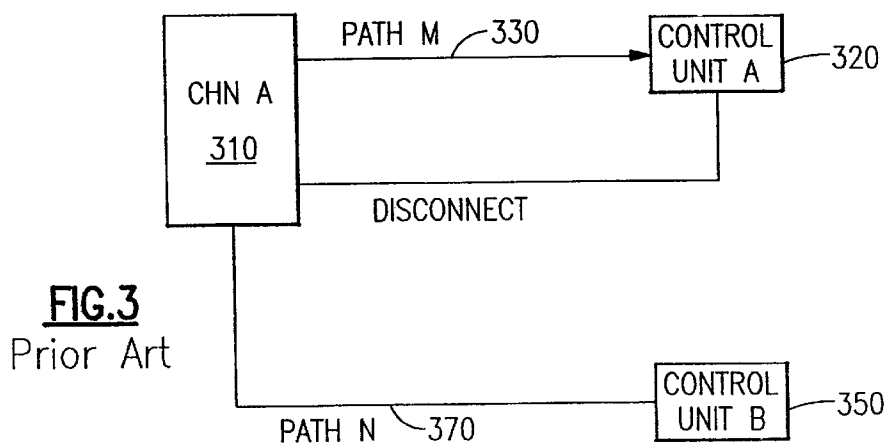
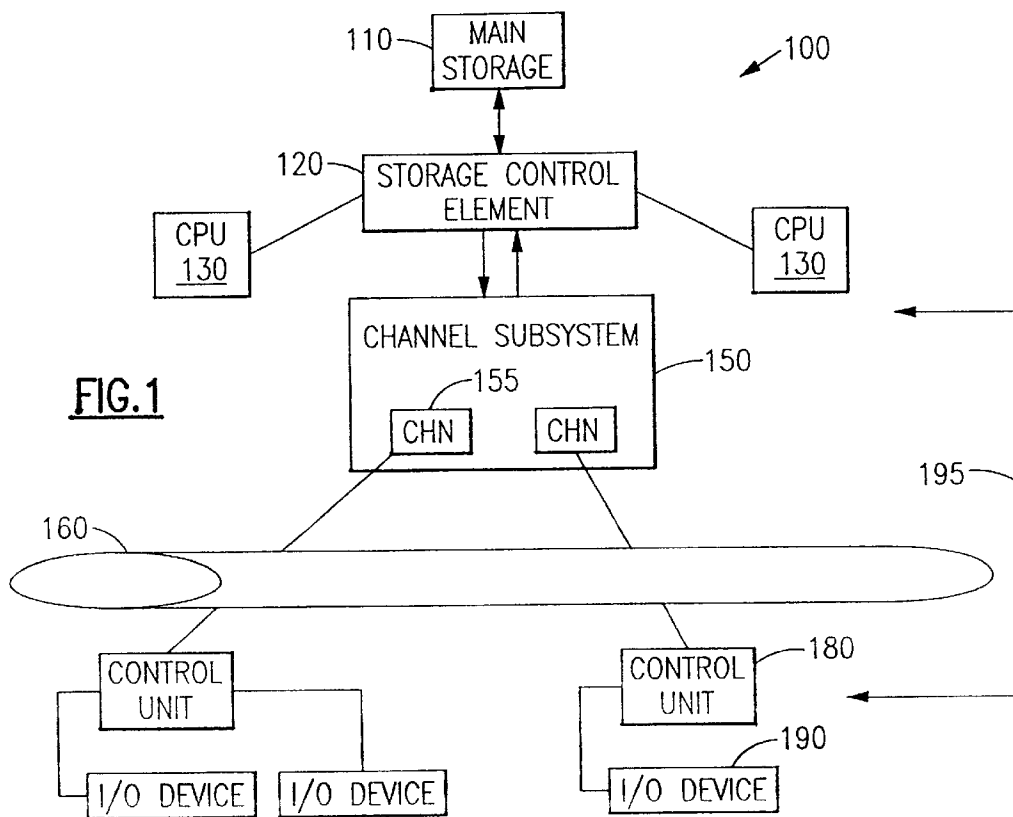
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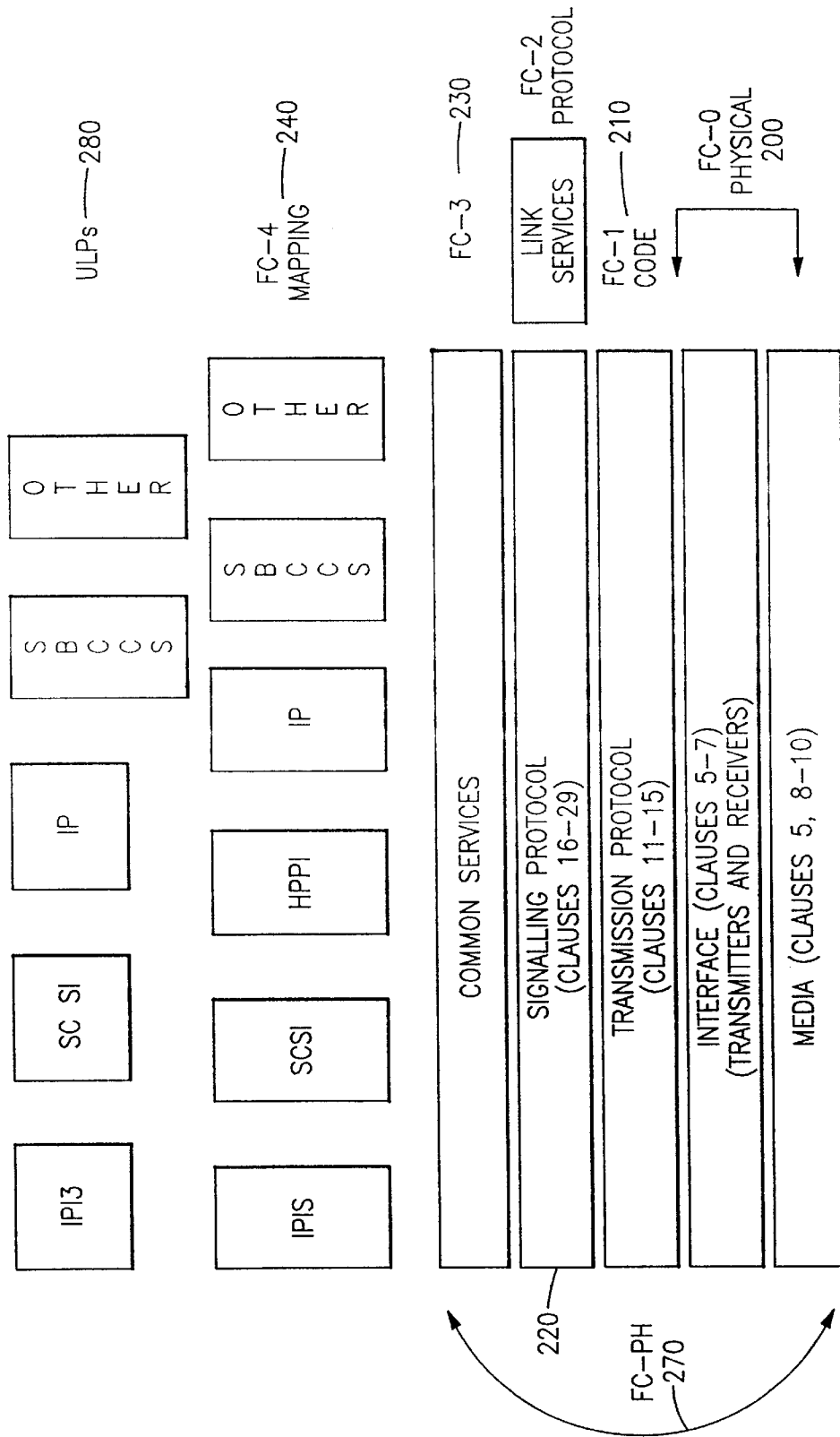


FIG.2

## METHOD AND APPARATUS FOR A FIBRE CHANNEL CONTROL UNIT TO EXECUTE SEARCH COMMANDS LOCALLY

This is a divisional application of Ser. No. 09/172,695 filed on Oct. 14, 1998 now U.S. Pat. No. 6,185,631.

### FIELD OF INVENTION

This invention relates to concept of transferring information in a computer program product for use with a computer system having a main storage device in processing communication with a plurality of input/output devices.

### BACKGROUND OF THE INVENTION

In a network computing environment, multitudes of commands and requests for retrieval and storage of data are processed every second. To properly address the complexity of routing these commands and requests, a number of different resolutions have been implemented. In some data processing architectures, such as International Business Machines Enterprise System Architecture/390 (Enterprise System Architecture/390 is a registered trademark of International Business Machines Corporation), a channel subsystem is utilized to pass information between the main storage and input/output (I/O) devices. The channel subsystem includes one or more channel paths, each including one or more channels and one or more control units. Recently developed technologies such as the International Business Machines ESCON switch (ESCON is a registered trademark of International Business Machines Corporation), connect the I/O devices to the main memory through the control units using legacy channels to support the data transfer there between.

But as the technology improves, the performance of new system processors will require many more legacy channels than are presently in use to support the resulting increase of information transfer in the data processing systems. Current architectural constraints make the addition of such legacy channels an expensive proposition. A further challenge is to provide the link data rate required to support the data rates of new I/O devices such as DASDs and Tapes. In this case, simply adding more legacy channels does not adequately address the problem. A new architecture is needed that can scale up to the higher link speeds needed for normal transaction processing. Therefore, any new proposed architecture, must include a capacity to accommodate higher bandwidth channel links such as Fibre Channel links while providing better data rates and higher link speeds.

This application is being filed at the same time as related application, Ser. Nos. 09/172,488, 09/172,696, and 09/172,462.

This application incorporates by reference the following patents and publications:

- 1) Fibre Channel Single Byte-2(FC-SB-2) Architecture (AR-6865-00-POK)
- 2) Fibre Channel (FC-PH) REV 4.3 ANSI X3.230-199x
- 3) U.S. Pat. No. 5,526,484 to Casper et. al.

### SUMMARY OF THE INVENTION

The present invention provides for a computer program product for use with a computer system having a main storage device in processing communication with an information transfer interface mechanism capable of coupling to a plurality of input/output devices. The computer program device comprises of a data storage element included in the

main storage device having a computer usable medium with computer readable program means for receiving and retrieving data and computer readable code means for concurrently receiving multiple packets of data from said interface mechanism. It also includes computer readable code means for concurrently storing multiple packets of data concurrently in said data storage element as well as computer readable code means for storage and retrieval of multiple packets of data concurrently between said interface mechanism and said data storage element. In one embodiment of the present invention the interface mechanism can include a control unit and channels and in yet another embodiment a fabric is in processing communication with the control unit and the channels. The computer product can also comprise computer readable code for transferring information using time division multiplexing. It can also comprise computer readable code for interleaving multiple starts each to a separate device on one channel by using multiplexing capability.

### BRIEF DESCRIPTION OF THE DRAWINGS

The subject matter which is regarded as the invention is particularly pointed out and distinctly claimed in the concluding portion of the specification. The invention, however, both as to organization and method of practice, together with further objects and advantages thereof, may best be understood by reference to the following description taken in connection with the accompanying drawings in which:

FIG. 1 is an illustration a network computing environment utilizing one embodiment of the present invention;

FIG. 2 is an illustration of different Fibre Channel hierarchies; and

FIG. 3 is an illustration of prior art systems demonstrating communication paths.

### DETAILED DESCRIPTION OF THE INVENTION

In a network computing system environment **100** having a data processing system architecture such as the one depicted in FIG. 1, information is passed between a program storage device or a main storage such as the one shown at **110**, and one or more input/output devices (hereinafter I/O devices) **190**, using channel subsystems **150**. Through a fabric that can include one or more optical fiber and switches **160**, channel paths are established, comprising channels **155** and one or more control units shown at **180**. These channel paths are the communication links established between the I/O devices **190** and the main storage for processing and exchange of information. The channel subsystem, control units and the fiber are all part of an information transfer interface mechanism **195** which enables the main storage remain in processing communication with the input/output devices. The information transfer interface mechanism can include other or different components. The fabric can be simply replaced by one or more switches. In one embodiment of the present invention, the interface mechanism does not even include the fabric or any switches.

The main storage **110** stores data and programs which are input from I/O devices **190**. Main storage is directly addressable and provides for high speed processing of data by central processing units and one or more I/O channel subsystem devices. One example of a main storage is a customer's storage area and a system area (not shown). I/O devices **190** receive information or store information in main storage. Some examples of I/O devices include magnetic-tape units, direct-access storage devices (DASD), displays,

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