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Akagiri et al.

[11]

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[54]	TWO-STAGE COMPRESSION AND
	EXPANSION OF COUPLING PROCESSED
	MULTI-CHANNEL SOUND SIGNALS FOR
	TRANSMISSION AND RECORDING

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Calif.

[73] Assignee: Sony Corporation, Tokyo, Japan

[21] Appl. No.: 446,689

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[86] PCT No.: PCT/US94/14267

> § 371 Date: Nov. 20, 1995 § 102(e) Date: Nov. 20, 1995

[30] Foreign Application Priority Data

Dec. 7, 1993		[JP]	Japan	5-306898
[51]	Int. Cl. ⁶			H04S 3/00 ; H04B 1/66;
[52]	U.S. Cl.			H04H 5/00 704/500 ; 381/2; 704/227;

704/229 395/2.39; 381/2; 704/227, 229, 230, 500

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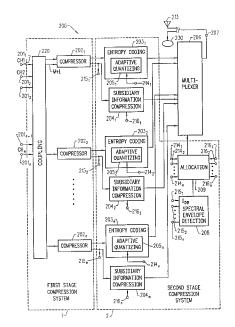
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Primary Examiner—David R. Hudspeth Assistant Examiner—Tálivaldis Ivars Ŝmits Attorney, Agent, or Firm-Limbach & Limbach LLP

[57] **ABSTRACT**

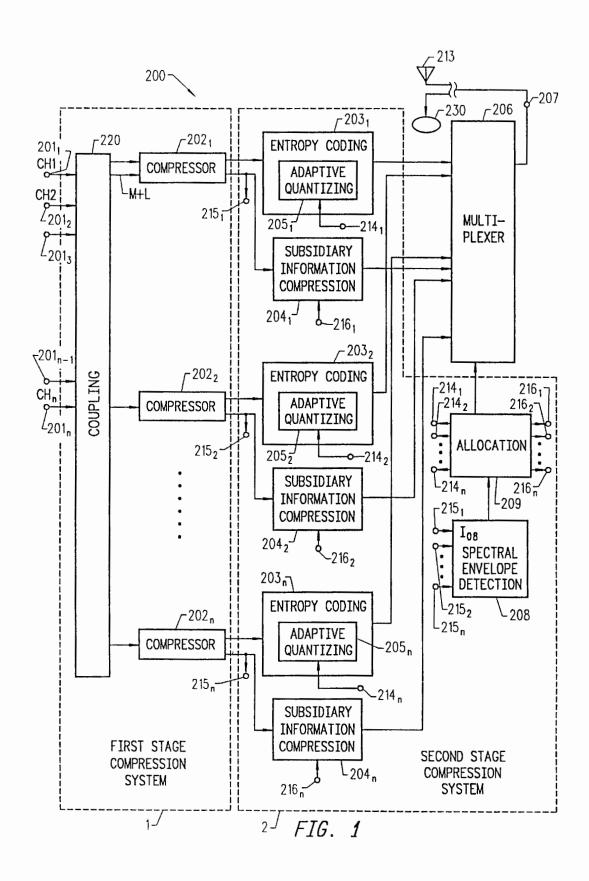
A multi-channel signal compressor for compressing digital sound signals in the respective channels of a multi-channel sound system. The apparatus comprises a first-stage compression system and a second-stage compression system. In the first-stage compression system, a coupling circuit performs coupling between the digital sound signals of at least two of the channels to generate coupling-processed signals, one for each of the channels. A compressor circuit receives the coupling-processed signals from the coupling circuit and frequency divides each coupling-processed signal into frequency range signals in respective frequency ranges, and compresses the frequency range signals obtained by dividing each coupling-processed signal to generate a first-stage compressed signal. In the second-stage compression system, a determining circuit receives the first-stage compressed signal for each channel from the first-stage compression system and determines an energy for each channel from the first-stage compressed signal of the respective channel. A channel bit apportionment decision circuit operates in response to the determining circuit, and apportions a predetermined number of bits among the channels to apportion a number of bits to each channel. Finally, an additional compressor additionally compresses the first-stage compressed signal of each channel using, for each channel, the number of bits apportioned to the respective channel by the channel bit apportionment decision circuit.

16 Claims, 10 Drawing Sheets

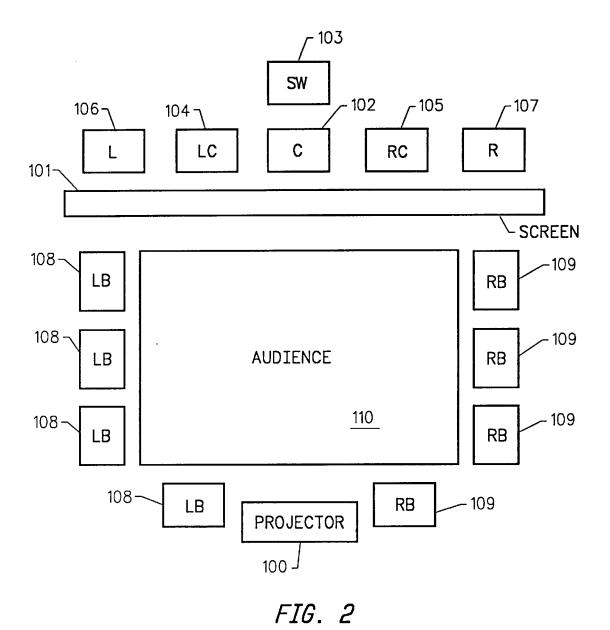




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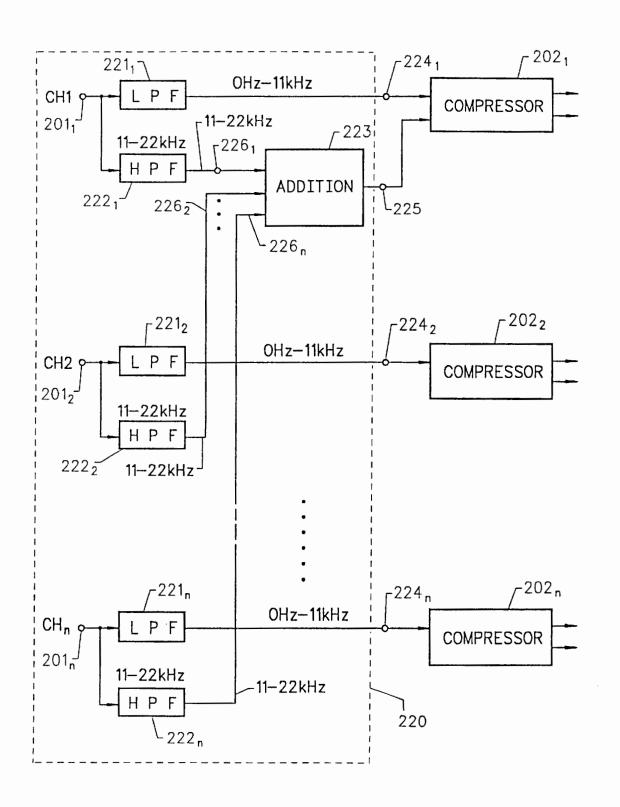
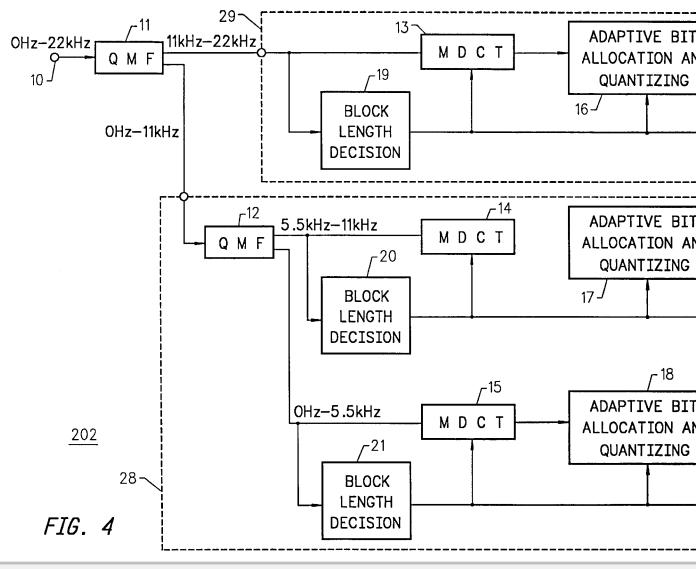


FIG. 3







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