

- [54] LOW POWER OSCILLATOR CIRCUIT
- [75] Inventor: Jerald G. Leach, Houston, Tex.
- [73] Assignee: Texas Instruments Incorporated, Dallas, Tex.
- [21] Appl. No.: 334,487
- [22] Filed: Dec. 24, 1981
- [51] Int. Cl.⁴ H03B 5/12
- [52] U.S. Cl. 331/108 B; 331/57; 331/DIG. 3; 307/448; 307/481
- [58] Field of Search 331/45, 50, 55, 56, 331/57, 104, 108 A, 108 B, 108 C, 111, 113 R, 143, 144, DIG. 3; 307/445, 448, 480, 481, 482

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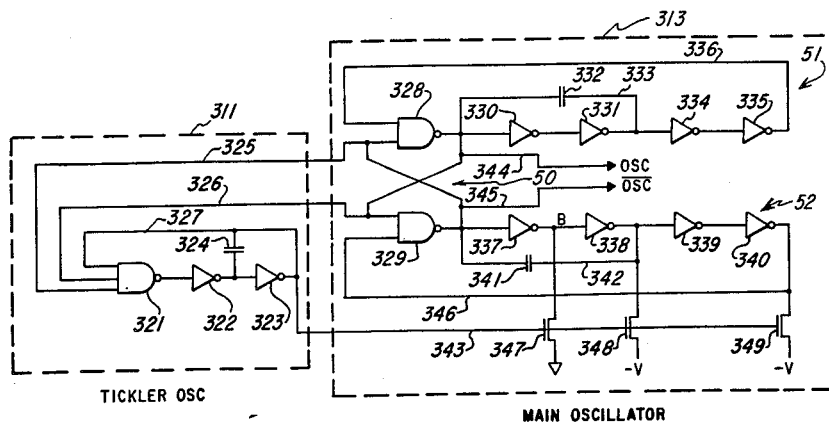
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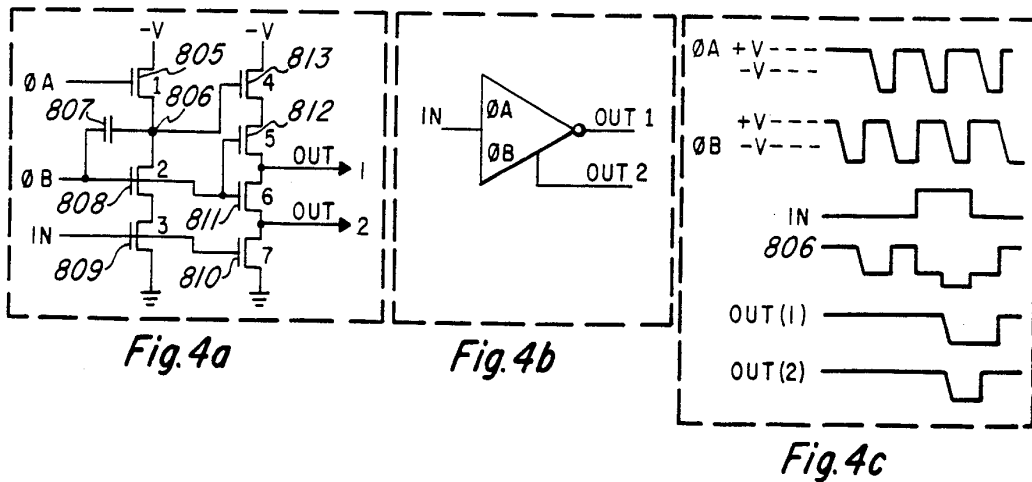
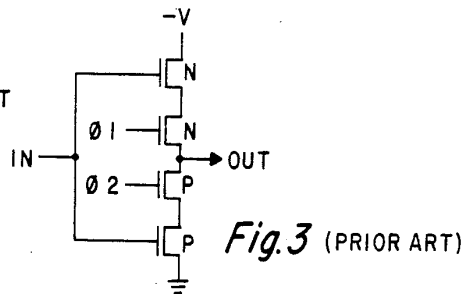
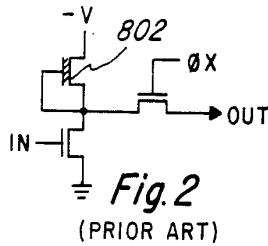
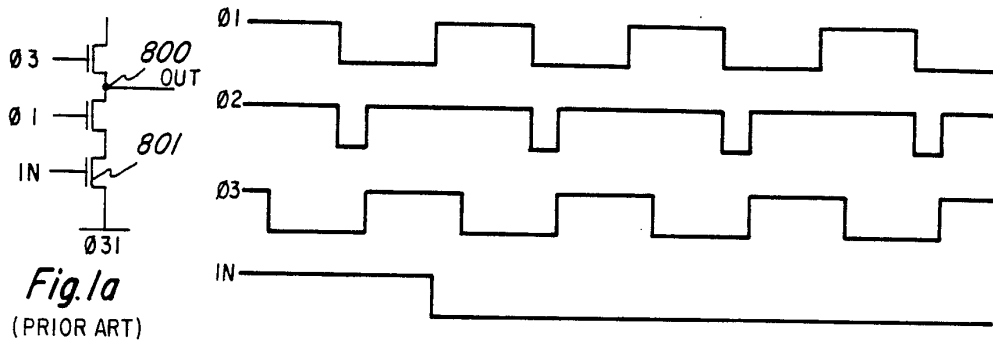
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 Assistant Examiner—Robert J. Pascal
 Attorney, Agent, or Firm—Thomas E. Tyson; Leo N. Heiting; Melvin Sharp

[57] ABSTRACT

A low power oscillator circuit including a latch connected to two loops. Each loop includes dynamic inverters and static inverters connected in cascade. The loops are connected to the latch such that the output of the final stage is an input to the latch. An initialization circuit is included on one loop to initiate oscillation. Storage capacitors are included in the loops to provide an oscillator output voltage that is greater than the oscillator power supply voltage.

2 Claims, 14 Drawing Figures





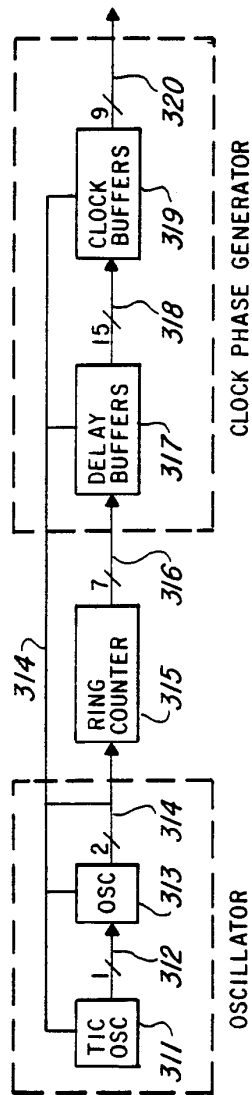


Fig. 5

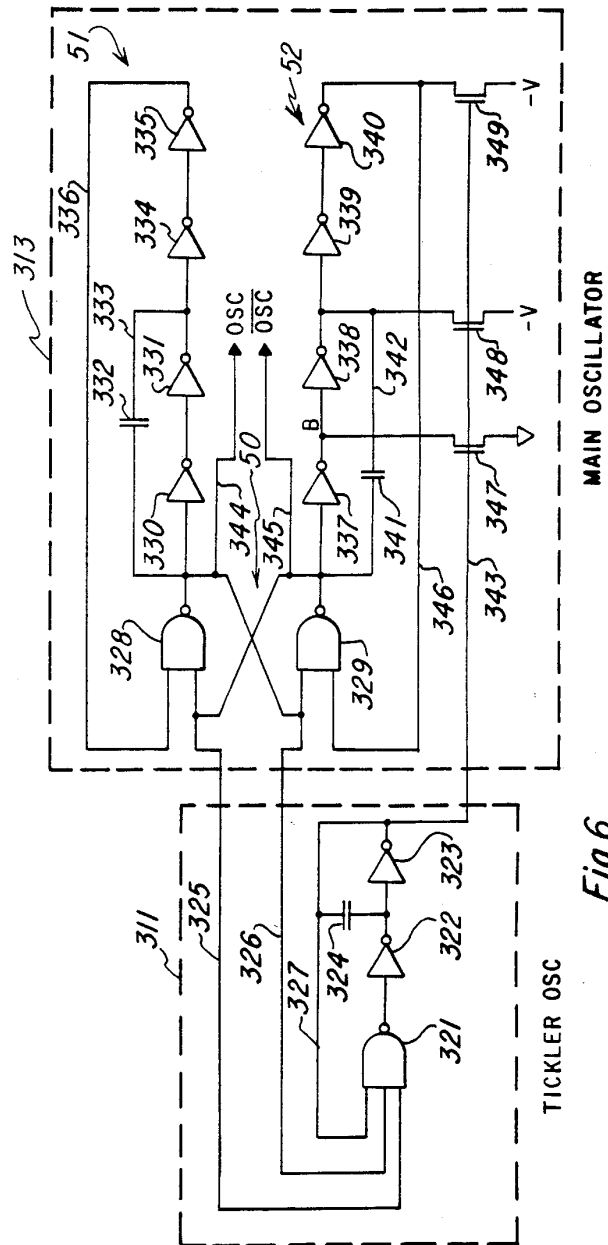


Fig. 6

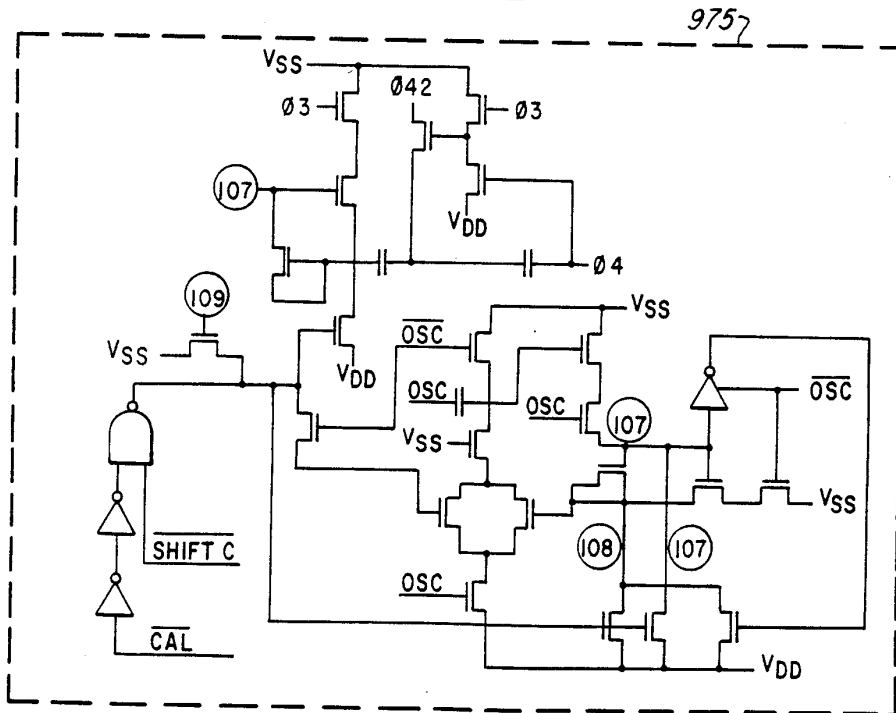
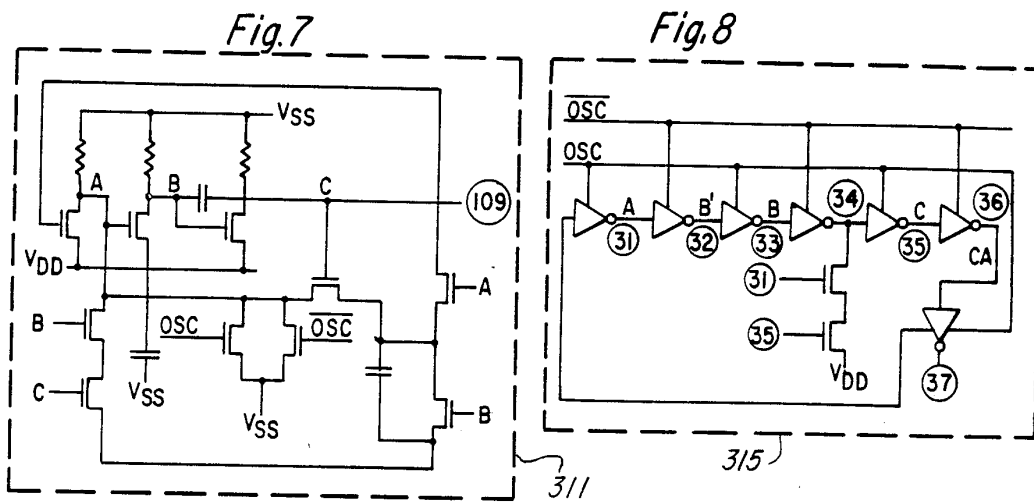


Fig. 9

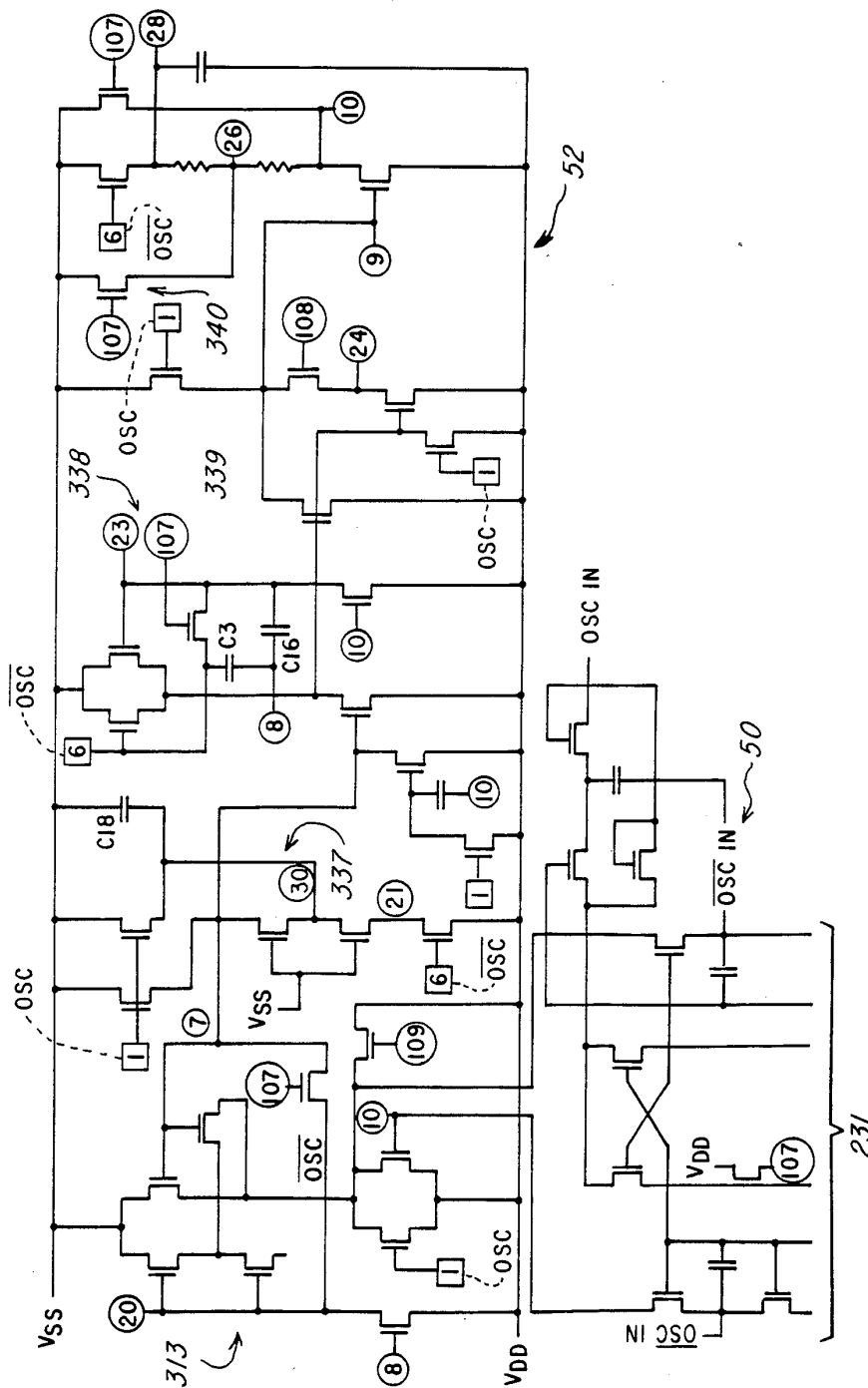


Fig. 10a

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