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QUALCOMM INCORPORATED 5775 MOREHOUSE DR. SAN DIEGO, CA 92121			TRAN, KHANH C	
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### DETAILED ACTION

1. The present application is being examined under the pre-AIA first to invent provisions.

2. The RCE filed 7/17/2014 has been entered. Claims 1-20 are still pending in this Office action.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of pre-AIA 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1, 11-12, 14 and 17 are rejected under pre-AIA 35 U.S.C. 102(e) as being anticipated by Kaukovuori et al. U.S. Patent 8,442,473.

Regarding claim 1, Kaukovuori et al. discloses an apparatus (FIG. 15 embodiment) comprising:

a first amplifier stage configured to receive and amplify an input radio frequency (RF) signal and provide a first output RF signal to a first load circuit when the first amplifier stage is enabled, the input RF signal employing carrier aggregation comprising transmissions sent on multiple carriers at different frequencies to a wireless device, the

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first output RF signal including at least a first carrier of the multiple carriers (Kaukovuori et al. teaches **a method of receiving data** transmitted via a combination of at least a plurality of radio frequency signals **using carrier aggregation** (see column 2 lines 44-49). FIG. 15 discloses a Radio Frequency Integrated Circuit (RFIC1) 1 including first amplifier stage LNA to provide a first output RF signal to a digital data path. The two clusters are each received with different bandwidth filter (see column 10, lines 22-53).

and a second amplifier stage configured to receive and amplify the input RF signal and provide a second output RF signal to a second load circuit when the second amplifier stage is enabled, the second output RF signal including at least a second carrier of the multiple carriers different than the first carrier (similarly, FIG. 15 further discloses a Radio Frequency Integrated Circuit (RFIC1) 1 including second amplifier stage LNA to provide a second output RF signal to a digital data path. The two clusters are each received with different bandwidth filter (see column 10, lines 22-53)).

Regarding claim 11, Kaukovuori et al. further discloses an input matching circuit coupled to the first and second amplifier stages and configured to receive a receiver input signal and provide the input RF signal (FIG. 15 discloses an RF FEM coupled to the RFIC1 and RFIC2 and configured to provide an RF input (see column 10 lines 25-35).

Regarding claim 12, Kaukovuori et al. further discloses the input matching circuit being tunable and comprising at least one adjustable circuit component (FIG. 15

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discloses an RF FEM configured to split the RF input signal (see column 10 lines 25-35).

Regarding claim 14, Kaukovuori et al. further discloses the first amplifier stage configured to receive and amplify the input RF signal and provide the first output RF signal to the first load circuit when the first amplifier stage is enabled (as recited in claim 1 rejection, FIG. 15 discloses a Radio Frequency Integrated Circuit (RFIC1) 1 including first amplifier stage LNA to provide a first output RF signal to a digital data path. The two clusters are each received with different bandwidth filter (see column 10, lines 22-53)).

and the second amplifier stage configured to receive and amplify the input RF signal and provide the second output RF signal to the second load circuit when the second amplifier stage is enabled (similarly, FIG. 15 further discloses a Radio Frequency Integrated Circuit (RFIC1) 1 including second amplifier stage LNA to provide a second output RF signal to a digital data path. The two clusters are each received with different bandwidth filter (see column 10, lines 22-53))

**Note:** the rejection is based on *the input RF signal (not a second input RF signal)*.

Regarding claim 17, claim is rejected on the same ground as for claim 1 because of similar scope.

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