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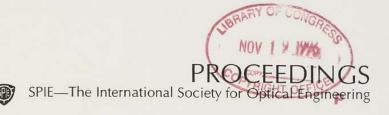
High-Speed Computing, Digital Signal Processing, and Filtering Using Reconfigurable Logic

John Schewel Peter M. Athanas V. Michael Bove, Jr. John Watson Chairs/Editors

20-21 November 1996 Boston, Massachusetts







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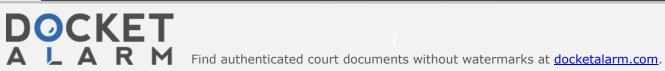
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