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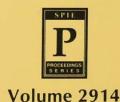


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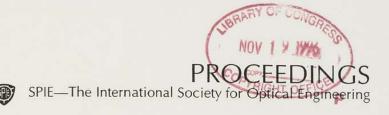
High-Speed Computing, Digital Signal Processing, and Filtering Using Reconfigurable Logic

John Schewel Peter M. Athanas V. Michael Bove, Jr. John Watson Chairs/Editors

20-21 November 1996 Boston, Massachusetts







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Sponsored and Published by SPIE—The International Society for Optical Engineering



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Please use the following format to cite material from this book:
Author(s), "Title of paper," in High-Speed Computing, Digital Signal Processing, and Filtering
Using Reconfigurable Logic, John Schewel, Peter M. Athanas, V. Michael Bove, Jr., John
Watson, Editors, Proc. SPIE 2914, page numbers (1996).

Library of Congress Catalog Card No. 96-69766
ISBN 0-8194-2316-5

Published by SPIE—The International Society for Optical Engineering P.O. Box 10, Bellingham, Washington 98227-0010 USA Telephone 360/676-3290 (Pacific Time) • Fax 360/647-1445

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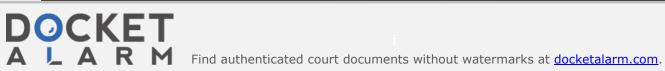
Printed in the United States of America.



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