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I, Rachel J. Watters, am a librarian, and the Director of Wisconsin TechSearch (“WTS”), located at 728 State Street, Madison, Wisconsin, 53706. WTS is an interlibrary loan department at the University of Wisconsin-Madison. I have worked as a librarian at the University of Wisconsin library system since 1998. I have been employed at WTS since 2002, first as a librarian and, beginning in 2011, as the Director. Through the course of my employment, I have become well informed about the operations of the University of Wisconsin library system, which follows standard library practices.

This Declaration relates to the dates of receipt and availability of the following:

**Lange, H., & Koch, A. (2000). Memory access schemes for configurable processors. In *International Workshop on Field Programmable Logic and Applications* (pp. 615-625). Springer : Berlin, Heidelberg.**

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Exhibit A to this Declaration is true and accurate copy of the front matter of the *International Workshop on Field Programmable Logic and Applications*

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(2000) publication, which includes a stamp on the verso page showing that this book is the property of the Kurt F. Wendt Library at the University of Wisconsin-Madison.

Attached as Exhibit B is the cataloging system record of the University of Wisconsin-Madison Libraries for its copy of the *International Workshop on Field Programmable Logic and Applications* (2000) publication. As shown in the “Receiving date” field of this Exhibit, the University of Wisconsin-Madison Libraries owned this book and had it cataloged in the system as of November 7, 2000.

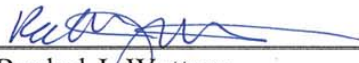
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I declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code.

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Date: October 2, 2018

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\_\_\_\_\_  
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# Field-Programmable Logic and Applications

The Roadmap to Reconfigurable Computing

10th International Conference, FPL 2000  
Villach, Austria, August 27-30, 2000  
Proceedings



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Reiner W. Hartenstein  
University of Kaiserslautern, Computer Science Department  
P. O. Box. 30 49, 67653 Kaiserslautern, Germany  
E-mail: hartenst@rhrk.uni-kl.de

Herbert Grünbacher  
Carinthia Tech Institute  
Richard-Wagner-Str. 19, 9500 Villach, Austria  
E-mail: hg@cti.ac.at

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