Intel Corporation v. Qualcomm Incorporated

IPR2019-00047

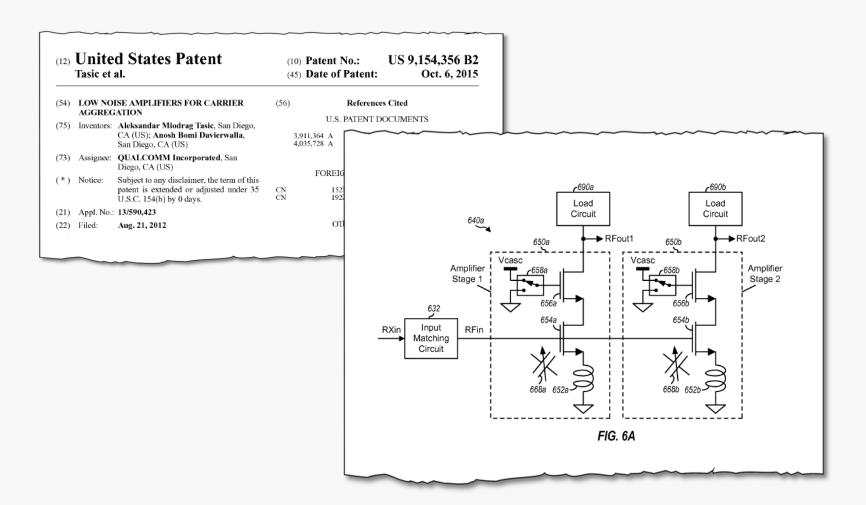
IPR2019-00048

IPR2019-00049

U.S. Patent No. 9,154,356

Patent Owner's Demonstrative Exhibits

U.S. Patent No. 9,154,356



Instituted Grounds

IPR2019-00047

Ground I: Claims 1, 11, 17 and 18 as Anticipated by Uehara

Ground II: Claims 7 and 8 as Obvious over Uehara and Perumana

Ground III: Claim 10 as Obvious over Uehara and Youssef

Ground IV: Claims 1, 11, 17 and 18 as Obvious over Uehara and Feasibility Study

Ground V: Claims 7 and 8 as Obvious over Uehara, Feasibility Study, and Permuna

Ground VI: Claim 10 as Obvious over Uehara, Feasibility Study, and Youssef

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Ground I: Claims 1, 17, and 18 as Obvious over Jeon and Xiong

Ground II: Claims 9 and 10 as Obvious over Jeon, Xiong, and Youssef

Ground III: Claims 1, 17, and 18 as Obvious over Jeon, Xiong, and Feasibility Study

Ground IV: Claims 9 and 10 as Obvious over Jeon, Xiong, Youssef, and Feasibility Study

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Ground I: Claims 2–8, and 11 as Obvious over Jeon and Xiong

Ground II: Claims 2–8, and 11 as Obvious over Jeon, Xiong, and Feasibility Study

Patentability

- 1) "Independently Enabled or Disabled" Amplifier Stages
 - a) Uehara Fails to Anticipate
 - b) Jeon and Xiong Fail to Render Obvious
- 2) The Petition Relies on an Overly Broad Interpretation of "Carrier Aggregation"
- 3) Petitioner Fails to Establish that Uehara and Jeon/Xiong Disclose "Carrier Aggregation"
- 4) Petitioner Fails to Sufficiently Articulate a Motivation to Combine Uehara or Jeon and Xiong with the Feasibility Study



Patentability

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