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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
13/590,423	08/21/2012	Aleksandar Modrag Tasic	121973	9482
23696	7590	04/18/2014	EXAMINER	
QUALCOMM INCORPORATED 5775 MOREHOUSE DR. SAN DIEGO, CA 92121			TRAN, KHANH C	
			ART UNIT	PAPER NUMBER
			2631	
			NOTIFICATION DATE	DELIVERY MODE
			04/18/2014	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

us-docketing@qualcomm.com

Office Action Summary	Application No. 13/590,423	Applicant(s) TASIC ET AL.	
	Examiner KHANH C. TRAN	Art Unit 2631	AIA (First Inventor to File) Status No

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTHS FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 2/6/2014.
 A declaration(s)/affidavit(s) under **37 CFR 1.130(b)** was/were filed on _____.
- 2a) This action is **FINAL**. 2b) This action is non-final.
- 3) An election was made by the applicant in response to a restriction requirement set forth during the interview on _____; the restriction requirement and election have been incorporated into this action.
- 4) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims*

- 5) Claim(s) 1-20 is/are pending in the application.
5a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 6) Claim(s) _____ is/are allowed.
- 7) Claim(s) 1,11,12,17 and 19 is/are rejected.
- 8) Claim(s) 2-10,13-16,18 and 20 is/are objected to.
- 9) Claim(s) _____ are subject to restriction and/or election requirement.

* If any claims have been determined allowable, you may be eligible to benefit from the **Patent Prosecution Highway** program at a participating intellectual property office for the corresponding application. For more information, please see http://www.uspto.gov/patents/init_events/pph/index.jsp or send an inquiry to PPHfeedback@uspto.gov.

Application Papers

- 10) The specification is objected to by the Examiner.
- 11) The drawing(s) filed on 8/21/2012 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

Certified copies:

- a) All b) Some** c) None of the:
1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

** See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Information Disclosure Statement(s) (PTO/SB/08a and/or PTO/SB/08b)
- 3) Interview Summary (PTO-413) _____
Paper No(s)/Mail Date. _____

DETAILED ACTION

1. The present application is being examined under the pre-AIA first to invent provisions.

2. The Amendment filed on 2/6/2014 has been entered. Claims 1-20 are still pending in this Office action.

Response to Arguments

3. Applicant's arguments, see Applicants' Remarks, filed 2/6/2014, with respect to the rejection(s) of claim(s) 1, 7, 17 and 19 under 35 U.S.C. 103(a) have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Hirose U.S. Patent 7,317,894 B2.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of pre-AIA 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1, 11-12 and 17 are rejected under pre-AIA 35 U.S.C. 102(b) as being anticipated by Hirose U.S. Patent 7,317,894 B2.

Art Unit: 2631

Regarding claim 1, Hirose discloses an apparatus (FIG. 6 digital broadcast receiver) comprising:

a first amplifier stage configured to receive and amplify an input radio frequency (RF) signal and provide a first output RF signal to a first load circuit when the first amplifier stage is enabled (variable gain amplifier 15, corresponding to the claimed first amplifier stage, receiving input RF signal (e.g. satellite wave signal and ground wave signal) and providing an output to intermediate frequency demodulation stage (for ground wave), which corresponds to the claimed first load circuit (see column 5 lines 1-30 and FIG. 6) ;

the input RF signal comprising transmissions sent on multiple carriers at different frequencies to a wireless device, the first output RF signal including at least a first carrier of the multiple carriers (as recited above, the input RF signal comprising satellite wave signal and ground wave signal (column 5 lines 1-4) and the output to intermediate frequency demodulation stage for ground wave, corresponding to the claimed first carrier of the multiple carriers);

and a second amplifier stage configured to receive and amplify the input RF signal and provide a second output RF signal to a second load circuit when the second amplifier stage is enabled (variable gain amplifier 25, corresponding to the claimed second amplifier stage, receiving input RF signal (e.g. satellite wave signal and ground wave signal) and providing an output to intermediate frequency demodulation stage (for satellite wave), which corresponds to the claimed second load circuit (see column 5 lines 1-30 and FIG. 6), the second output RF signal including at least a second carrier of

Art Unit: 2631

the multiple carriers different than the first carrier (as recited above, the input RF signal comprising satellite wave signal and ground wave signal (column 5 lines 1-4) and the output to intermediate frequency demodulation stage for satellite wave, corresponding to the claimed second carrier of the multiple carriers).

Note: the rejection is based on the assumption that both first and second amplifier stages are enabled.

Regarding claim 11, Hirose further discloses an input matching circuit coupled to the first and second amplifier stages and configured to receive a receiver input signal and provide the input RF signal (FIG. 6 discloses a combination of a voltage control type variable gain amplifier 33, a wave detection 35 and a control circuit 36 (see column 5 lines 5-20). The combination constitutes the input matching circuit as claimed).

Regarding claim 12, Hirose further discloses the input matching circuit being tunable and comprising at least one adjustable circuit component (the combination of a voltage control type variable gain amplifier 33, a wave detection 35 and a control circuit 36 is tunable and the voltage control type variable gain amplifier 33 is adjustable).

Regarding claim 17, claim is rejected on the same ground as for claim 1 because of similar scope.

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