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Hirose

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(54) **SATELLITE DIGITAL RADIO BROADCAST RECEIVER**

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(30) **Foreign Application Priority Data**

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(51) **Int. Cl.**
H04H 1/00 (2006.01)

(52) **U.S. Cl.** **455/3.02**; 455/234.1; 455/250.1

(58) **Field of Classification Search** 455/3.01-3.02, 455/234.1, 232.1, 272, 277.1, 277.2, 226.1, 455/226.2, 67.11, 67.12, 133, 136; 725/68-71, 725/63-64

See application file for complete search history.

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(57) **ABSTRACT**

A satellite digital radio broadcast receiver has an integrated circuit including a first reception series for processing a satellite wave signal from a satellite and a second reception series for processing a ground wave signal from a repeater in order to receive both the satellite wave signal and the ground wave signal having the same broadcast contents and different modulation methods. The receiver has an automatic gain control unit for amplifying a signal from a single antenna at a variable gain amplifier, and in accordance with the level of a signal outputted from the variable gain amplifier, for controlling the gain of the variable gain amplifier; and a two-way distributor for distributing an output of the automatic control unit to two distribution outputs, wherein one distribution output of the two-way distributor is supplied to the first reception series of the integrated circuit and the other distribution output is supplied to the second reception series of the integrated circuit.

2 Claims, 8 Drawing Sheets

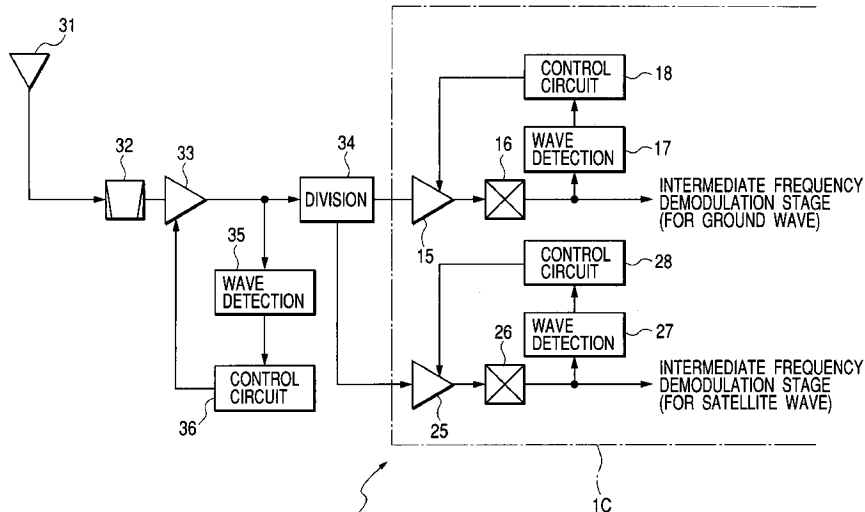


FIG. 1

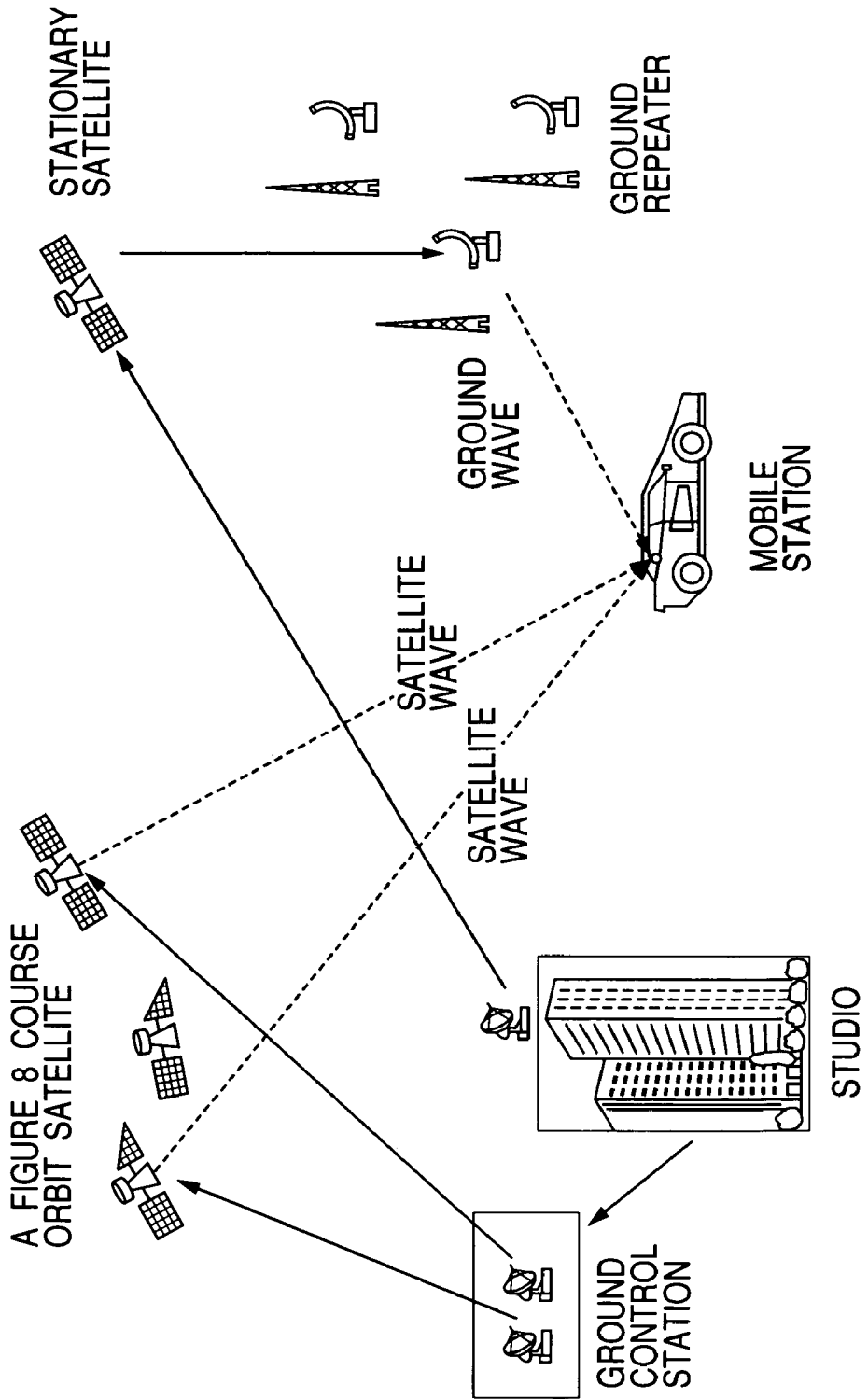


FIG. 2

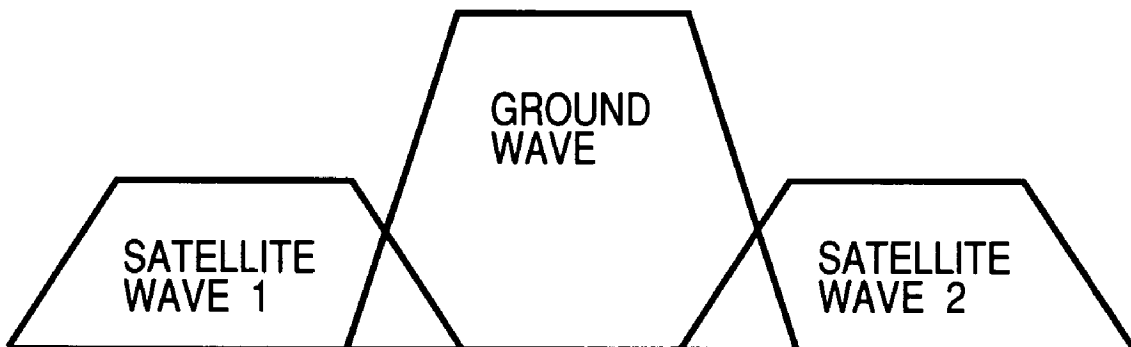


FIG. 3
PRIOR ART

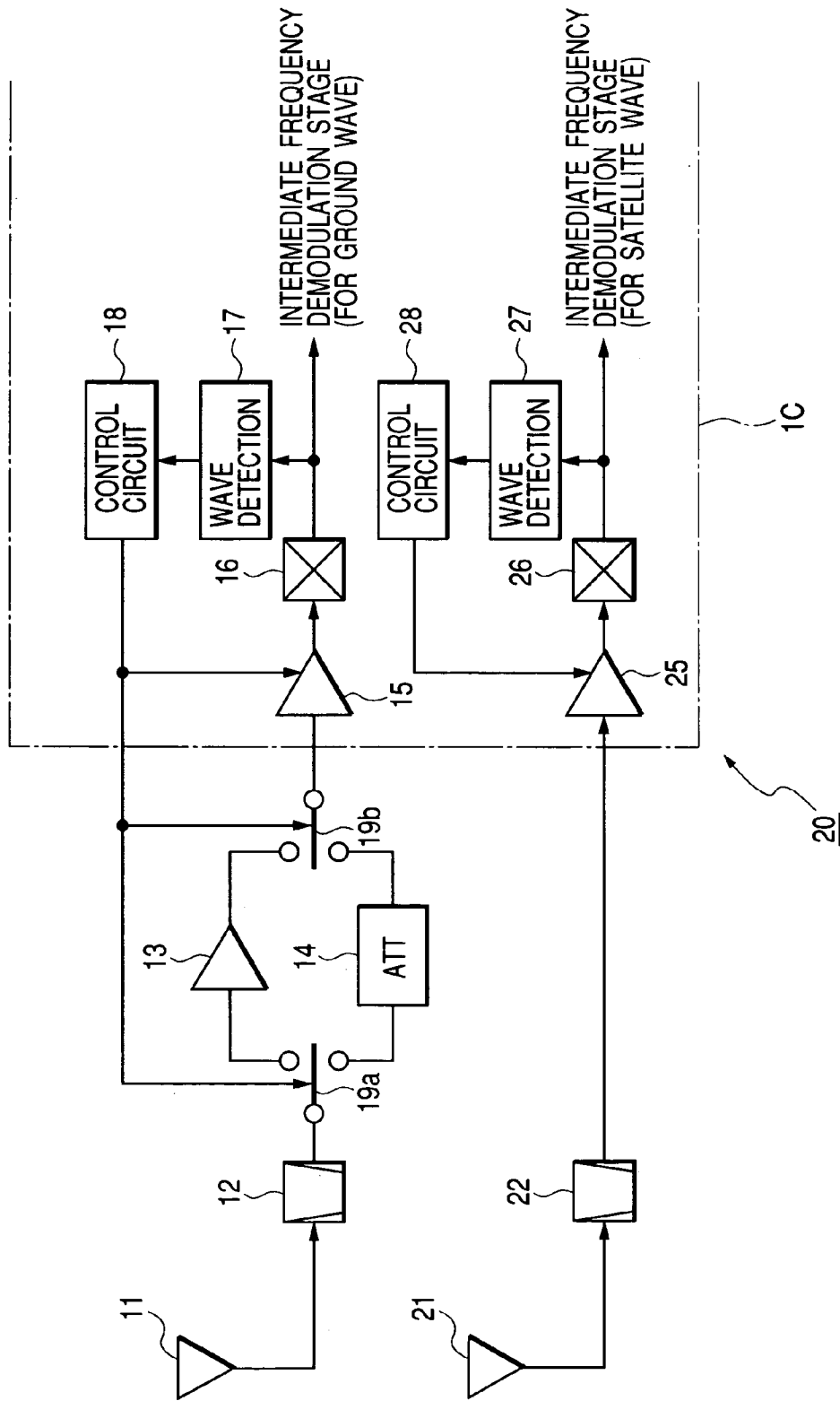
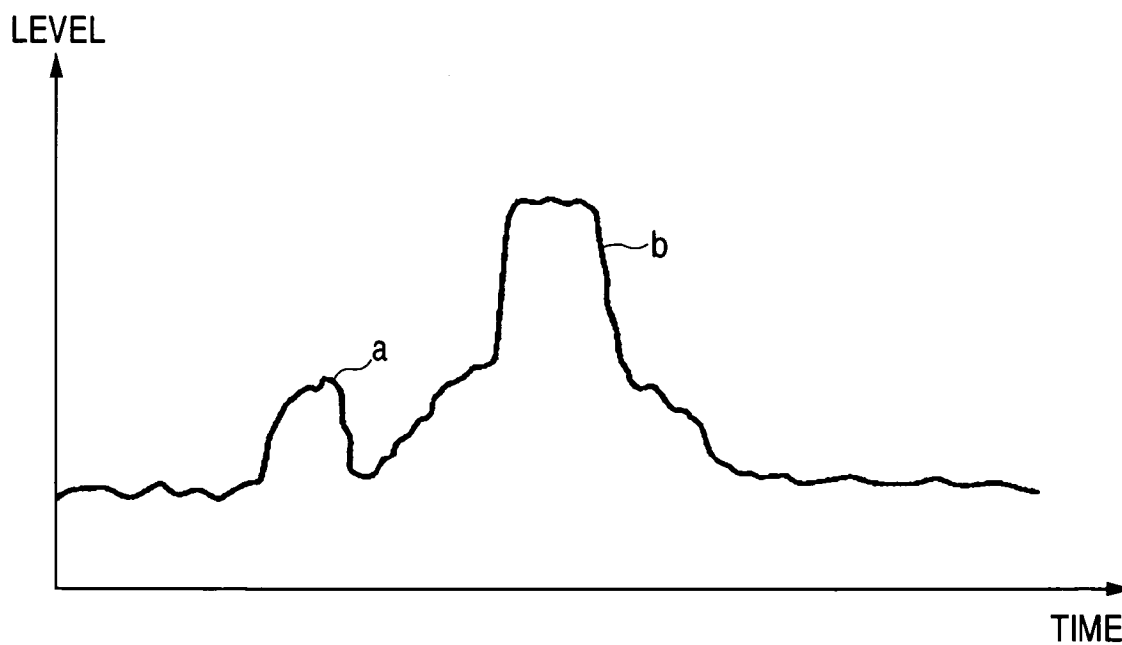


FIG. 4



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