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23696	7590	04/18/2014	EXAMINER	
QUALCOMM INCORPORATED 5775 MOREHOUSE DR. SAN DIEGO, CA 92121			TRAN, KHANH C	
			ART UNIT	PAPER NUMBER
			2631	
			NOTIFICATION DATE	DELIVERY MODE
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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DETAILED ACTION

1. The present application is being examined under the pre-AIA first to invent provisions.

2. The Amendment filed on 2/6/2014 has been entered. Claims 1-20 are still pending in this Office action.

Response to Arguments

3. Applicant's arguments, see Applicants' Remarks, filed 2/6/2014, with respect to the rejection(s) of claim(s) 1, 7, 17 and 19 under 35 U.S.C. 103(a) have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Hirose U.S. Patent 7,317,894 B2.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of pre-AIA 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1, 11-12 and 17 are rejected under pre-AIA 35 U.S.C. 102(b) as being anticipated by Hirose U.S. Patent 7,317,894 B2.

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Regarding claim 1, Hirose discloses an apparatus (FIG. 6 digital broadcast receiver) comprising:

a first amplifier stage configured to receive and amplify an input radio frequency (RF) signal and provide a first output RF signal to a first load circuit when the first amplifier stage is enabled (variable gain amplifier 15, corresponding to the claimed first amplifier stage, receiving input RF signal (e.g. satellite wave signal and ground wave signal) and providing an output to intermediate frequency demodulation stage (for ground wave), which corresponds to the claimed first load circuit (see column 5 lines 1-30 and FIG. 6) ;

the input RF signal comprising transmissions sent on multiple carriers at different frequencies to a wireless device, the first output RF signal including at least a first carrier of the multiple carriers (as recited above, the input RF signal comprising satellite wave signal and ground wave signal (column 5 lines 1-4) and the output to intermediate frequency demodulation stage for ground wave, corresponding to the claimed first carrier of the multiple carriers);

and a second amplifier stage configured to receive and amplify the input RF signal and provide a second output RF signal to a second load circuit when the second amplifier stage is enabled (variable gain amplifier 25, corresponding to the claimed second amplifier stage, receiving input RF signal (e.g. satellite wave signal and ground wave signal) and providing an output to intermediate frequency demodulation stage (for satellite wave), which corresponds to the claimed second load circuit (see column 5 lines 1-30 and FIG. 6), the second output RF signal including at least a second carrier of

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the multiple carriers different than the first carrier (as recited above, the input RF signal comprising satellite wave signal and ground wave signal (column 5 lines 1-4) and the output to intermediate frequency demodulation stage for satellite wave, corresponding to the claimed second carrier of the multiple carriers).

Note: the rejection is based on the assumption that both first and second amplifier stages are enabled.

Regarding claim 11, Hirose further discloses an input matching circuit coupled to the first and second amplifier stages and configured to receive a receiver input signal and provide the input RF signal (FIG. 6 discloses a combination of a voltage control type variable gain amplifier 33, a wave detection 35 and a control circuit 36 (see column 5 lines 5-20). The combination constitutes the input matching circuit as claimed).

Regarding claim 12, Hirose further discloses the input matching circuit being tunable and comprising at least one adjustable circuit component (the combination of a voltage control type variable gain amplifier 33, a wave detection 35 and a control circuit 36 is tunable and the voltage control type variable gain amplifier 33 is adjustable).

Regarding claim 17, claim is rejected on the same ground as for claim 1 because of similar scope.

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