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Office Action Summary	Application No. 13/590,423	Applicant(s) TASIC ET AL.	
	<b>Examiner</b> KHANH C. TRAN	<b>Art Unit</b> 2631	AIA (First Inventor to File) Status No
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the	corresponden	ce address
<ul> <li>A SHORTENED STATUTORY PERIOD FOR REPL</li> <li>THIS COMMUNICATION.</li> <li>Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication.</li> <li>If NO period for reply is specified above, the maximum statutory period</li> <li>Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).</li> </ul>	36(a). In no event, however, may a reply be ti will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDON	mely filed h the mailing date o ED (35 U.S.C. § 133	f this communication.
Status			
<ul> <li>1) Responsive to communication(s) filed on <u>2/6/2</u></li> <li>A declaration(s)/affidavit(s) under <b>37 CFR 1</b>.</li> </ul>			
2a) This action is <b>FINAL</b> . 2b) This	s action is non-final.		
3) An election was made by the applicant in resp		set forth durin	ng the interview on
; the restriction requirement and election	n have been incorporated into this	s action.	
4) Since this application is in condition for allowa closed in accordance with the practice under l			to the merits is
Disposition of Claims* 5)			
5a) Of the above claim(s) is/are withdra			
6) Claim(s) is/are allowed.	withom consideration.		
7)⊠ Claim(s) <u>1,11,12,17 and 19</u> is/are rejected.			
8) Claim(s) <u>2-10,13-16,18 and 20</u> is/are objected	to		
9) Claim(s) are subject to restriction and/c			
* If any claims have been determined <u>allowable</u> , you may be e		secution High	way program at a
participating intellectual property office for the corresponding a			inaj programara
http://www.uspto.gov/patents/init_events/pph/index.jsp or send			
Application Papers 10) The specification is objected to by the Examine	or.		
11) $\square$ The drawing(s) filed on <u>8/21/2012</u> is/are: a) $\square$		the Exeminer	
· · · · · · · ·			
Applicant may not request that any objection to the			
Replacement drawing sheet(s) including the correc	tion is required if the drawing(s) is or	jected to. See	37 CFR 1.121(0).
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign	n priority under 35 U.S.C. § 119(a	ı)-(d) or (f).	
a) All b) Some** c) None of the:			
1. Certified copies of the priority documen		tion Nie	
2. Certified copies of the priority documen			
3. Copies of the certified copies of the price	-	ved in this Nat	tional Stage
application from the International Burea			
** See the attached detailed Office action for a list of the certifi	ea copies not receivea.		
Attachment(s) 1) X Notice of References Cited (PTO-892)	o) 🗖 haa mitaa oo m		
	3) Interview Summary Paper No(s)/Mail D		
2) 🕅 Information Disclosure Statement(s) (PTO/SB/08a and/or PTO/	SB/08b)		
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## DETAILED ACTION

1. The present application is being examined under the pre-AIA first to invent provisions.

2. The Amendment filed on 2/6/2014 has been entered. Claims 1-20 are still pending in this Office action.

## **Response to Arguments**

3. Applicant's arguments, see Applicants' Remarks, filed 2/6/2014, with respect to the rejection(s) of claim(s) 1, 7, 17 and 19 under 35 U.S.C. 103(a) have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Hirose U.S. Patent 7,317,894 B2.

#### Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of pre-AIA 35 U.S.C.

102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1, 11-12 and 17 are rejected under pre-AIA 35 U.S.C. 102(b) as being

anticipated by Hirose U.S. Patent 7,317,894 B2.

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Regarding claim 1, Hirose discloses an apparatus (FIG. 6 digital broadcast receiver) comprising:

a first amplifier stage configured to receive and amplify an input radio frequency (RF) signal and provide a first output RF signal to a first load circuit when the first amplifier stage is enabled (variable gain amplifier 15, corresponding to the claimed first amplifier stage, receiving input RF signal (e.g. satellite wave signal and ground wave signal) and providing an output to intermediate frequency demodulation stage (for ground wave), which corresponds to the claimed first load circuit (see column 5 lines 1-30 and FIG. 6) ;

the input RF signal comprising transmissions sent on multiple carriers at different frequencies to a wireless device, the first output RF signal including at least a first carrier of the multiple carriers (as recited above, the input RF signal comprising satellite wave signal and ground wave signal (column 5 lines 1-4) and the output to intermediate frequency demodulation stage for ground wave, corresponding to the claimed first carrier of the multiple carriers);

and a second amplifier stage configured to receive and amplify the input RF signal and provide a second output RF signal to a second load circuit when the second amplifier stage is enabled (variable gain amplifier 25, corresponding to the claimed second amplifier stage, receiving input RF signal (e.g. satellite wave signal and ground wave signal) and providing an output to intermediate frequency demodulation stage (for satellite wave), which corresponds to the claimed second load circuit (see column 5 lines 1-30 and FIG. 6), the second output RF signal including at least a second carrier of

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the multiple carriers different than the first carrier (as recited above, the input RF signal comprising satellite wave signal and ground wave signal (column 5 lines 1-4) and the output to intermediate frequency demodulation stage for satellite wave, corresponding to the claimed second carrier of the multiple carriers).

# <u>Note</u>: the rejection is based on the assumption that both first and second amplifier stages are enabled.

Regarding claim 11, Hirose further discloses an input matching circuit coupled to the first and second amplifier stages and configured to receive a receiver input signal and provide the input RF signal (FIG. 6 discloses a combination of a voltage control type variable gain amplifier 33, a wave detection 35 and a control circuit 36 (see column 5 lines 5-20). The combination constitutes the input matching circuit as claimed).

Regarding claim 12, Hirose further discloses the input matching circuit being tunable and comprising at least one adjustable circuit component (the combination of a voltage control type variable gain amplifier 33, a wave detection 35 and a control circuit 36 is tunable and the voltage control type variable gain amplifier 33 is adjustable).

Regarding claim 17, claim is rejected on the same ground as for claim 1 because of similar scope.

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