

UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE PATENT TRIAL AND APPEAL BOARD

MICROSOFT CORPORATION.

Petitioner,

v.

SAINT REGIS MOHAWK TRIBE,

Patent Owner

Patent No. 7,620,800

Issued: November 17, 2009

Filed: April 9, 2007

Inventors: Jon M. Huppenthal, David E. Caliga

Title: MULTI-ADAPTIVE PROCESSING SYSTEMS AND
TECHNIQUES FOR ENHANCING PARALLELISM AND
PERFORMANCE OF COMPUTATIONAL FUNCTIONS

Inter Partes Review No. IPR2018-01606

**PETITION FOR *INTER PARTES* REVIEW OF U.S. PATENT NO. 7,620,800
UNDER 35 U.S.C. §§ 311-319 AND 37 C.F.R. § 42.1-.80 & 42.100-.123**

TABLE OF CONTENTS

I. INTRODUCTION 1

II. COMPLIANCE WITH REQUIREMENTS FOR INTER PARTES REVIEW 3

 A. Certification the 800 Patent May Be Contested by Petitioner 3

 B. Fee for Inter Partes Review (§ 42.15(a))..... 3

 C. Mandatory Notices (37 CFR § 42.8(b)) 3

 D. Proof of Service (§§ 42.6(e) and 42.105(a)) 5

III. IDENTIFICATION OF CHALLENGED CLAIMS 5

IV. RELEVANT INFORMATION CONCERNING THE CONTESTED PATENT 6

 A. Effective Filing Date 6

 B. Level of Ordinary Skill 6

 C. Overview of 800 Patent..... 7

 D. Prosecution History of 800 Patent..... 9

 E. Claim Construction..... 10

 1. “functional unit” 10

 2. “data driven” 11

Petition for *Inter Partes* Review of U.S. Patent No. 7,620,800

3.	“transforming an algorithm into a data driven calculation”	11
4.	“form”	12
5.	“clusters of functional units”	14
6.	“data dimension”	15
7.	“seamlessly”	15
8.	“data mining”	16
V.	OVERVIEW OF SPLASH 2	17
A.	General Architecture of Splash 2.....	17
A.	Systolic Algorithms For Searching Genetic Databases	20
VI.	PRECISE REASONS FOR REQUESTED RELIEF	26
A.	Claims 1 and 15 are Anticipated by Splash2	26
1.	Claim 1 is Anticipated	26
2.	Claim 15 is Anticipated	44
B.	Claims 1 and 15 are Obvious over Splash2	45
1.	Considering the Chapters of Splash2 Together	45
2.	Transforming an Algorithm into a Calculation	47
3.	Forming.....	48
4.	First Wherein Clause.....	49

Petition for *Inter Partes* Review of U.S. Patent No. 7,620,800

5. Second Wherein Clause51

C. Claims 1 and 15 are Obvious over Splash2 in view of Gaudiot51

D. Claims 17, 7, and 24 Are Obvious over Splash2 in view of Chunky
SLD, With or Without Gaudiot55

1. Claim 17 is Obvious.....55

2. Claim 7 is Obvious.....67

3. Claim 24 is Obvious.....68

VII. CONCLUSION.....70

I. INTRODUCTION

U.S. Patent No. 7,620,800 (“the 800 Patent”) describes a multiprocessor computer system for performing systolic, data driven processing on reconfigurable computing elements, such as FPGAs. The application from which it issued was filed in 2007.

Systolic data driven processing on multiprocessor systems employing FPGAs was well known by that time. The principal reference relied on here is a 1996 book describing what is likely the most successful example of such a system, the Splash 2 computer. *See* EX1007. Splash 2 was used by numerous scientists and engineers to carry out various types of processing, including numerous systolic applications. As demonstrated below, the Splash 2 book discloses details of the Splash 2 system and of certain processing carried out on that system for the comparison of genetic sequences that together satisfy each and every element of numerous claims of the 800 Patent.

One would think that such a seminal prior art reference would have been provided to the examiner of the 800 Patent, if the applicants had been aware of it. In fact, they were aware of the book, but provided only a small excerpt to the

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