

Claim 22

The method of claim 1 wherein said calculation comprises a **(BA) protein folding function**.

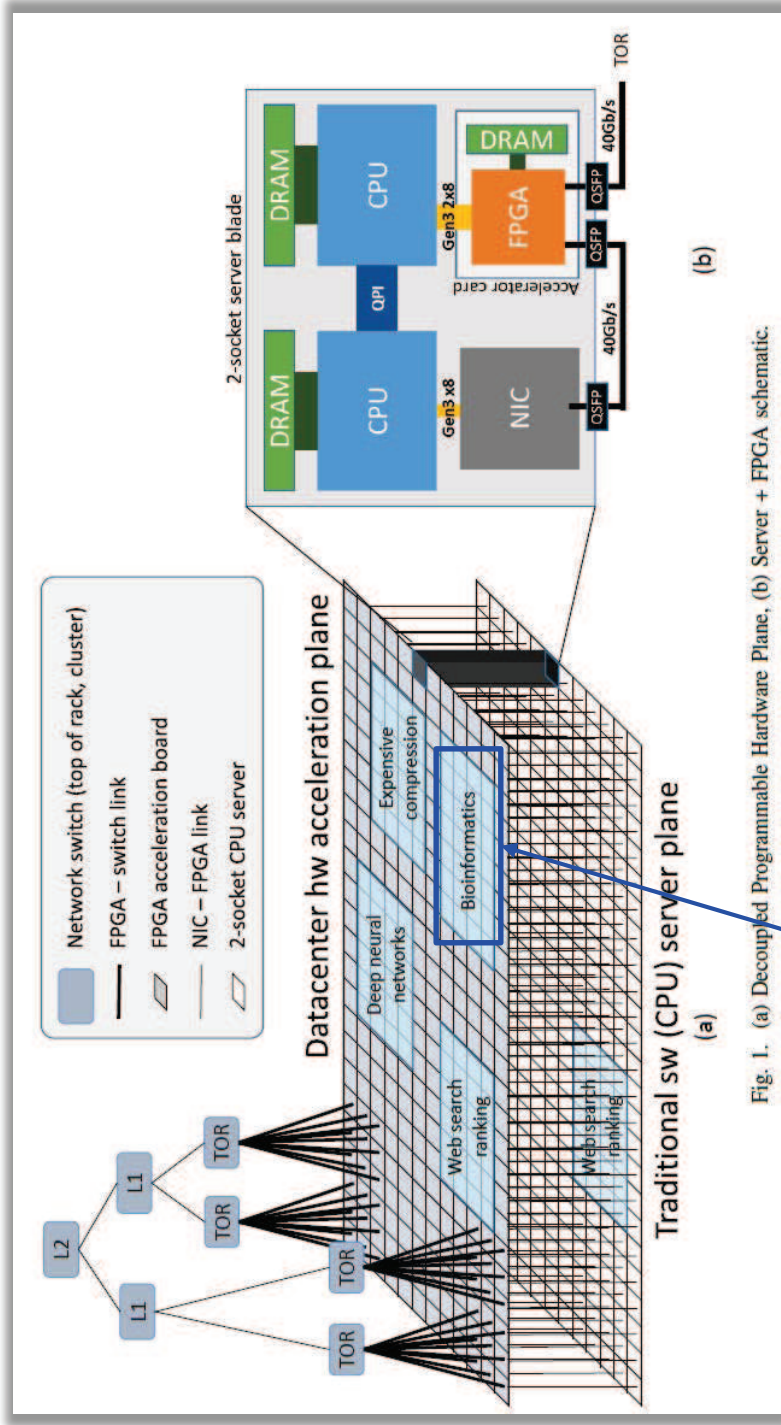


Fig. 1. (a) Decoupled Programmable Hardware Plane, (b) Server + FPGA schematic.

**(BA)**

**Source:** Adrian M. Caulfield et al.: A Cloud-Scale Acceleration Architecture, Microsoft Corporation, IEEE Computer Society, October 15, 2016