UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

MICROSOFT CORPORATION.

Petitioner,

v.

SAINT REGIS MOHAWK TRIBE,

Patent Owner

Patent No. 7,620,800

Issued: November 17, 2009

Filed: April 9, 2007

Inventors: Jon M. Hupenthal, David E. Caliga

Title: MULTI-ADAPTIVE PROCESSING SYSTEMS AND TECHNIQUES FOR ENHANCING PARALLELISM AND PERFORMANCE OF COMPUTATIONAL FUNCTIONS

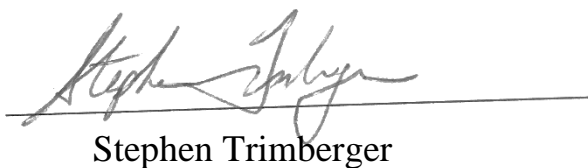_____

Inter Partes Review No. IPR2018-01605

Inter Partes Review No. IPR2018-01606

Inter Partes Review No. IPR2018-01607

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DECLARATION OF DR. STEPHEN TRIMBERGER

I, Stephen Trimberger, Ph.D., do hereby declare and state, that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, under Section 1001 of Title 18 of the United States Code.

Executed on: August 27, 2018

Stephen Trimberger

I, Stephen Trimberger, Ph.D, state and declare as follows:

1.   I am over the age of 18, of sound mind, and capable of making this declaration.

2.   I am submitting this declaration based on my own personal knowledge of the facts stated here, and I am not being compensated by Microsoft for drafting this declaration. I am, however, being compensated by Microsoft in a related matter for which I have been retained as an expert consultant.

3.   I am presently the Director of The Trimberger Family Foundation where I manage operations of a charitable foundation, including overseeing investments, legal and accounting professionals. I am also Glenn L. Martin Visiting Research Engineer at the University of Maryland, where I am investigating new semiconductor technologies. I am employed at the United States Defense Advanced Research Projects Agency where I am investigating new semiconductor technologies. I was formerly employed by Xilinx, Inc. from 1988 to 2017 culminating in a position in Xilinx Research Labs. In that capacity, I was responsible for corporate technology vision and leadership, and led a research group investigating various aspects of semiconductor devices, including power optimization, die stacking, variation, hardware security, defect tolerance, and multi-context FPGA architecture and software.

I. Papers Presented at 1997 Institute of Electrical and Electronics Engineers FPGAs for Custom Computing Machines (FCCM) Symposium

4. As a result of my work in FPGAs at Xilinx, I attended nearly every IEEE FCCM Symposium from 1995 to 2004. The Symposiums are held yearly.

5. The paper *Carl Ebeling et al.*, "Mapping Applications to the RaPiD Configurable Architecture" IEEE Symposium on FPGAs for Custom Computing Machines. April 16-18, 1997 Napa Valley, California: 106-115 ("RaPiD") was presented and published at the 1997 IEEE Symposium on FPGAs for Custom Computing Machines in California on April 16-18, 1997 ("1997 IEEE FCCM Symposium" or "IEEE Symposium")[1]. RaPiD is attached to this declaration and labeled Exhibit 1009, and also includes true and correct copies of the proceeding cover page, title pages, copyright pages, table of contents, and the RaPiD article itself.

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[1] I note that the cover of the proceedings states the title as "IEEE Symposium on FPGAs for Custom Computing Machines," while the title page states "The 5th Annual IEEE Symposium on Field-Programmable Custom Computing Machines," and the header on the Table of Contents states "Symposium on Field-Programmable Custom computing Machines – FCCM'97." The symposium is commonly referred to as "FCCM'97." Since no other IEEE symposiums have a similar title, all titles have been used by researchers without confusion.

6.   Exhibit 1009 is in a condition that creates no suspicion about its authenticity. Exhibit 1009 could be found in a number of libraries and with the Institute of Electrical and Electronics Engineers ("IEEE"), for example. This particular version of Exhibit 1009 appears to have been found within the custody of the Library of Congress, as evidenced by the stamp on its pages. The Library of Congress is one of a number of places a document such as Exhibit 1009 would likely be found.

7.   The paper *Michael Rencher and Brad L. Hutchins*, "Automated Target Recognition on Splash 2" IEEE Symposium on FPGAs for Custom Computing Machines. April 16-18, 1997 Napa Valley, California: 192-200 ("Chunky SLD") was also presented and published at the 1997 IEEE FCCM Symposium. Chunky SLD is attached to this declaration and labeled Exhibit 1011, and also includes true and correct copies of the proceeding cover page, title pages, copyright pages, table of contents, and the Chunky SLD article itself.

8.   Exhibit 1011 is in a condition that creates no suspicion about its authenticity. Exhibit 1011 could be found in a number of libraries and with the IEEE, for example. This particular version of Exhibit 1011 appears to have been found within the custody of the British Library's Boston Spa facility, as evidenced

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