

Huppenthal

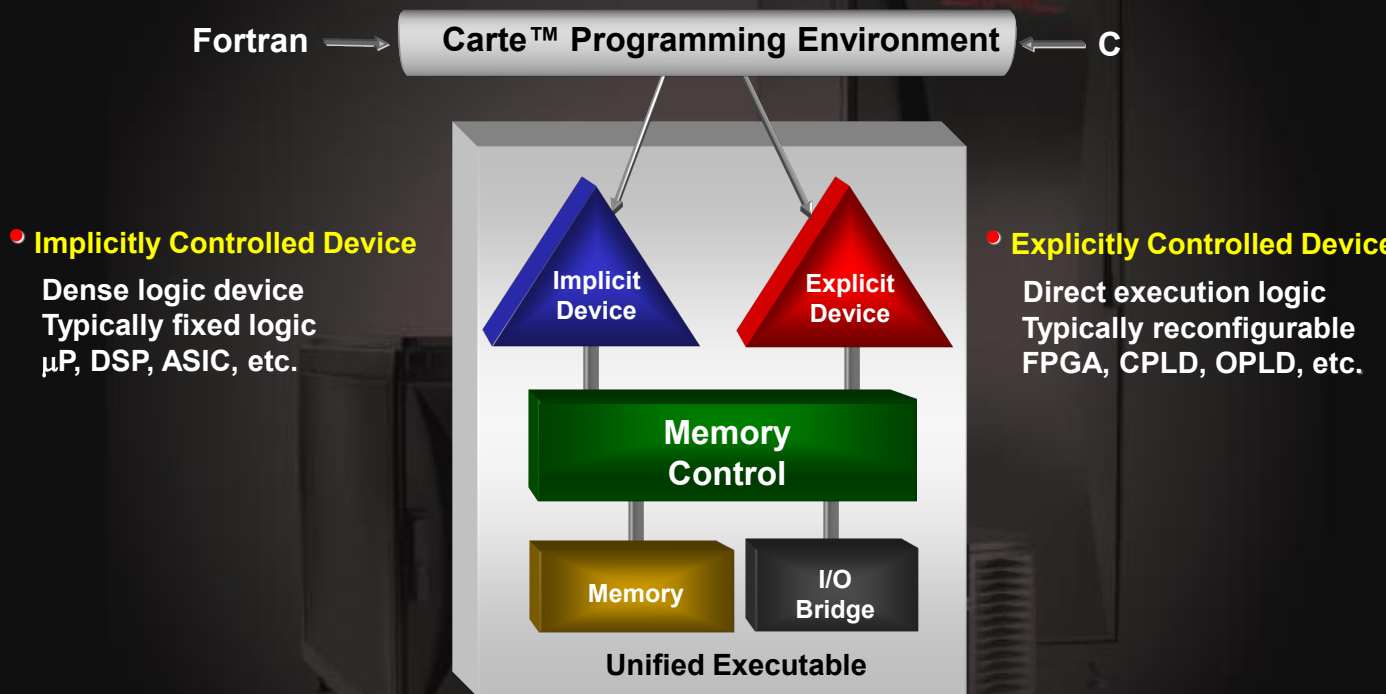
Reference 9

SRRC

ARCHITECTURE

IMPLICIT + EXPLICIT

IMPLICIT+EXPLICIT™ Architecture



SRC's explicitly controlled processor is called MAP®

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SRC Milestones

- **First SRC system with MAP[®] installed at ORNL in 1999**
 - Direct Execution Logic had merit
 - Needs tightly integrated programming environment
- **First SRC-6E installed at GWU in 2002**
 - Included Carte[™] Programming Environment
- **First SRC-6 with Hi-Bar[®] switch and Common Memory installed May 2004**
- **First MAPstation[™] workstation delivered in Aug 2004**
- **First Portable MAPstation delivered in April 2005 to AF**

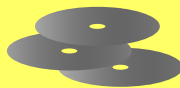
System Solutions

Technology Building Blocks

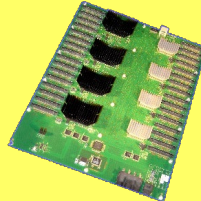
MAP[®]
Processor



Carte[™]
Environment



Hi-Bar[®]
Switch



SNAP[™]
Interface



Common
Memory



Embedded



Workstation



Server

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