

Huppenthal

Reference 5

COMPUTATION BOOK

NAME

JON HUPPENTHAL

Course SRC BOOK 1



AMPAD Corp., Dallas, TX. 75252

22-157

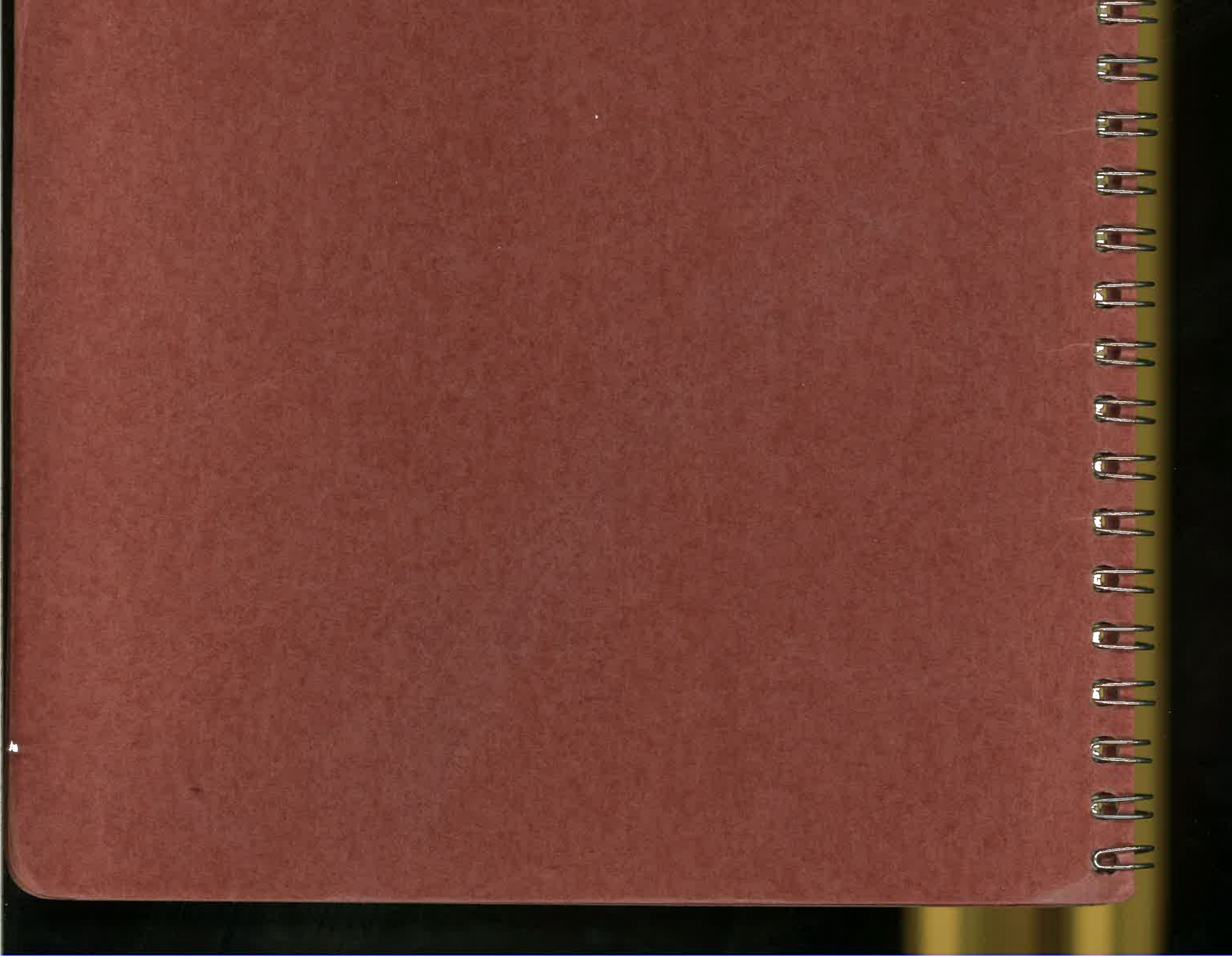
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A1 SAMPLES AT END OF Q3-97 FUNCTIONAL GOOD PARTS
B0 SAMPLES AT Q1-98

1M CACHE INITIALLY AVAILABLE FOLLOWED BY 2M 1Q LATER

3.3V I/O RING } ALL REQUIRED BY DESCHUTES
2.5V CACHE }
1.8V CORE VOLTAGE }

MOLEX MANUFACTURES SLOT 2 CONNECTOR

- PLASTIC COVER ON BACK - AL ON FRONT

SIZE	CURRENT	GOAL
H	5.1	4.7
W	6.4	6
THICK	.8	3/4

CONN ALONG THIS DIM

- 50W DESIGN ENVELOPE

- L2 REGULATOR COULD BE SHARED BETWEEN 2 PROCESSORS
BUT NOT CORE VOLTAGE

- 300 LFM WITH A 1" TOTAL HEAT SINK HEIGHT
WITH .15" BASE THICKNESS YOU GET
PLENTY OF MARGIN (73%)

- FULLY LOADED BUS IS ABOUT 10"