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# (12) United States Patent

### Brooks et al.

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# (54) SYSTEM FOR TRANSFORMING STREAMING VIDEO DATA

(75) Inventors: Roger K. Brooks, Palo Alto, CA (US); Stephen A. Molloy, Los Gatos, CA (US); Chi-Te Feng, Hsi Chih (TW); Qing Zhang, Sunnyvale, CA (US); Yanda Ma, Milpitas, CA (US); Dave

Singhal, San Jose, CA (US)

(73) Assignee: Vidiator Enterprises Inc., Nassau (BS)

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## Related U.S. Application Data

- (60) Provisional application No. 60/157,468, filed on Oct. 1, 1999.
- (51) **Int. Cl.** *H04N 7/173* (2006.01)

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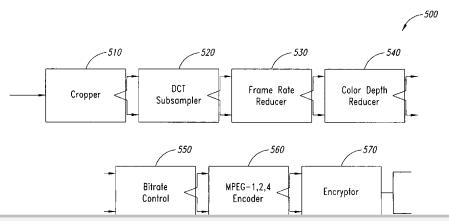
## (Continued)

Primary Examiner—Shawn S. An (74) Attorney, Agent, or Firm—Seed IP Law Group PLLC

## (57) ABSTRACT

According to one embodiment, a circuit configured to form an output video stream includes a resolution modification circuit configured to receive a plurality of video frames from a frame buffer, and configured to modify resolution of the plurality of video frames, when the desired resolution for the output video stream is different than a resolution of the input video stream, the plurality of frames of data derived from an input video stream, a frame reducing circuit coupled to the resolution reducing circuit configured to reduce a number of video frames in the plurality of video frames from the resolution reducing circuit, when a desired frame rate for the output video stream is different than a frame rate of the input video stream, a depth reduction circuit coupled to the frame reducing circuit configured to reduce bit depth of the plurality of video frames from the frame reducing circuit, when a desired bit depth for the output video stream is different than a bit depth of the input video stream, and a rate reduction circuit coupled to the depth reduction circuit, configured to scale the plurality of video frames from the depth reduction circuit, in response to a desired bit rate for the output video stream, and an encoder coupled to the rate reduction circuit, configured to encode the plurality of video frames from the rate reduction circuit into the output video stream is also contemplated.

## 17 Claims, 8 Drawing Sheets





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## SAMPLE NETWORK TOPOLOGY

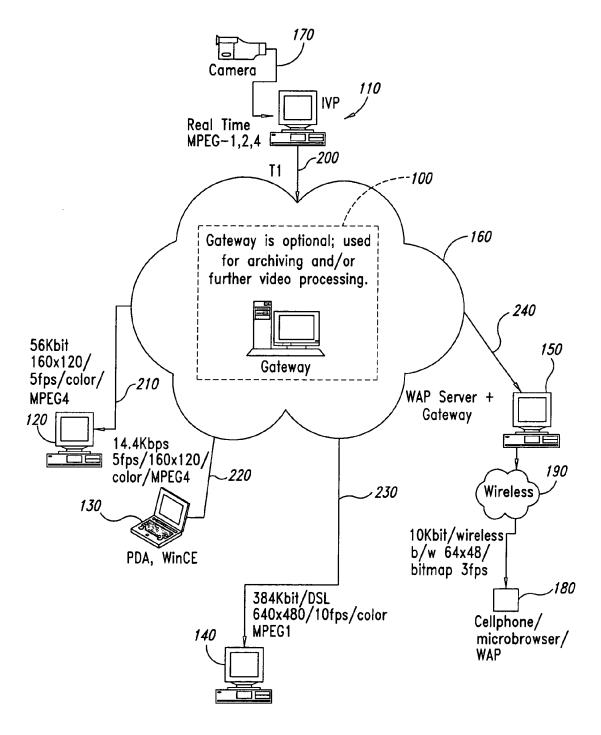
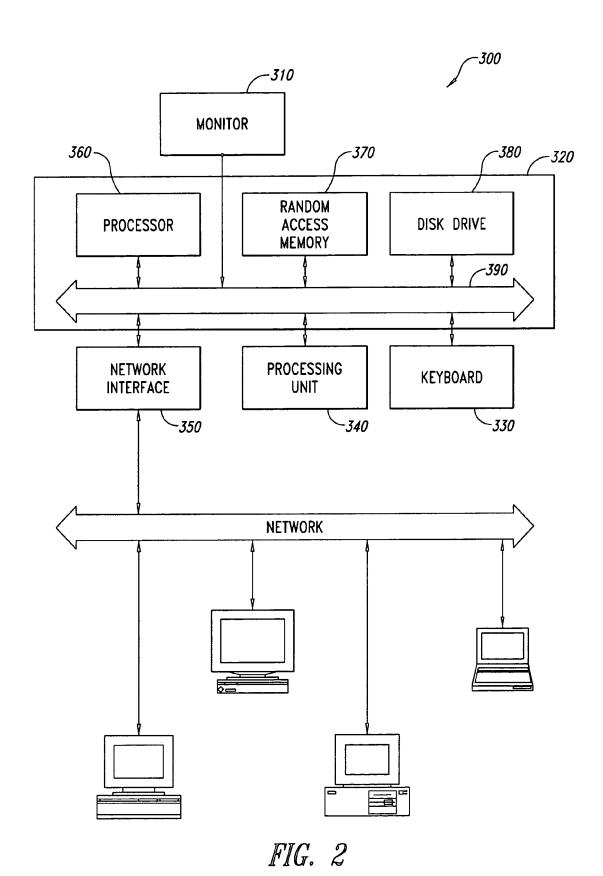
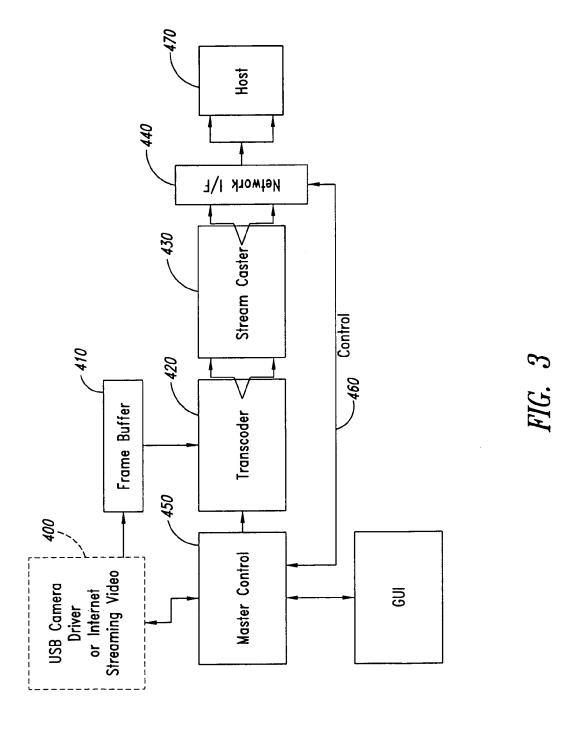


FIG. 1











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