# (12) United States Patent Toda et al.

(10) Patent No.: US 6,809,390 B2 (45) Date of Patent: Oct. 26, 2004

(54)	SEMICONDUCTOR CHIP MOUNTING
	SUBSTRATE, ELECTROOPTICAL DEVICE,
	LIQUID-CRYSTAL DEVICE,
	ELECTROLUMINESCENT DEVICE, AND
	ELECTRONIC EQUIPMENT

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(\*) Notice: Subject to any disclaimer, the term of this

patent is extended or adjusted under 35 U.S.C. 154(b) by 160 days.

(21) Appl. No.: 09/999,207

(22) Filed: Nov. 30, 2001

### (65) **Prior Publication Data**

US 2002/0088984 A1 Jul. 11, 2002

### (30) Foreign Application Priority Data

Nov. 30, 2000	(JP)	 2000-365715
Oct. 15, 2001	(JP)	 2001-317142

(51) **Int. Cl.**<sup>7</sup> ...... **H01L 31/0203**; G09G 3/36; G03H 1/04

(52) U.S. Cl. ...... 257/434; 345/98; 359/58

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### (57) ABSTRACT

Disclosed is a semiconductor chip mounting substrate 4a bearing semiconductor chips 19a and 19b thereof. The first substrate 4a includes a power source line 22 for supplying a supply voltage potential to the semiconductor chips 19a and 19b, a ground line 23 for supplying a ground voltage potential to the semiconductor chips 19a and 19b, output lines 21a and 21b to which an output signal is supplied from the semiconductor chips 19a and 19b, and an insulator layer 11 for covering the output lines 21a and 21b. The insulator layer 11 is formed so that no insulator layer is arranged in the area between the power source line 22 and the ground line 23.

### 10 Claims, 21 Drawing Sheets

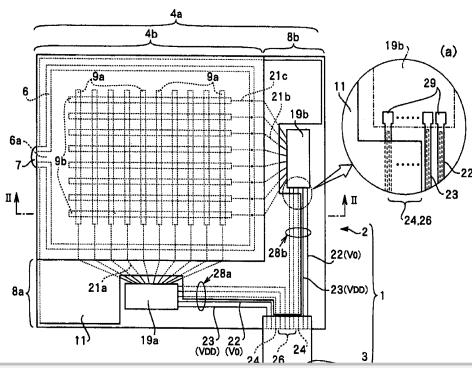
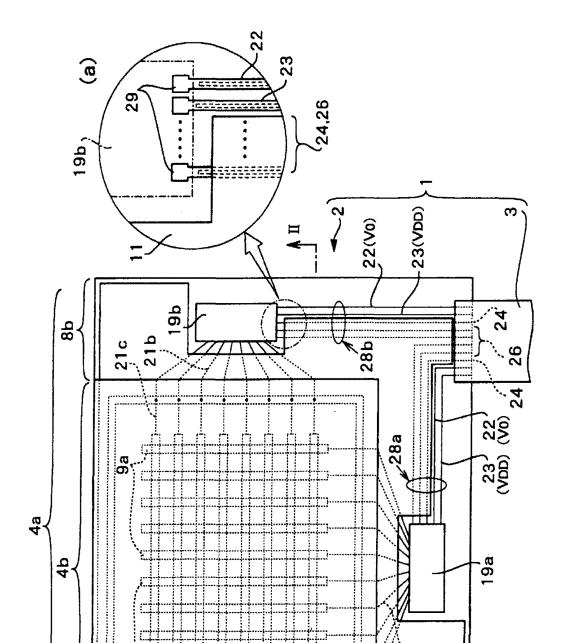
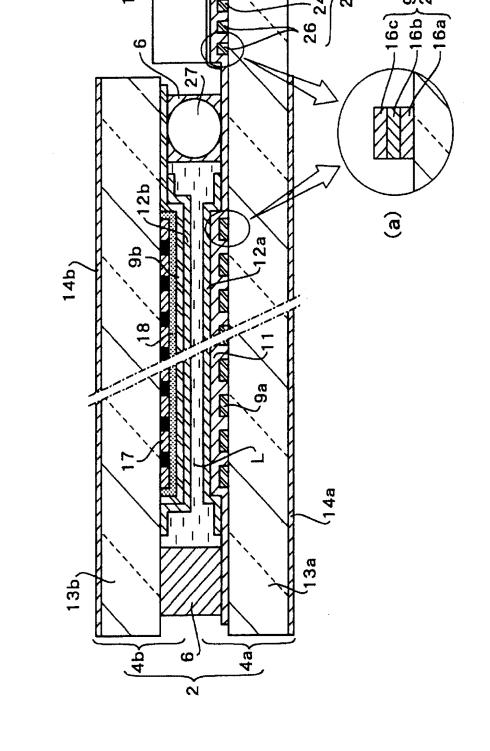




FIG. 1

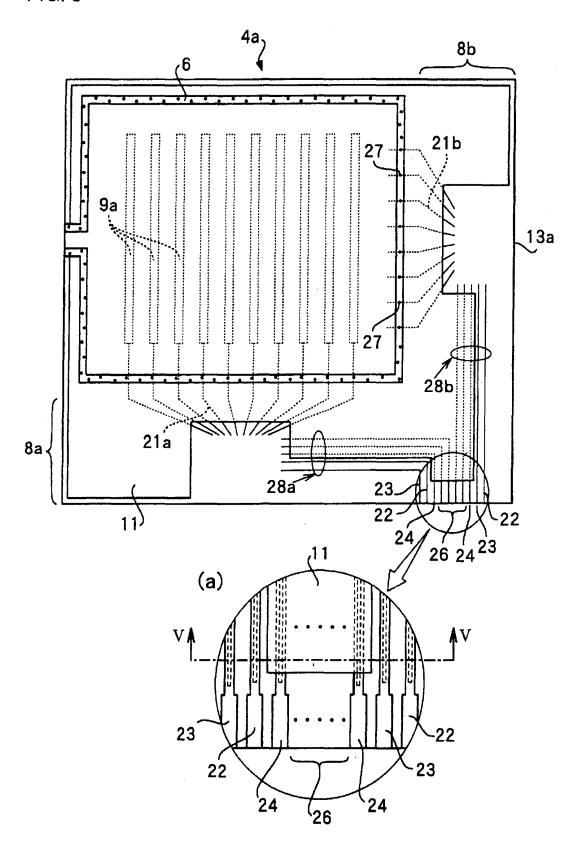






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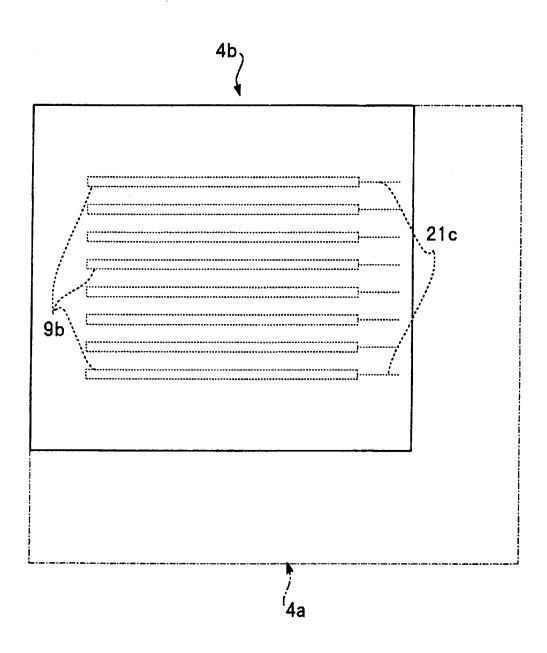
FIG. 3





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FIG. 4



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