

Petitioner Bluehouse Global Ltd.

Ex. 1004



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YAMAZAKI et al.(10) **Pub. No.: US 2011/0109351 A1**(43) **Pub. Date: May 12, 2011**(54) **SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD THEREOF****Publication Classification**(51) **Int. Cl.****H03K 3/00** (2006.01)**H01L 29/786** (2006.01)**H01L 21/44** (2006.01)(52) **U.S. Cl. 327/109; 257/43; 438/104; 257/E29.296;
257/E21.476**(75) **Inventors:** **Shunpei YAMAZAKI**, Setagaya
(JP); **Jun KOYAMA**, Sagami-hara
(JP); **Hiro-yuki MIYAKE**, Atsugi
(JP)(73) **Assignee:** **SEMICONDUCTOR ENERGY
LABORATORY CO., LTD.**,
Atsugi (JP)(21) **Appl. No.: 12/938,533**(22) **Filed: Nov. 3, 2010**(30) **Foreign Application Priority Data**

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ABSTRACT

An oxide semiconductor layer which is intrinsic or substantially intrinsic and includes a crystalline region in a surface portion of the oxide semiconductor layer is used for the transistors. An intrinsic or substantially intrinsic semiconductor from which an impurity which is to be an electron donor (donor) is removed from an oxide semiconductor and which has a larger energy gap than a silicon semiconductor is used. Electrical characteristics of the transistors can be controlled by controlling the potential of a pair of conductive films which are provided on opposite sides from each other with respect to the oxide semiconductor layer, each with an insulating film arranged therebetween, so that the position of a channel formed in the oxide semiconductor layer is determined.

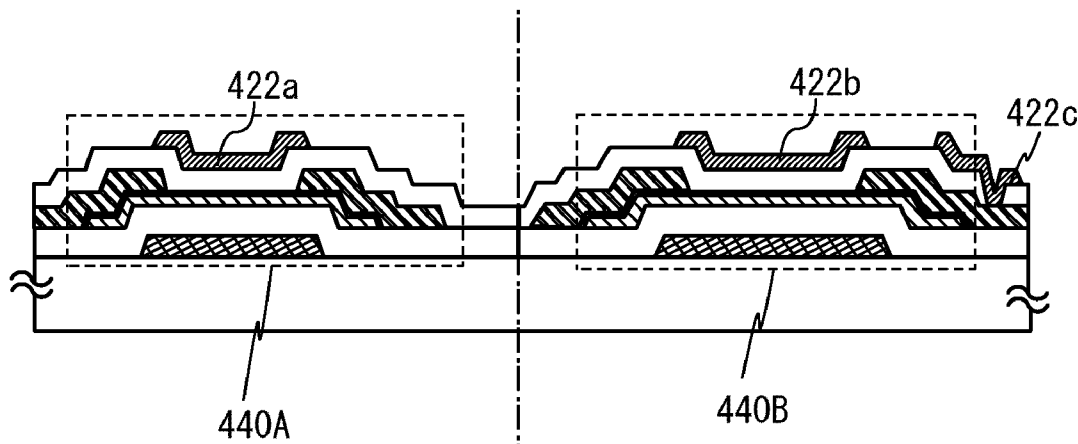


FIG. 1A

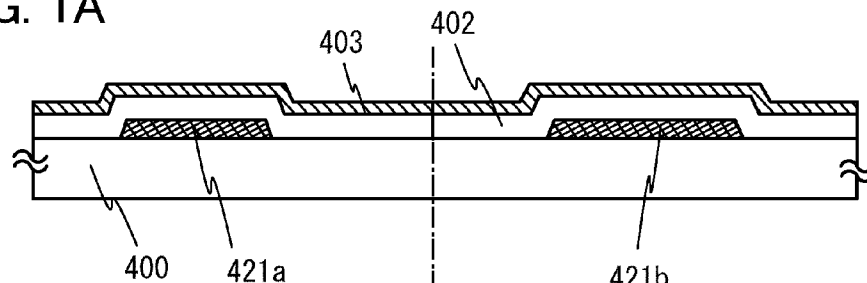


FIG. 1B

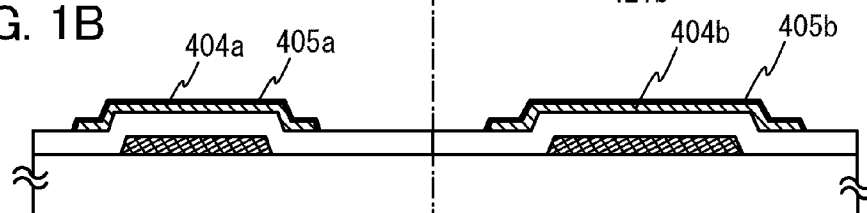


FIG. 1C

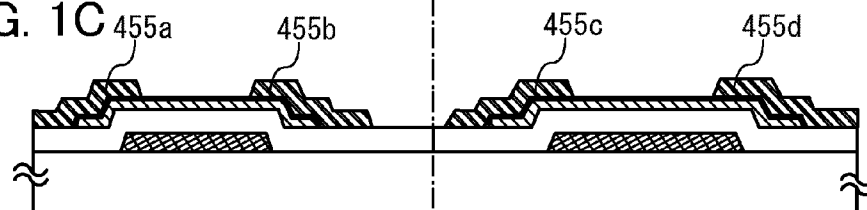


FIG. 1D

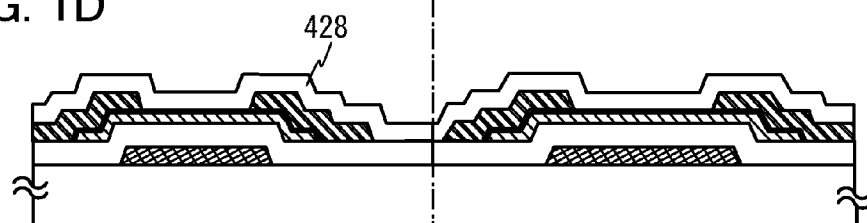


FIG. 1E

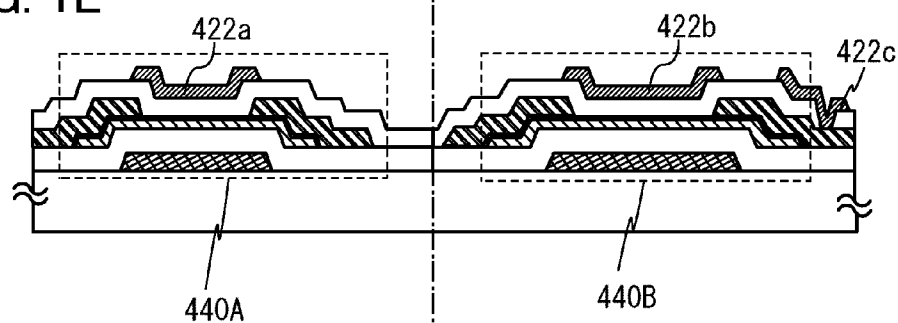


FIG. 3A

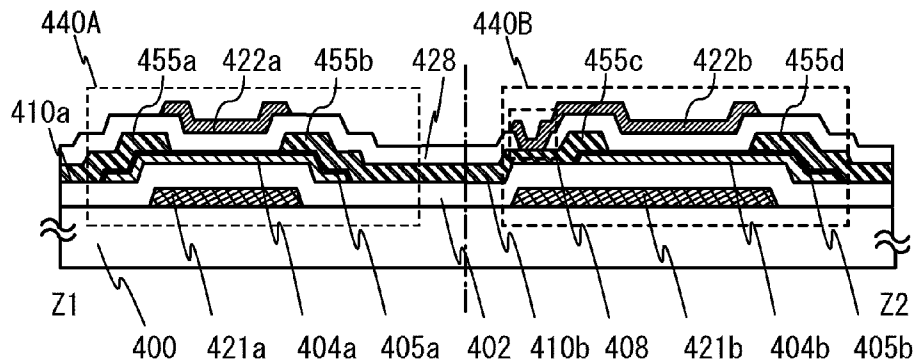


FIG. 3B

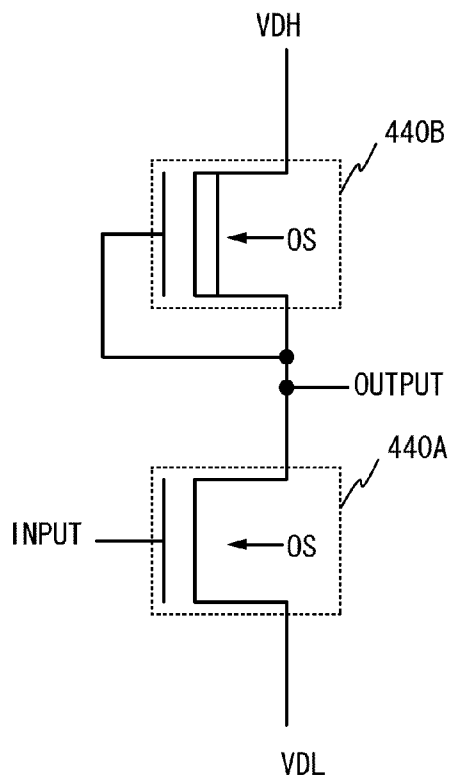
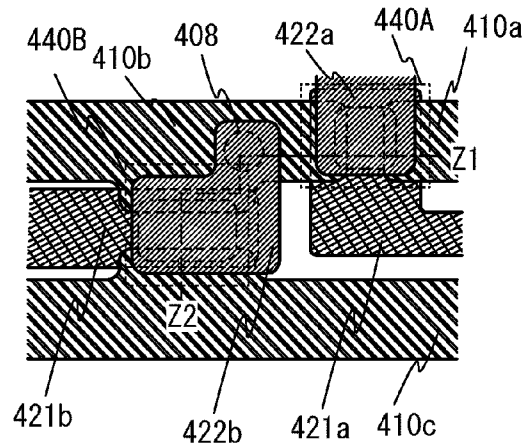


FIG. 3C



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