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**Kuriyagawa et al.**

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(54) **DISPLAY DEVICE AND MANUFACTURING METHOD THEREOF**

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(73) Assignees: **Hitachi Displays, Ltd.**, Chiba (JP); **Panasonic Liquid Crystal Display Co., Ltd.**, Hyogo (JP)

(\* ) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 35 days.

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(30) **Foreign Application Priority Data**  
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(51) **Int. Cl.**  
**H01L 21/00** (2006.01)  
(52) **U.S. Cl.** ..... **438/34**; 257/E33.004; 257/E33.003; 257/E33.064

(58) **Field of Classification Search** ..... 438/34; 257/E33.004, E33.003, E33.064  
See application file for complete search history.

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(57) **ABSTRACT**

A display device in which a plurality of gate wires and a plurality of drain wires that intersect the gate wires are provided, and thin film transistors connected to the gate wires and the drain wires are formed for respective pixel regions. At least one of the gate wires, the drain wires, and lead wires drawn from the gate wires or the drain wires is formed of a light-transmitting patterned conductive film. The light-transmitting patterned conductive film is formed of at least a first light-transmitting patterned conductive film, and a second light-transmitting patterned conductive film laminated on the first light-transmitting patterned conductive film. The second light-transmitting patterned conductive film is formed of a conductive film for coating only the surface of the first light-transmitting patterned conductive film including its side wall surface.

**7 Claims, 6 Drawing Sheets**

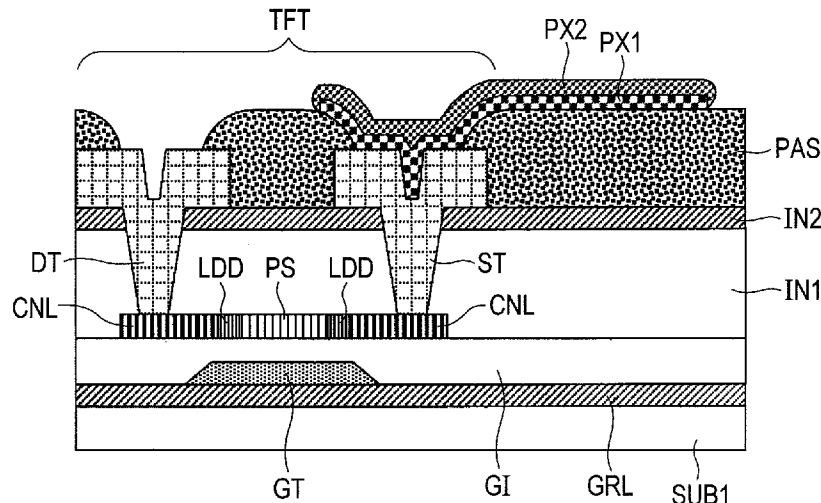


FIG. 1

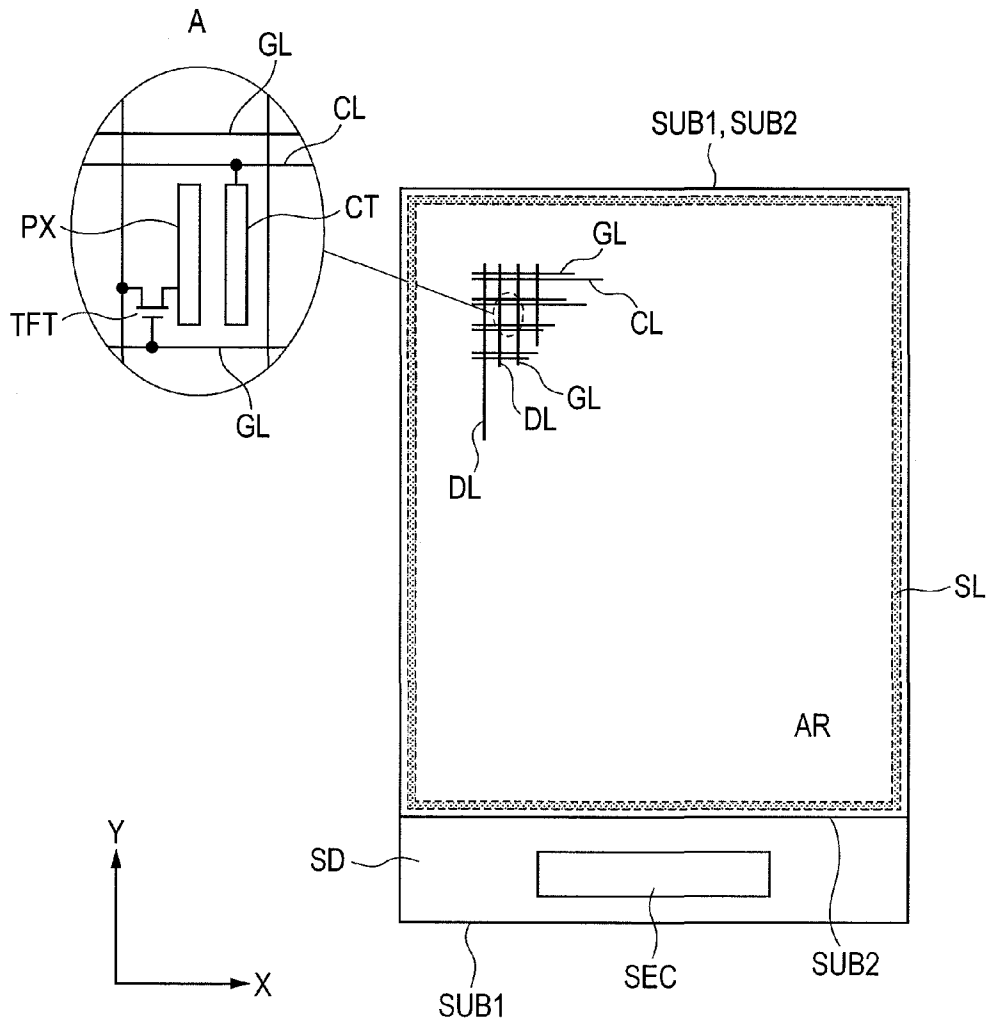


FIG. 2

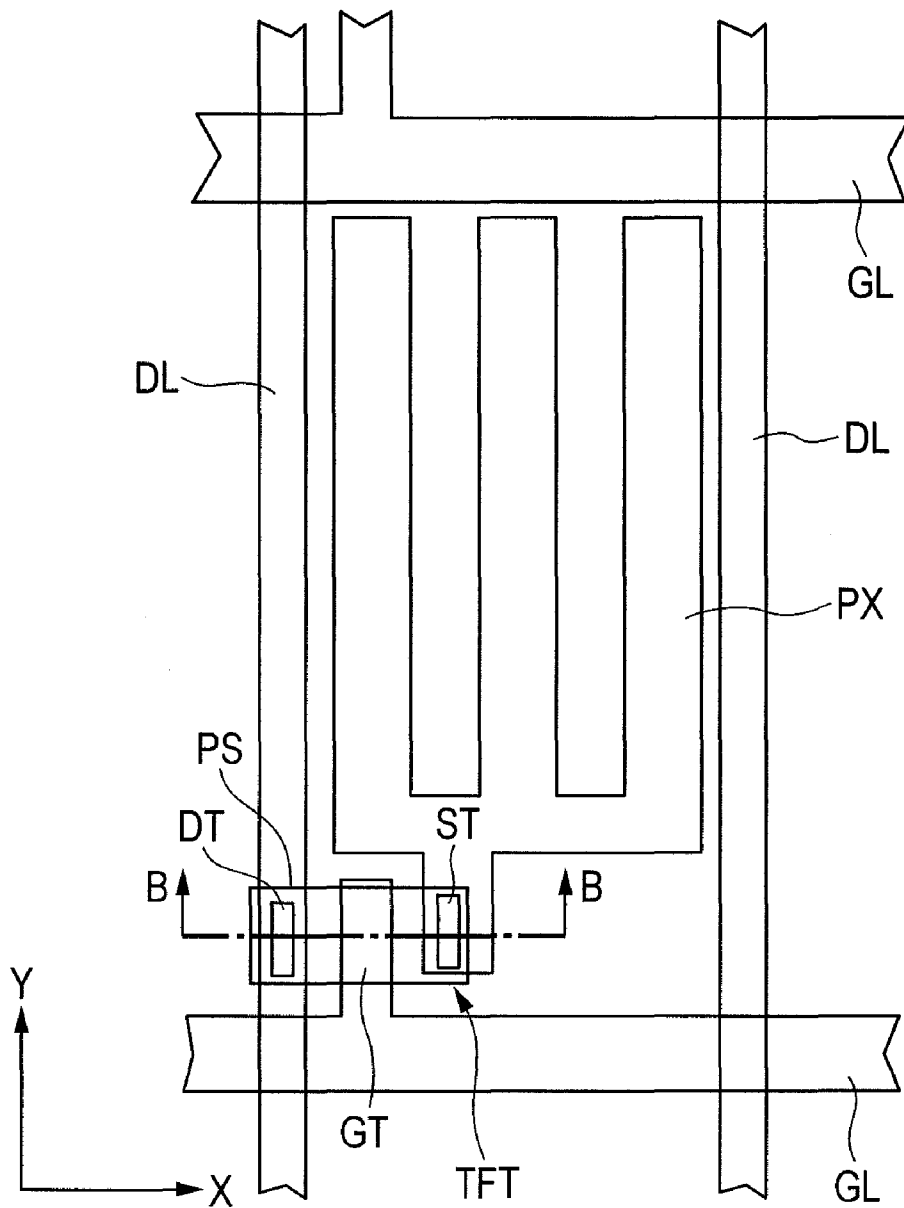


FIG. 3

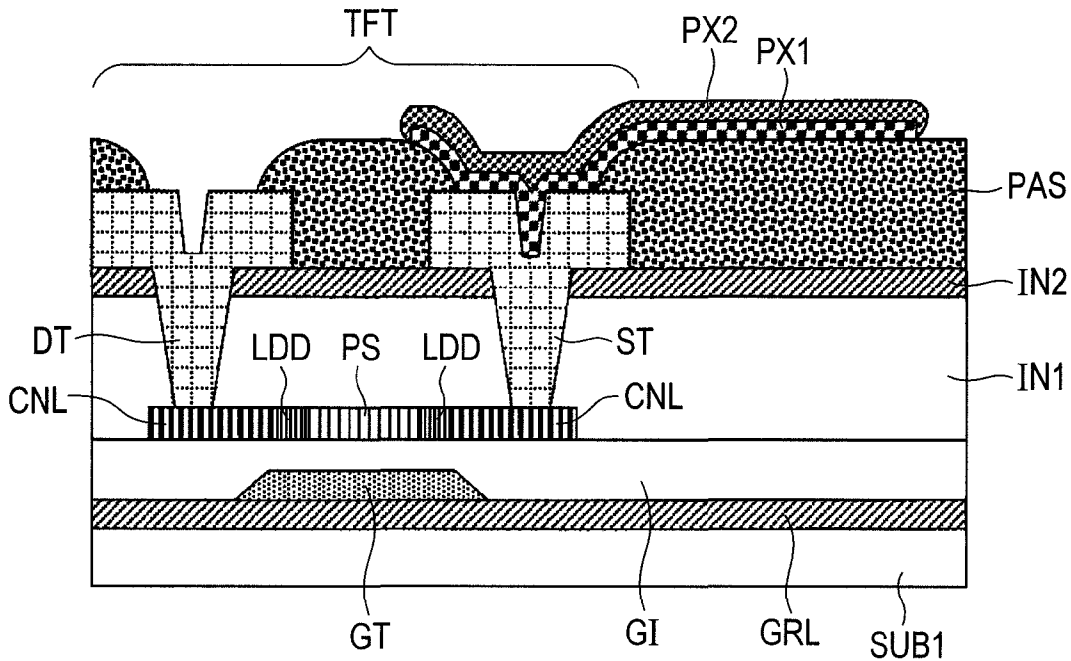


FIG. 4

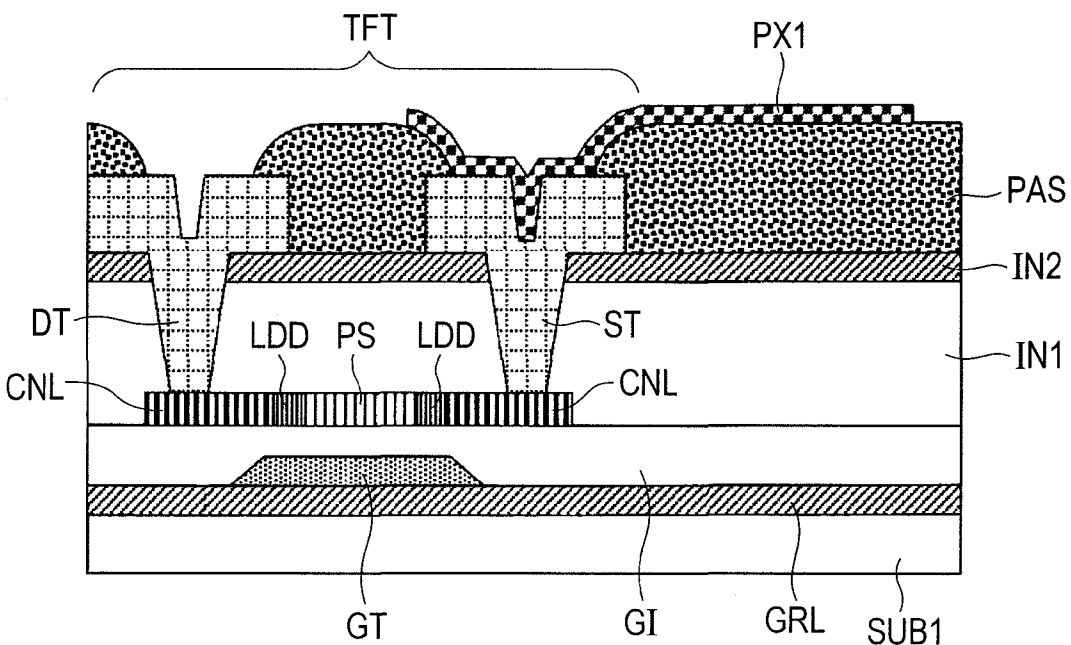


FIG. 5

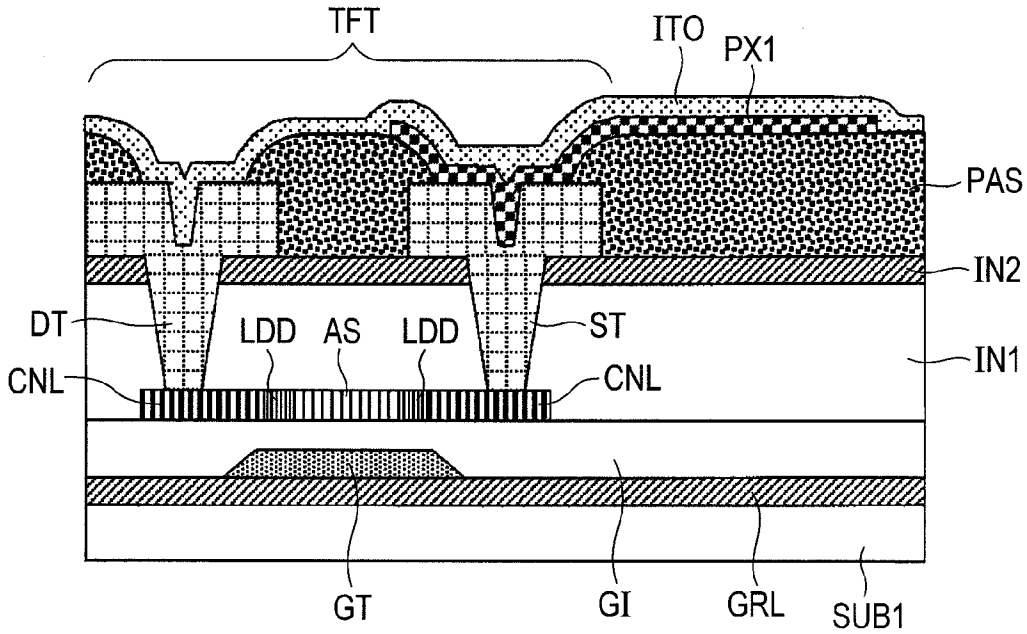
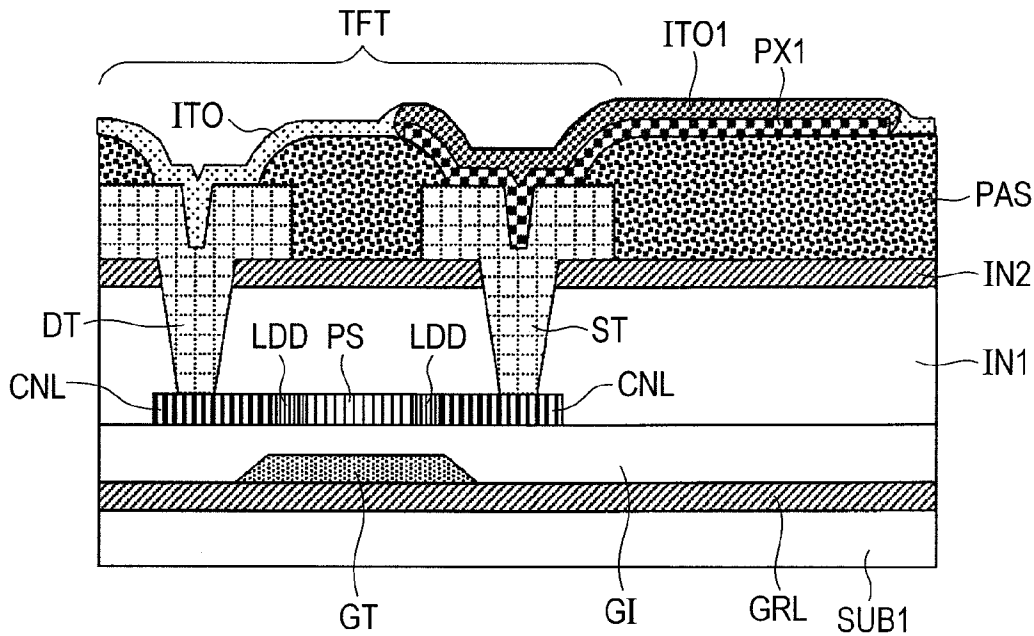


FIG. 6



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