

### (19) United States

## (12) Patent Application Publication (10) Pub. No.: US 2007/0140199 A1 Zhao et al.

# (54) POWER SAVING IN MULTI-PROCESSOR

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(21) Appl. No.:

11/634,366

(22) Filed:

Dec. 5, 2006

#### Related U.S. Application Data

Continuation-in-part of application No. 10/759,597, filed on Jan. 15, 2004.

(43) Pub. Date:

Jun. 21, 2007

Continuation-in-part of application No. 11/155,387, filed on Jun. 16, 2005, which is a continuation of application No. 10/083,044, filed on Feb. 25, 2002, now Pat. No. 6,943,667.

Provisional application No. 60/455,178, filed on Mar. 16, 2003.

#### **Publication Classification**

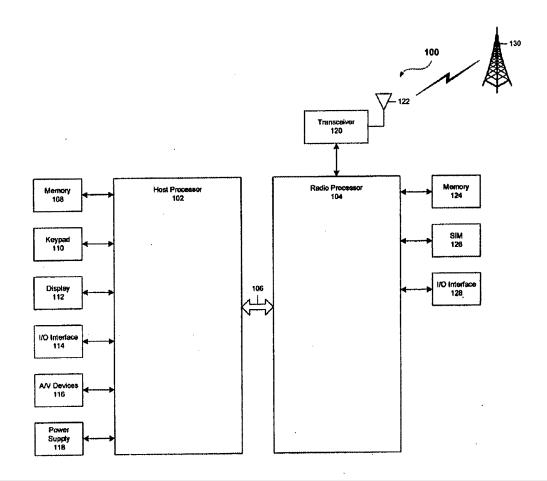
(51) Int. Cl. H04Q 7/24

(2006.01)

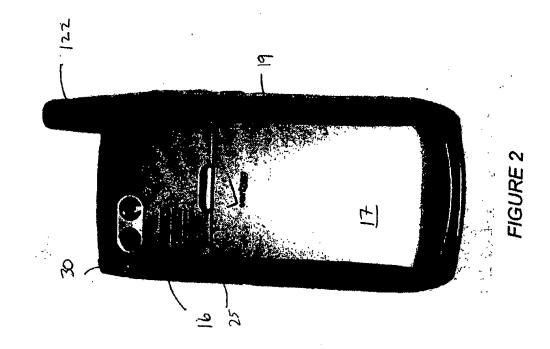
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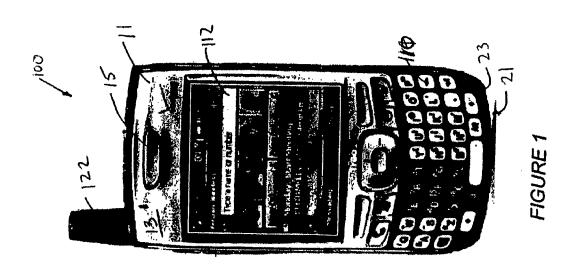
#### **ABSTRACT** (57)

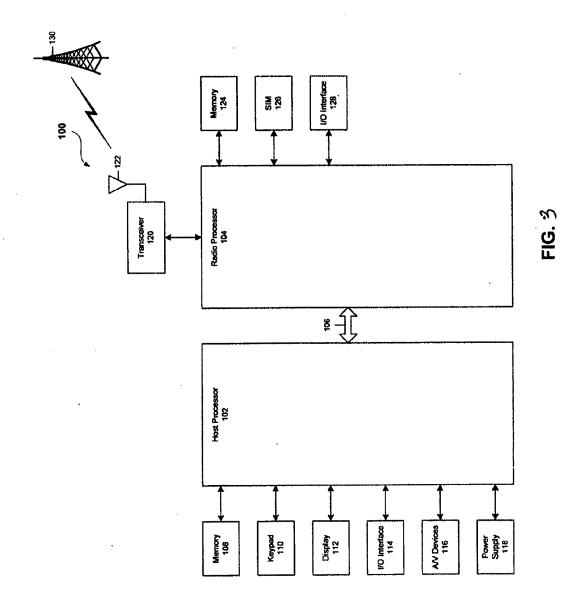
A mobile computing device comprises a first microprocessor having a sleep mode and a wake mode and a second microprocessor configured for wireless communication. A communication port is configured to communicate data from the second processor to the first processor, wherein the second processor is configured to provide a wake signal to the first microprocessor and the first microprocessor is configured to open the communication port in response to the wake signal and to receive data from the second microprocessor through the communication port.

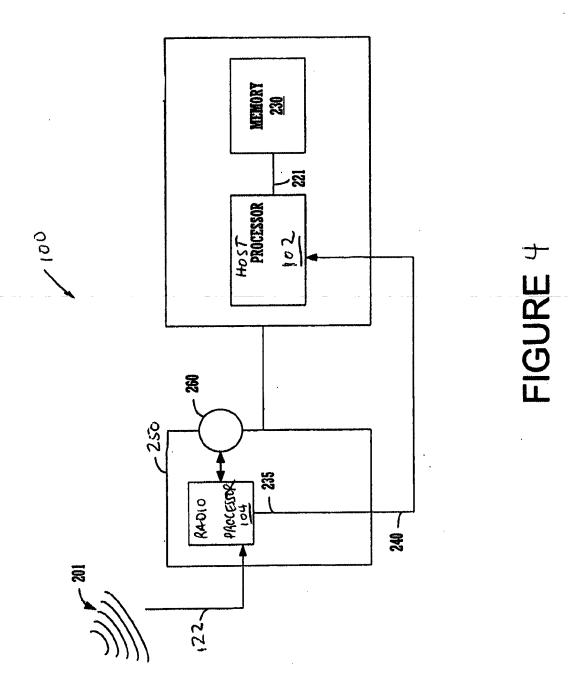




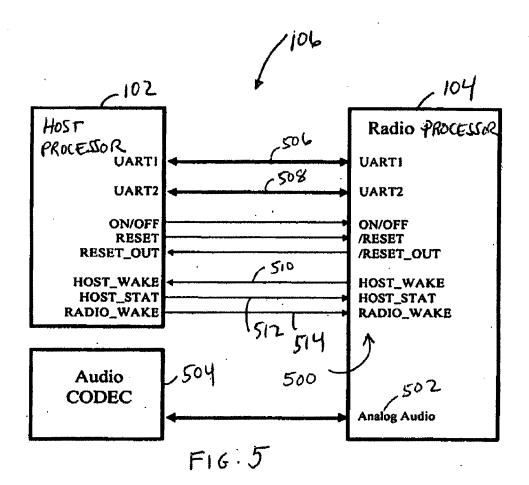












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