UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

Intel Corporation
Petitioner

v.

Qualcomm Incorporated
Patent Owner of U.S. Patent No. 8,838,949
Claims 10-17

Trial No. IPR2018-01335

DECLARATION OF BILL LIN, PH.D.
ON BEHALF OF PETITIONER

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C. "means for processing, by the secondary processor, the image header to determine at least one location within system memory to which the secondary processor is coupled to store each data segment" (claim 16) 36

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D. "means for receiving at the secondary processor, from the primary processor via the inter-chip communication bus, each data segment" (claim 16) 37

1. Function 37

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