

Apple Inc. (Petitioner)

V.

Qualcomm Incorporated (Patent Owner)

Demonstratives
Trial Nos. IPR2018-01315 and -01316
U.S. Patent No. 8,063,674

Before Hon. Trevor M. Jefferson, Daniel J. Galligan, and Scott B. Howard,
Administrative Patent Judges



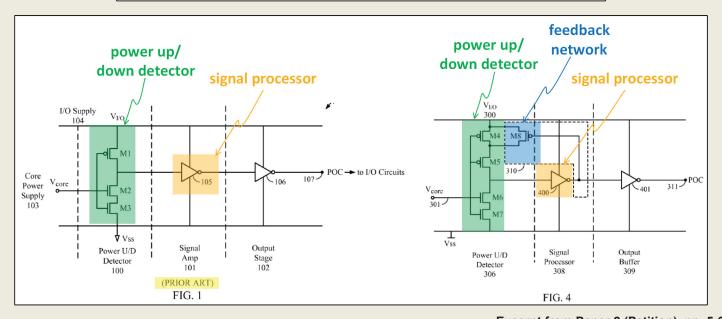
Background and Summary of Issues

- Issue 1: AAPA + Majcherczak
 - Explicit and Art-Specific Motivation to Combine
 - No Teaching Away
 - AAPA Is Eligible
- Issue 2: Steinacker, Doyle, and Park
 - Explicit and Art-Specific Motivation to Combine
 - No Teaching Away



Alleged Innovation of the '674 Patent

1:57-62. The main difference between this prior art POC system 10 and the purported invention of the '674 Patent is the addition of a feedback network 310. APPLE-1003, ¶ 60. A comparison of FIG. 1 and FIG. 4 illuminates this straightforward difference. *Id*.



Excerpt from Paper 2 (Petition), pp. 5-6



Independent Claim 1 of the '674 Patent

1. A multiple supply voltage device comprising:

a core network operative at a first supply voltage; and

a control network coupled to said core network wherein said control network is configured to transmit a control signal, said control network comprising: an up/down (up/down) detector configured to detect a power state of said core network; processing circuitry coupled to said up/down detector and configured to generate said control signal based on said power state;

one or more feedback circuits coupled to said up/down detector, said one or more feedback circuits configured to provide feedback signals to adjust a current capacity of said up/down detector;

at least one first transistor coupled to a second supply voltage, the at least one more first transistor being configured to switch on when said first supply voltage is powered down and to switch off when said first supply voltage is powered on;

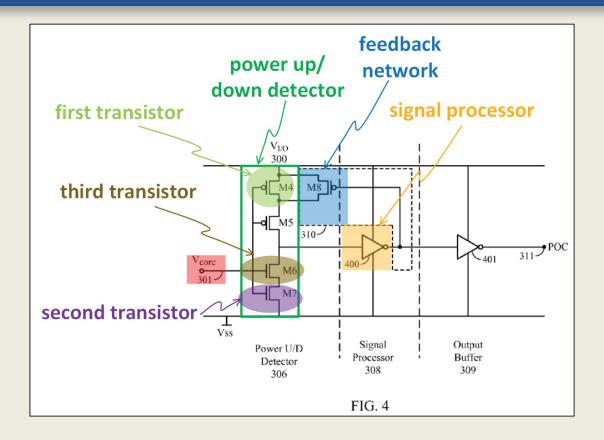
at least one second transistor coupled in series with the at least one first transistor and coupled to said first supply voltage, the at least one second transistor being configured to switch on when said first supply voltage is powered on and to switch off when said first supply voltage is powered down;

at least one third transistor coupled in series between the at least one first transistor and the at least one second transistor.

APPLE-1001, 8:44-9:3 (cited at Paper 7, pp. 6-7).



Figure 4 of the '674 Patent



See Paper 2, pp. 6, 56.



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