IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of: Chang Kwon, et al.

U.S. Patent No.: 8,063,674 Attorney Docket No.: 39521-0053IP1

Issue Date: November 22, 2011

Appl. Serial No.: 12/365,559

Filing Date: February 4, 2009

Title: MULTIPLE SUPPLY-VOLTAGE POWER-UP/DOWN

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PETITION FOR INTER PARTES REVIEW OF UNITED STATES PATENT NO. 8,063,674 PURSUANT TO 35 U.S.C. §§ 311–319, 37 C.F.R. § 42



TABLE OF CONTENTS

I.	RE	QUIREMENTS FOR IPR UNDER 37 C.F.R. § 42.104	1
	A.	Grounds for Standing Under 37 C.F.R. § 42.104(a)	1
	B.	Challenge Under 37 C.F.R. § 42.104(b) and Relief Requested	1
II.	SU	MMARY OF THE '674 PATENT	3
	A.	Brief Description	3
	B.	Summary of the Prosecution History of the '674 Patent	8
	C.	Claim Construction under 37 C.F.R. §§ 42.104(b)(3)	9
		1. "processing circuitry" (claim 1)	10
III.	MA	NNER OF APPLYING CITED PRIOR ART TO EVERY CLAIM FO	R
	WI	IICH AN IPR IS REQUESTED, THUS ESTABLISHING A	
		ASONABLE LIKELIHOOD THAT AT LEAST ONE CLAIM OF TH	E
	'67	4 PATENT IS UNPATENTABLE	10
	A.	[GROUND 1] – Steinacker in view of Doyle and Park Render Claims	1,
		2, and 5-7 Obvious	11
		1. Overview of Steinacker	11
		2. Overview of Doyle	13
		3. Overview of Park	
		4. The Combination of Steinacker, Doyle, and Park	17
		5. Motivation to Combine Steinacker, Doyle, and Park	21
		6. Claim 1	22
		7. Claim 2	33
		8. Claim 5	35
		9. Claim 6	36
		10. Claim 7	36
	В.	[GROUND 2a] – Applicant's Admitted Prior Art in view of	
		Majcherczak Renders Claims 1, 2, 5, and 6 Obvious	
		1. Overview of AAPA	
		2. Overview Majcherczak	
		3. Combination of AAPA and Majcherczak	
		4. Motivation to Combine AAPA and Majcherczak	
		5. Claim 1	
		6. Claim 2	
		7. Claim 5	
		8. Claim 6	
	C .	[GROUND 2b] - AAPA, Majcherczak, and Matthews Render Claim 7	
		Obvious	62



Attorney Docket No. 39521-0053IP1 IPR of U.S. Patent No. 8,063,674

IV.	CONCLUSION	64
V.	MANDATORY NOTICES UNDER 37 C.F.R § 42.8(a)(1)	64
	A. Real Party-In-Interest Under 37 C.F.R. § 42.8(b)(1)	
	B. Related Matters Under 37 C.F.R. § 42.8(b)(2)	
	C. Lead And Back-Up Counsel Under 37 C.F.R. § 42.8(b)(3)	
	D. Service Information	



EXHIBITS

APPLE-1001	U.S. Patent No. 8,063,674 to Kwon et al. ("the '674 patent")
APPLE-1002	Excerpts from the Prosecution History of the '674 Patent ("the Prosecution History")
APPLE-1003	Declaration of Dr. Robert Horst
APPLE-1004	Curriculum Vitae of Dr. Robert Horst
APPLE-1005	U.S. Patent No. 7,279,943 to Steinacker ("Steinacker")
APPLE-1006	U.S. Patent No. 4,717,836 to Doyle ("Doyle")
APPLE-1007	Jun Cheol Park and Vincent J. Mooney, <i>Sleepy Stack Leakage Reduction</i> , 14 IEEE Transactions On Very Large Scale Integration (VLSI) Systems 1251 (2006) ("Park")
APPLE-1008	U.S. Pat. Appl. Pub. No. 2002/0163364 to Majcherczak et al. ("Majcherczak")
APPLE-1009	U.S. Patent No. 6,646,844 to Matthews ("Matthews")
APPLE-1010	G. W. Griffiths, "A Review of Semiconductor Packaging and Its Role in Electronics Manufacturing," 8th IEEE/CHMT Inter- national Conference on Electronic Manufacturing Technology Symposium (1990)
APPLE-1011	Wang-Chang Albert Gu, "RF Front-End Modules in Cellular Handsets," 2004 IEEE Compound Semiconductor Integrated Circuit Symposium (2005)
APPLE-1012	Kaushik Roy <i>et al.</i> , "Leakage current mechanisms and leakage reduction techniques in deep-submicrometer CMOS circuits," 91 Proceedings of the IEEE 2, pp. 305-327 (Apr. 2003) ("Roy")



APPLE-1013	Yangyang Ye <i>et al.</i> , "A new technique for standby leakage reduction in high-performance circuits," 1998 Symposium on VLSI Circuits. Digest of Technical Papers (Cat. No.98CH36215), Honolulu, HI, USA, 1998, pp. 40-41 ("Borkar")
APPLE-1014	U.S. Patent No. 7,049,865 to Parker et al. ("Parker")
APPLE-1015	Qadeer A. Khan <i>et al.</i> , "A Sequence Independent Power-on-Reset Circuit for Multi-Voltage Systems," 2006 IEEE International Symposium on Circuits and Systems (Sep. 2006)
APPLE-1016	Declaration of Jacob Munford (with attachments)



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