

APPENDIX A

CURRICULUM VITA

MASSOUD PEDRAM

Personal Information

Charles Lee Powell Chair in Electrical Engineering and Computer Science
University of Southern California
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Impact on the Field

- Advancing the theory and practice of energy-efficient computing and introducing methodologies, design solutions, and automation algorithms for reducing power dissipation in electronic circuits and systems, wireless ad hoc networks, computation/communication tradeoffs, wired and wireless routing protocols, and modeling/design of electrical energy storage systems, and power conversion circuitry

Research Interests

- Energy-efficient computing, power-aware design methodologies and techniques, power/timing modeling and analysis in VLSI circuits, power/thermal management and control, power conversion and regulation, smart grid technologies, electrical energy storage systems, embedded systems, power-aware wireless networks and mobile ad hoc networks, cloud computing and data center resource provisioning and management, beyond-CMOS devices and circuits, superconductive electronics

Education

- University of California at Berkeley, Ph.D. in Electrical Engineering and Computer Sciences, 1991
- University of California at Berkeley, M.S. in Electrical Engineering and Computer Sciences, 1989
- California Institute of Technology, B.S. in Electrical Engineering, 1986

Employment History

- Charles Lee Powell Chair in Electrical Engineering and Computer Science in the Viterbi School of Engineering, University of Southern California, 2017-present
- Stephen and Etta Varra Professor in the Viterbi School of Engineering, University of Southern California, 2013-2017

- Professor, Department of Electrical Engineering, University of Southern California, 2000-2012
- Associate Professor, Department of Electrical Engineering, University of Southern California, 1996-2000
- Assistant Professor, Department of Electrical Engineering, University of Southern California, 1991-1996
- Part-time Research Position, Xerox Palo Alto Research Center, 1987-1989
- Graduate Student Researcher, Department of Electrical Engineering and Computer Sciences, University of California at Berkeley, 1986-1991

Honors and Awards

- Best Paper Award of the *Asia and South Pacific Design Automation Conference*, Jan. 2019
- USC Viterbi School of Engineering Senior Research Award, 2017
- "For contributions to modeling and design of low power VLSI circuits and systems, and energy efficient computing," *IEEE Circuits and Systems Society Charles A. Desoer Technical Achievement Award*, 2015
- The *Second Most Prolific and Second Most Cited Author Awards* from the 20th Anniversary *Int'l Symp. on Low Power Electronics and Design*, Rome, Italy, Jul. 2015
- *Frequent Author Award* (Top Three Author Award) at the 20th Anniversary *Asia and South Pacific Design Automation Conference*, Chiba/Tokyo, Japan, Jan. 2015
- ACM SIGDA *Distinguished Service Award*, 2014
- Best Paper Award of the *ACM/IEEE Int'l Symp. on Low Power Design and Electronics*, Aug. 2014
- Best Paper Award of the IEEE Computer Society Annual Symp. on VLSI, Jul. 2014
- Recognized as one of the four DAC Prolific Authors (with 50+ papers) and the DAC Bronze Cited Author at the 50th anniversary of the Design Automation Conf., Austin, TX, Jun. 2013
- 2013 DAC Volunteer Service Award sponsored by IEEE CEDA
- Best Paper Awards of the 1989, 2000, and 2012 IEEE Int'l Conference on Computer Design, VLSI in Computers and Processors
- ACM Distinguished Scientist, 2008
- ACM SIGDA Service Award, 2004
- "For contributions to the theory and practice of low-power design and CAD," IEEE Fellow, 2001
- ACM SIGDA Distinguished Service Award, 2000
- Presidential Early Career Award for Scientists and Engineers, 1996
- NSF's Young Investigator Award, 1994
- NSF's Research Initiation Award, 1992
- 2007 IEEE Trans. on Circuits and Systems Guillemin-Cauer Best Paper Award
- Honorable Mention Award at the 2008 Int'l Symp. on Low Power Electronics and Design
- Best Paper Awards of the 33rd ACM/IEEE Design Automation Conference, Jun. 1996 and the 42nd ACM/IEEE Design Automation Conference, Jun. 2005
- Distinguished Paper Citation of the IEEE Int'l Conference on Computer Aided Design, 1990
- 1996 IEEE Circuits and Systems Society VLSI Systems Transactions Best Paper Award

University Service

- Member of the Computer Engineering Faculty Recruitment Committee, 2010-2011, 2013/2014, 2014/2015, 2016-2017
- Member of the VSoE Research Committee at USC, Sept. 2013 - 2017
- Member of the VSoE Transformative Faculty Hire Committee, Sept. 2013 - 2016
- Member of the Ming Hsieh Institute's Advisory Council, Jul. 2013 – Dec. 2015
- Chair of a faculty Promotion Committee in the EE department, 2014/2015, 2016-2017
- Member of the EE Dept. Faculty Recruitment Committee, 2014/2015
- Served on the Best Dissertation Selection Committee in the EE Dept., 2013
- Internal reviewer for an NSF Career Program proposal, 2013
- Chaired committee for evaluating Janet Roveda's Recruitment as an associate professor, 2012
- Division Director of USC Computer Engineering Division, Dec. 2005-Jun. 2010, and Acting Division Director at the end of 2012 and early 2013
- Served as the Chair of the 2005 Distinguished Lecture Series of the EE Dept.
- Served as the Associate Director of the CENG division of the EE Department at USC from 1998 to 2001
- Served on various departmental committees including the Faculty Merit Review Committee, Faculty Recruitment Committee, and the Promotion Committees in the EE department
- Chaired the Outstanding Research Paper Award Committee of the EE-Systems Department at USC in 1999
- Served on the Graduate Student Admissions Committee for entering M.S. and Ph.D. students in the CENG division of the EE-Systems Department at USC

Professional Service

- Participated in and contributed to the Computing Community Consortium (CCC) "Next Steps in Quantum Computing: Computer Science's Role," May 22-23, 2018
- Associate Editor of *Research, A Science Partner Journal*, 2018-present
- Served on the IEEE CASS Fellow (Selection) Committee, 2018
- Executive Committee Chair of the *Int'l Symp. on Low Power Electronics and Design*, July 2012 - present
- Editor-in-Chief of the *ACM Trans. on Design Automation of Electronic Systems*, Jun. 2008–May 2014
- Inaugural Editor-in-Chief of the *IEEE Journal on Emerging and Selected Topics in Circuits and Systems*, Jan. 2010–Dec. 2013
- Served as a member of the 2011 Selection Committee for the ACM SIGDA Outstanding New Faculty Award
- Served as the Chair of the Selection Committee for the IEEE CEDA and ACM SIGDA 2010 *A. Richard Newton Technical Impact Award in Electronic Design Automation*
- General Co-chair of the 2010 *IEEE Int'l Symp. on Circuits and Systems*, Paris, France
- Associate Editor
 - Foundations and Trends in Electronic Systems and Circuits Journal, Now Publishers, Inc. of Delft, 2006-2014
 - Journal of Low Power Electronics, 2005-2014
 - IEEE Trans. on Computer Aided Design, 1997-2006

- IEEE Trans. on Circuits and Systems I, 2002-2003
- ACM Trans. on Design Automation of Electronic Systems, 1998-2004
- Distinguished Lecturer/Speaker
 - Association for Computing Machinery (ACM), 2007-2015
 - IEEE Solid State Circuits Society, 2001-2007
- VP of Publications of the IEEE Circuits and Systems Society, 2005-2006
- Chair of the Distinguished Lecturer Program, IEEE Circuits and Systems Society, 2003-2004
- Member of the Board of Governors, IEEE Circuits and Systems Society, 2000-2003
- Member of the ACM-SIGDA Advisory Board, managing the ACM Outstanding Ph.D. Dissertation Award in Electronic Design Automation, the DAC Young Student Support Program, the Multimedia Monograph Series, and the SIGDA Technical Committees, 1996-2009
- Chair of the Best Paper Award Committee for the IEEE Trans. on Computer Aided Design, 1999
- Member of the Research or Technical Advisory Board
 - Atrenta Inc., 2003-2015
 - Envis Inc., 2006-2009
 - PwrLite Inc., 2007-2009
 - Fulcrum Microsystems, 2001-2004
 - Aplus Design Technologies Inc., 2000-2003
 - Magma Design Automation Inc., 1997-2002
 - EPIC Design Technology, 1994-1997
- General Chair
 - *Int'l Symp. on Quality of Electronic Design*, San Jose, CA, Apr. 2007
 - *Int'l Symp. on Physical Design*, Monterey, CA, Apr. 2003
 - *Int'l Symp. on Low Power Electronics and Design*, Monterey, CA, Aug. 1997
 - *Int'l Symp. on Low Power Design*, Laguna, CA, Apr. 1995
- Technical Chair
 - *Int'l Symp. on Physical Design*, Del Mar, CA, Apr. 2002
 - *Int'l Symp. on Low Power Electronics and Design*, Monterey, CA, Aug. 1996
 - *Int'l Workshop on Low Power Design*, Napa Valley, CA, Apr. 1994
- Executive Committee Member
 - *Int'l Symp. on Low Power Electronics and Design*, since 1996
 - *Int'l Conference on Computer Aided Design*, since 2006
- Technical Program Committee Member
 - *The 18th CSI International Symposium on Computer Architecture & Digital Systems*, Tehran, Iran, Oct. 7-8, 2015
 - *Int'l Conference on Hardware/Software Codesign and System Synthesis*, 2013-2015
 - *Symp. on Embedded Systems for Real-Time Multimedia (ESTIMedia)*, 2007-2012
 - *Symp. on VLSI Design, Automation and Test (VLSI-DAT)*, 2012
 - 2009 *Great Lakes Symp. on VLSI*
 - 2008 IEEE Int'l Design and Test Workshop, Tunisia
 - *Symp. on VLSI Design, Automation and Test (VLSI-DAT)*, Hsinchu, Taiwan, 2009
 - NanoArch, 2007-2008
 - *The 13th CSI Computer Conference (CSICC'08)*, Kish Island, Iran, March 9-11, 2007
 - Conference on VLSI – *Design Automation Technologies*, Taipei, Taiwan, 2006-2007
 - 1998-2000 *Int'l Conference on Computer Aided Design*
 - 1998-2001 *Int'l Symp. on Physical Design*
 - 1997-2000 *Int'l Workshop on Logic Synthesis*
 - 2000 *Asia and South Pacific Design Automation Conference*

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