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UNITED STATES PATENT AND TRADEMARK OFFICE
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 2
              BEFORE THE PATENT TRIAL AND APPEAL BOARD
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 4
        APPLE INC.,
                   Petitioner,
 5
                                        Case IPR2018-01249
 6
                                        Patent 7, 693, 002
 7
        QUALCOMM INCORPORATED,
 8
                   Patent Owner.
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14
                DEPOSITION OF MASSOUD PEDRAM, PH.D.
15
                       LOS ANGELES, CALIFORNIA
16
                        THURSDAY, JUNE 6, 2019
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        DORIEN SAITO, CSR 12568, CLR
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                                        Case IPR2018-01249
                                        Patent 7, 693, 002
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                   Patent Owner.
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12
                  DEPOSITION OF MASSOUD PEDRAM, taken
13
                  on behalf of PETITIONER, at 555
14
                  South Flower Street, 50th Floor,
15
                  Los Angeles, California 90071,
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                  commencing at 10:01 a.m., Thursday,
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                  June 6, 2019, before DORIEN SAITO,
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                  CSR 12568, CLR.
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1	I N D E X		
2	WITNESS:		
3	MASSOUD PEDRAM	PAGE	
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9	(NONE)		
10	QUESTIONS INSTRUCTED NOT TO ANSWER:		
11	(NONE)		
12	()		
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1	EXHIBIT	S : (Previously marked.)		
2				
3	NUMBER	DESCRIPTION	PAGE	
4	Apple 1001	U.S. Patent No.:7,693,002 B2	39	
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6	Apple 1005	U.S. Patent No.: 4,951,259	85	
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8	Apple 1006	U.S. Patent Application	85	
9		Publication		
10		No.: US 2006/0098520 A1		
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12	Apple 1007	Excerpt from VLSI Memory	85	
13		Chip Design Book by Kiyoo Itoh		
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