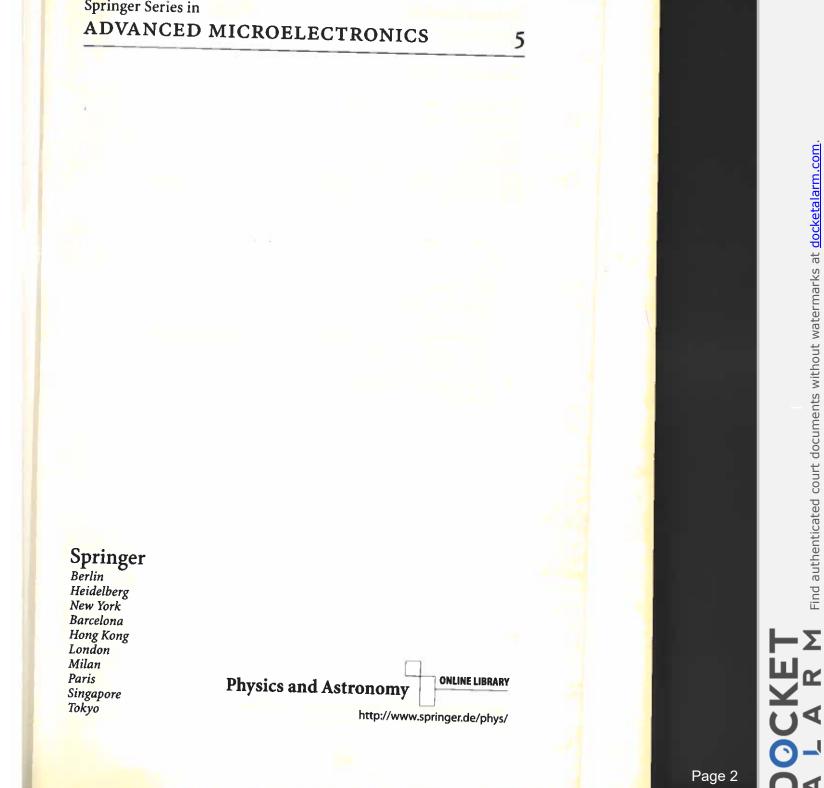
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Kiyoo Itoh VLSI Memory Chip Design



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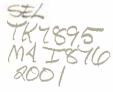


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Dr. Kiyoo Itoh Hitachi Ltd., Central Research Laboratory 1-280, Higashi-Koigakubo Kokubunji-shi Tokyo185-8601 Japan e-mail: k-itoh@crl.hitachi.co.jp



Series Editors:

Dr. Kiyoo Itoh Hitachi Ltd., Central Research Laboratory 1-280 Higashi-Koigakubo Kokubunji-shi Tokyo 185-8601 Japan

Professor Takayasu Sakurai Center for Collaborative Research University of Tokyo 7-22-1 Roppongi, Minato-ku, Tokyo 106-8558 Japan

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