

## United States Patent [19]

### Ohsawa

[54] MIII TIDI EVED

4.280.212

4,645,944

## **Patent Number:**

5,870,340

Date of Patent: [45]

Feb. 9, 1999

| [54] | MULTIPLEXER   |  |  |
|------|---|--|--|
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| [73] | Assignee:   | Kabushiki Kaisha Toshiba, Kawasaki,<br>Japan |  |
| [21] | Appl. No.: 889,441  |  |  |
| [22] | Filed:  | Jul. 8, 1997                                 |  |
|      | Rel   | ated U.S. Application Data                   |  |
| [62] | Division of Ser. No. 763,036, Dec. 10, 1996, Pat. No. 5,701,095, which is a continuation of Ser. No. 393,076, Feb. 23, 1995, abandoned. |  |  |
| [30] | Forei   | gn Application Priority Data                 |  |
| Feb. | 25, 1994  | [JP] Japan 6-028593                          |  |
| [51] | Int. Cl.6   | G11C 7/00                                    |  |
| [52] | U.S. Cl   |  |  |
| [58] | Field of S  | earch 365/189.02, 201,                       |  |
|      |   | 365/203, 189.05, 190                         |  |
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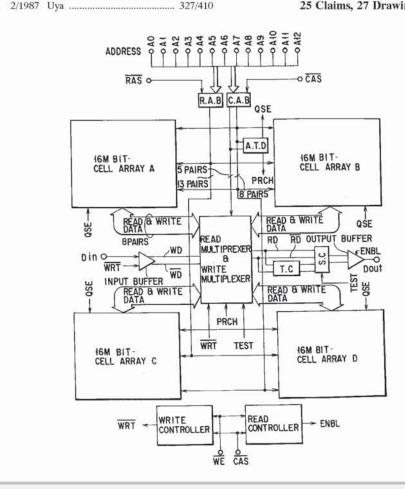
0 593 152 4/1994 European Pat. Off. . 0 615 251 9/1994 European Pat. Off. .

Primary Examiner—Tan T. Nguyen Attorney, Agent, or Firm-Banner & Witcoff, Ltd.

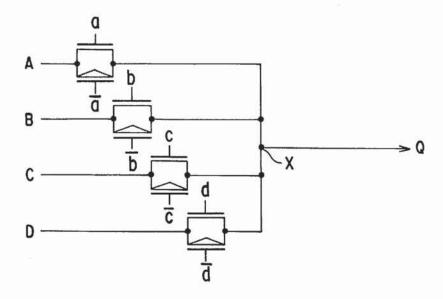
#### ABSTRACT

A semiconductor integrated circuit device has a data selecting circuit connected to a first power supply terminal, a precharge circuit, connected to a second power supply terminal, for receiving a precharge signal, and a wiring line connected to a common connection point between the data selecting circuit and the precharge circuit. The data selecting circuit includes at least two, i.e., first and second data transmission circuits. A first input data signal and a first selecting signal are supplied to the first data transmission circuit. A second input data signal and a second selecting signal are supplied to the second data transmission circuit.

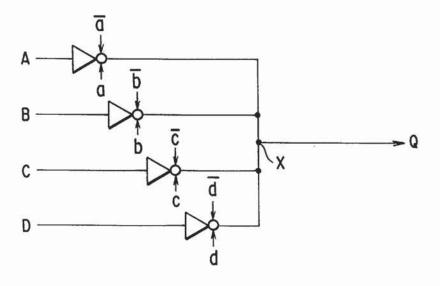
### 25 Claims, 27 Drawing Sheets



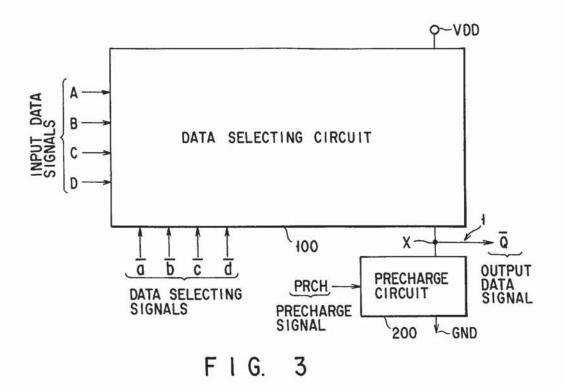


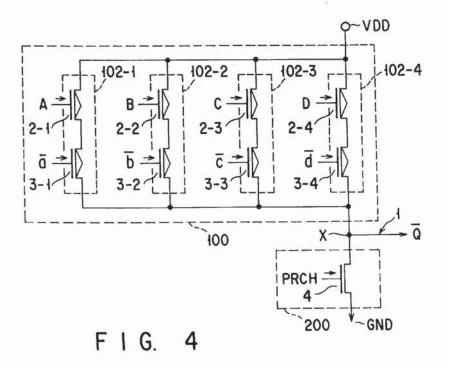


F I G. 1 (PRIOR ART)

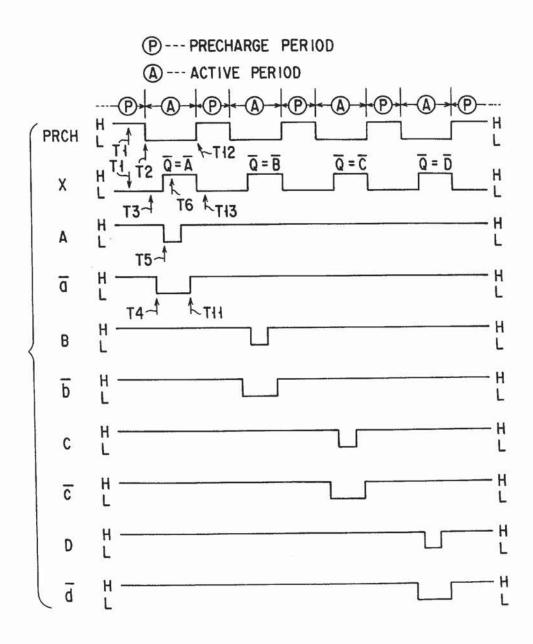


F I G. 2 (PRIOR ART)



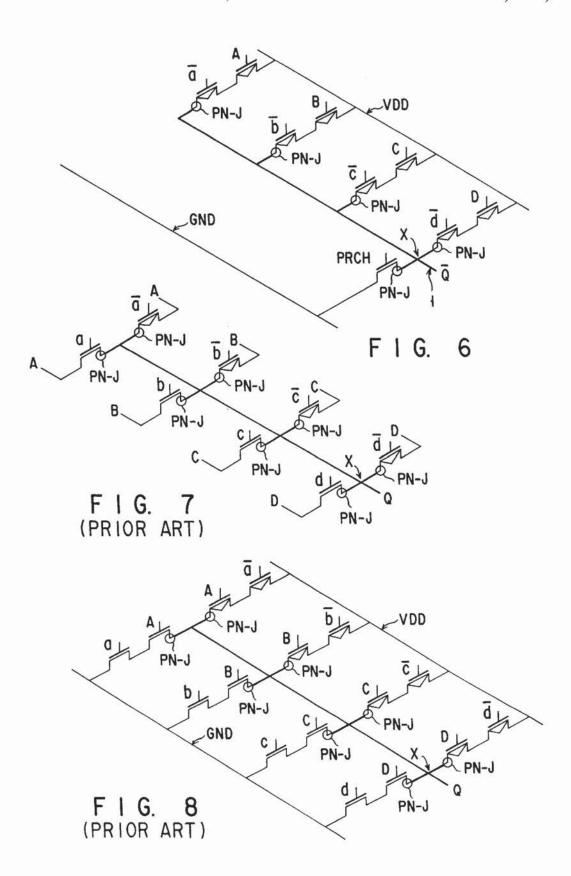






F I G. 5







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