

**Intel Corporation**  
**v.**  
**Qualcomm Incorporated**

IPR2018-01152

IPR2018-01153

IPR2018-01154

IPR2018-01240

U.S. Patent No. 8,698,558

**Patent Owner's Demonstrative Exhibits**

# U.S. Patent No. 8,698,558

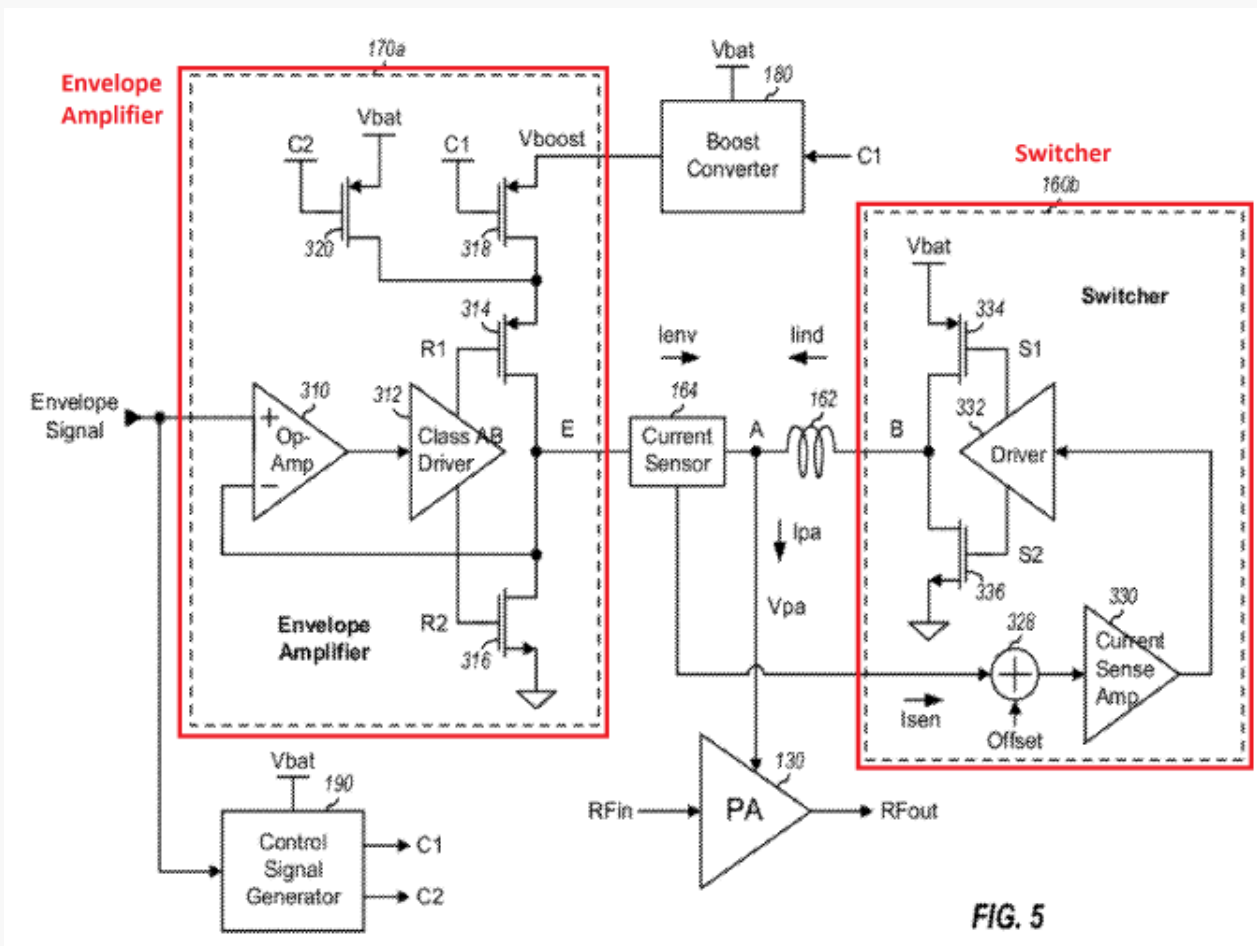


FIG. 5

# Contested Grounds

## I. SWITCHER GROUNDS

- IPR2018-01154, Ground I: Claims 15, 17-18, and 20 as anticipated by Kwak
- IPR2018-01154, Ground II: Claim 16 as obvious in view of Kwak

## II. ENVELOPE AMPLIFIER GROUNDS

- IPR2018-01153, Ground I: Claims 6 and 8 as obvious over Chu combined with Choi
- IPR2018-01153, Ground II: Claims 1-9 as obvious over Chu combined with Choi and Myers
- IPR2018-01240, Ground I: Claim 10 as obvious over Chu combined with Choi and Hanington
- IPR2018-01240, Ground II: Claim 11 as obvious over Chu combined with Choi, Hanington and Myers
- IPR2018-01152, Ground III: Claim 13 as obvious over Chu combined with Choi
- IPR2018-01152, Ground IV: Claim 13 as obvious over Chu combined with Choi and Myers
- IPR2018-01154, Ground III: Claim 19 as obvious over Kwak combined with Choi

IPR2018-01152, Paper 16 at 19-44;

IPR2018-01153, Paper 16 at 19-49;

IPR2018-01154, Paper 16 at 17-43;

IPR2018-01240, Paper 16 at 21-50.

# Patentability

## I. SWITCHER GROUNDS

- Petitioner Improperly Combines Two Different Embodiments from Kwak
- Kwak Does Not Disclose Adding an Offset to Generate a Larger Supply Current via the Inductor

## II. ENVELOPE AMPLIFIER GROUNDS

- The Claims Require a Selective Boost
- The POSA Would Not Combine Myers with Choi
- The POSA Would Not Combine Chu and Choi

IPR2018-01152, Paper 16 at 19-44;

IPR2018-01153, Paper 16 at 19-49;

IPR2018-01154, Paper 16 at 17-43;

IPR2018-01240, Paper 16 at 21-50.

# Patentability

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