MOST®

Media **O**riented **S**ystems **T**ransport

**Multimedia and Control
Networking Technology**

OS8104
Preliminary Product Data Sheet
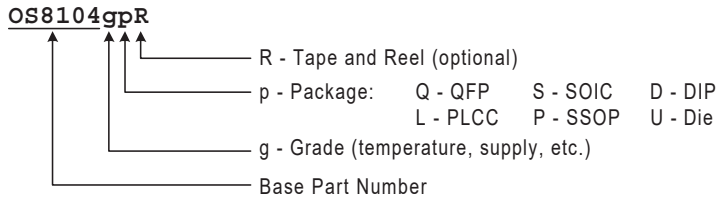
DS8104PP2

Sept. 2000

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Ordering Information

OS8104gpR

- R - Tape and Reel (optional)
- p - Package: Q - QFP   S - SOIC   D - DIP
                        L - PLCC   P - SSOP   U - Die
- g - Grade (temperature, supply, etc.)
- Base Part Number

Valid Part Numbers:

Order Number	Grade		Package
	Temperature	Supply	
OS8104AQ	-40 to +85° C	4.5 to 5.5 V	44-pin TQFP
OS8104AQR	-40 to +85° C	4.5 to 5.5 V	44-pin TQFP, Tape and Reel

This table represents parts that were available at the time of printing and may not represent parts that are currently available. For the latest list of valid ordering numbers for this product, please contact your local sales office.

Support and Further Information

For more information on the MOST technology, product line, and custom IC development using MOST tools, contact one of our offices below.

Oasis SiliconSystems AG
1101 S. Capital of Texas Highway
Building B, Suite 101
Austin, Texas 78746 USA

Tel: (+1) 512 306-8450
Fax: (+1) 512 306-8442
america@oasis.com

Oasis SiliconSystems AG
Bannwaldallee 48
D-76185 Karlsruhe
Germany

Tel: (+49) (0) 721 6 25 37 - 0
Fax: (+49) (0) 721 6 25 37 - 119
europe@oasis.de

Oasis SiliconSystems AG
4-16 Oomaru Tsuzuki-ku
Yokohama 224-0061, Japan

Tel: (+81) (90) 2757 1419
Fax: (+81) (45) 941 7818
pacrim@oasis.de

Technical Support

For technical support please refer to one of the following email addresses:
        support@oasis.de
        support@oasis.com

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Patents

There are a number of patents and patents pending on the MOST technology. The rights to these patents are not granted without any specific Agreement between the users and the patent owners.

Preliminary Product Data Sheet

A Preliminary Product Data Sheet describes a product which is in limited production and subject to change. Oasis SiliconSystems has worked diligently to ensure that the information in this document is accurate and reliable. However, the information in this document is subject to change without notice and is provided "as is" without warranty of any kind (expressed or implied).

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Conventions

Within this manual, the following abbreviations and symbols are used for improving readability:

	Comment
PIN	Name of a PIN
BIT	Name of a single bit within a register
x..y	Range from x to y. Used as abbreviation e.g. for a group of bits like *D7..0*
[a,b,c]	List of alternative elements to choose from.
0xzz	Hexadecimal number (value zz)
bREG	Single-byte (8-bit) register
wREG	Single-word register (16-bit)
mREG	Multi-byte register (e.g. message buffer)
rsvd	The respective bit or register is reserved for future use
/	Inverter. Attached pin or bit uses inverted logic (low or 0 active)
x	Don't care
↑	Rising edge
↓	Falling edge

Revision History

Revision	Date	Description
1.1		Initial Data Sheet
2	Sept. 2000	Fully revised.

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DOCKET ALARM

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Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

Real-Time Litigation Alerts

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Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

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