

MOST[®]

Media Oriented Systems Transport

**Multimedia and Control
Networking Technology**

OS8104

Preliminary Product Data Sheet

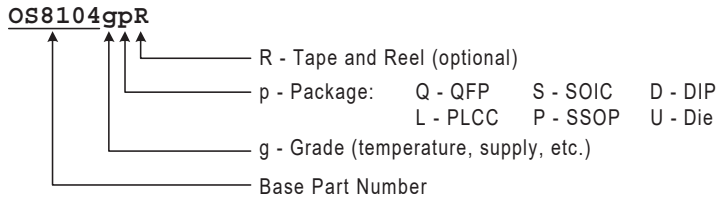
DS8104PP2

Sept. 2000

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Ordering Information



Valid Part Numbers:

Order Number	Grade		Package
	Temperature	Supply	
OS8104AQ	-40 to +85° C	4.5 to 5.5 V	44-pin TQFP
OS8104AQR	-40 to +85° C	4.5 to 5.5 V	44-pin TQFP, Tape and Reel

This table represents parts that were available at the time of printing and may not represent parts that are currently available. For the latest list of valid ordering numbers for this product, please contact your local sales office.

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Preliminary Product Data Sheet

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Conventions

Within this manual, the following abbreviations and symbols are used for improving readability:

	Comment
<i>PIN</i>	Name of a PIN
BIT	Name of a single bit within a register
x..y	Range from x to y. Used as abbreviation e.g. for a group of bits like <i>D7..0</i>
[a,b,c]	List of alternative elements to choose from.
0xzz	Hexadecimal number (value zz)
bREG	Single-byte (8-bit) register
wREG	Single-word register (16-bit)
mREG	Multi-byte register (e.g. message buffer)
rsvd	The respective bit or register is reserved for future use
/	Inverter. Attached pin or bit uses inverted logic (low or 0 active)
x	Don't care
↑	Rising edge
↓	Falling edge

Revision History

Revision	Date	Description
1.1		Initial Data Sheet
2	Sept. 2000	Fully revised.

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