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(12) United States Patent Jochym et al.

(54) ALTERNATIVE TO THROUGH-HOLE-PLATING IN A PRINTED CIRCUIT BOARD

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- Subject to any disclaimer, the term of this (*) Notice: patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.
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- Field of Search 174/265, 267, (58)174/262-264, 266; 29/842, 843, 844, 845; 361/792, 793, 794, 803

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(45) Date of Patent:

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Primary Examiner-David A. Zarneke

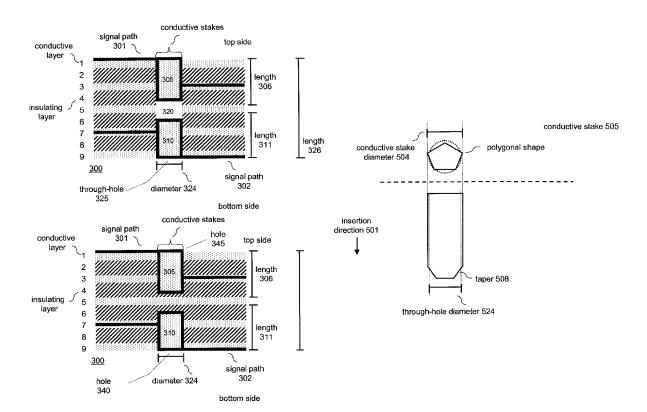
Assistant Examiner-Jeremy Norris

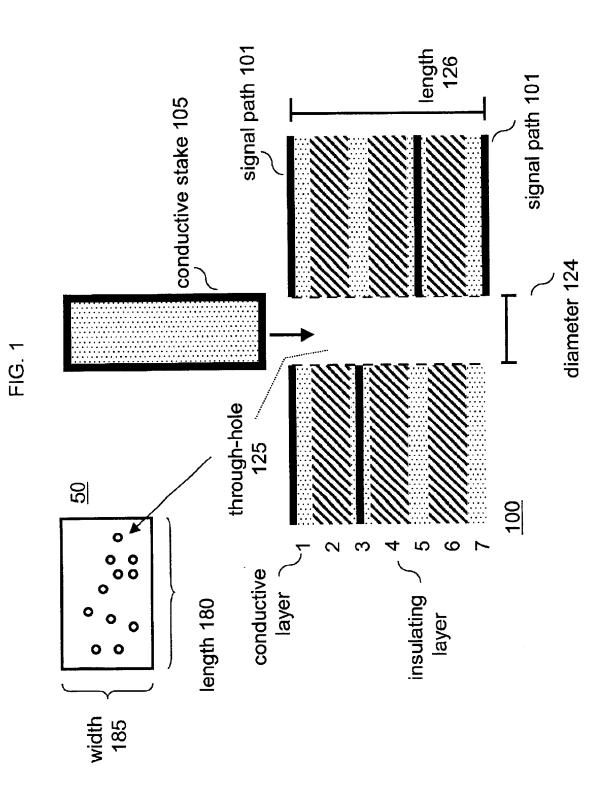
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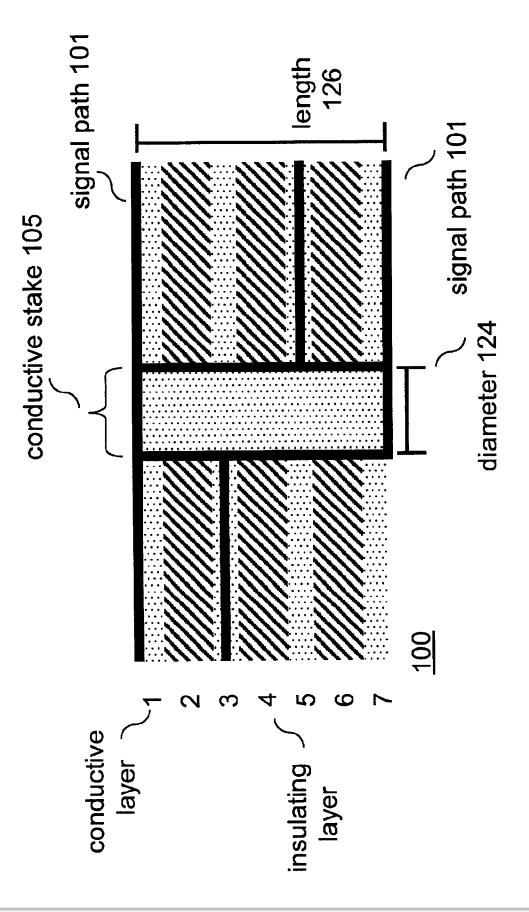
ABSTRACT

A printed circuit board (PCB) comprises a number of electrically conductive layers. Instead of coating, or plating, a PCB through-hole with an electrically conductive material to form a via (for the purpose of connecting together signal paths across the electrically conductive layers)-the via is formed by placing a conductive stake, or conductive pin, in the through-hole.

6 Claims, 10 Drawing Sheets







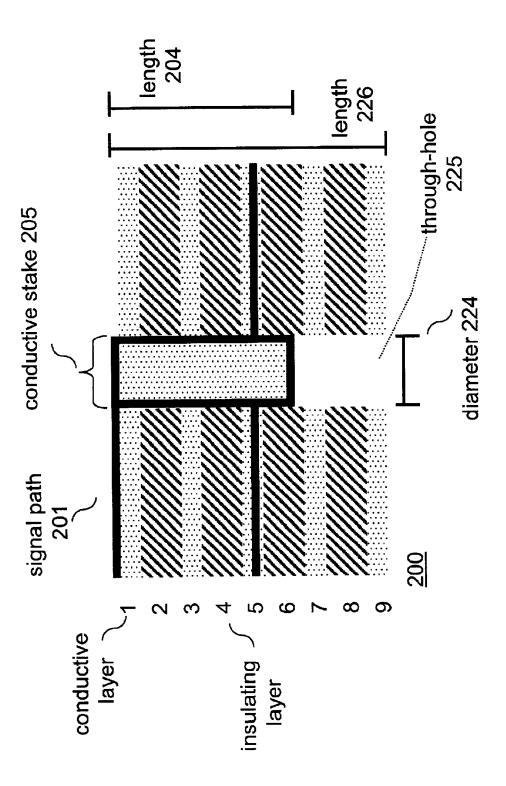


FIG. 3

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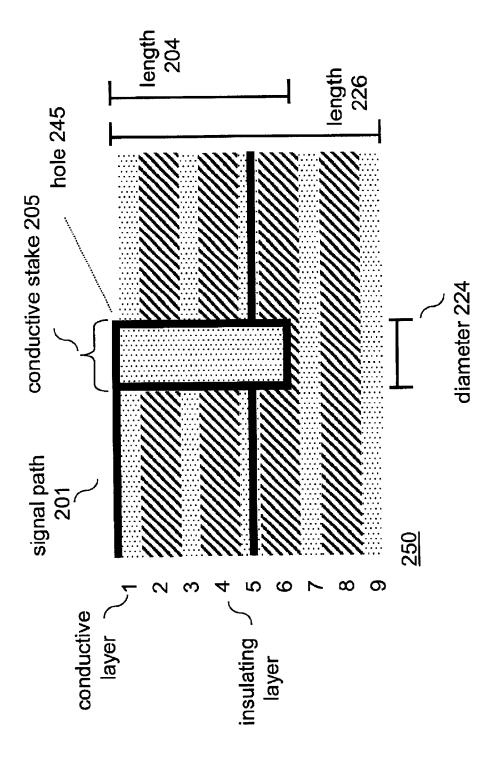


FIG. 4

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