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Filed on behalf of Intel Corporation

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UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

Intel Corporation
Petitioner

v.

Qualcomm Incorporated
Patent Owner

Case IPR2018-01154

PETITIONER'S UPDATED TABLE OF EXHIBITS

Table of Exhibits for U.S. Patent 8,698,558 Petition for *Inter Partes* Review

Exhibit	Description
1201	U.S. Patent No. 8,698,558
1202	File History for U.S. Patent No. 8,698,558
1203	Declaration of Dr. Alyssa B. Apsel
1204	Chu, W.Y. et al., “ <i>A 10 MHz bandwidth, 2 mV ripple PA regulator for CDMA transmitters,</i> ” IEEE Journal of Solid-State Circuits 2809-2819 (2008) (“Chu”)
1205	Declaration of IEEE regarding Chu, Kwak, Kim, and Blanken (“IEEE Chu Decl.”)
1206	Choi, J. et al., “ <i>Envelope tracking power amplifier robust to battery depletion,</i> ” Microwave Symposium Digest (MTT), 2010 IEEE MTT-S International 1332-36 (2010) (“Choi 2010”)
1207	Declaration of IEEE regarding Choi 2010 (“IEEE Choi Decl.”)
1208	Declaration of Debabani Choudhury (“Choudhury Decl.”)
1209	Declaration of Jinsung Choi (“Choi Decl.”) (<i>CORRECTED; Filed July 2, 2018</i>)
1210	Blanken, P.G. et al., “ <i>A 50MHz bandwidth multi-mode PA supply modulator for GSM, EDGE and UMTS application,</i> ” 2008 Radio Frequency Integrated Circuits Symposium (IEEE) 401-04 (2008) (“Blanken”)
1211	Kwak, T.W. et al., “ <i>A 2 W CMOS hybrid switching amplitude modulator for EDGE polar transmitters,</i> ” IEEE Journal of Solid-State Circuits 2666-76 (2007) (“Kwak”)
1212	U.S. Patent No. 5,929,702, “Method and Apparatus for High Efficiency High Dynamic Range Power Amplification,” to Myers et al. (“Myers”)
1213	Kim, D. et al., “High efficiency and wideband envelope tracking power amplifier with sweet spot tracking.” <i>Radio Frequency Integrated Circuits Symposium (RFIC): 255-258</i> (2010) (“Kim”)

1214	U.S. Patent No. 6,300,826, “Apparatus and Method for Efficiently Amplifying Wideband Envelope Signals” (filed May 5, 2000) (“Mathe ’826”)
1215	Maxim Integrated Products, Inc., <i>MAX9738 –16VP-P Class G Amplifier with Inverting Boost Converter</i> , Datasheet 19-3700, Rev. 0 (March 2008) (“Maxim”)
1216	Ertl, H. et al., “Basic considerations and topologies of switched-mode assisted linear power amplifiers,” <i>IEEE Transactions on industrial electronics</i> 44.1 (1997): 116-123 (“Ertl”)
1217	Kang, D. et al., “A multimode/multiband power amplifier with a boosted supply modulator” <i>IEEE Transactions on Microwave Theory and Techniques</i> 58.10 (2010): 2598-2608 (“Kang”)
1218	U.S. Patent No. 5,834,977, “Amplifying Circuit with Power Supply Switching Circuit” (filed October 30, 1996 and issued November 10, 1998) (“Maehara”)
1219	U.S. Patent No. 5,870,340, “Multiplexer” (filed July 8, 1997 and issued February 9, 1999) (“Ohsawa”)
1220	U.S. Patent No. 6,566,935, “Power Supply Circuit With a Voltage Selector” (filed August 28, 2000 and issued May 20, 2003) (“Renous”)
1221	Certificate of Correction for U.S. Patent No. 8,698,558 (“558 COC”)
1222	Qualcomm Incorporated’s Initial Claim Construction Brief, <i>Certain Mobile Electronic Devices and Radio Frequency and Processing Components Thereof</i> , Investigation No. 337-TA-1065 (“Qualcomm CC Brief”)
1223	Order No. 28: Construing Terms of the Asserted Patents, <i>Certain Mobile Electronic Devices and Radio Frequency and Processing Components Thereof</i> , Investigation No. 337-TA-1065 (“Markman Order”)
1224	Razavi, <i>Design of Analog CMOS Integrated Circuits</i> (2001) (“Razavi”) (excerpts)

CERTIFICATE OF SERVICE

I hereby certify that on July 2, 2018 I caused a true and correct copy of the foregoing materials:

- Updated Table of Exhibits for Petition for *Inter Partes* Review of U.S. Patent No. 8,698,558
- Corrected Exhibit 1209

to be served via Express Mail on the following attorney of record as listed on

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