

SONOS, INC. V. IMPLICIT, LLC

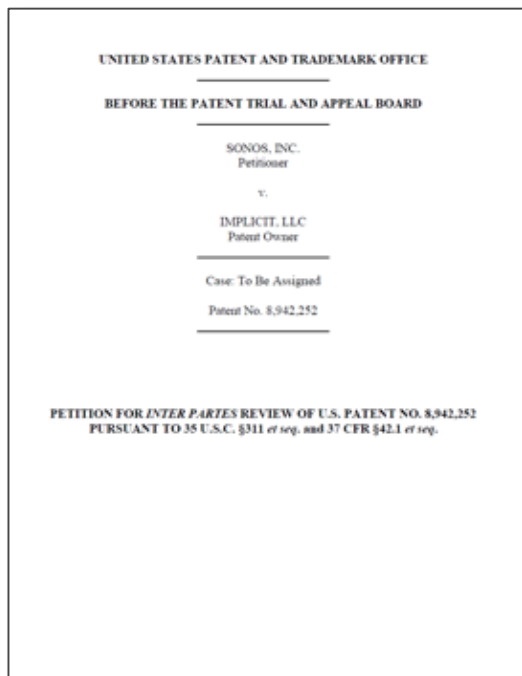
IPR2018-00766

U.S. Pat. No. 7,391,791

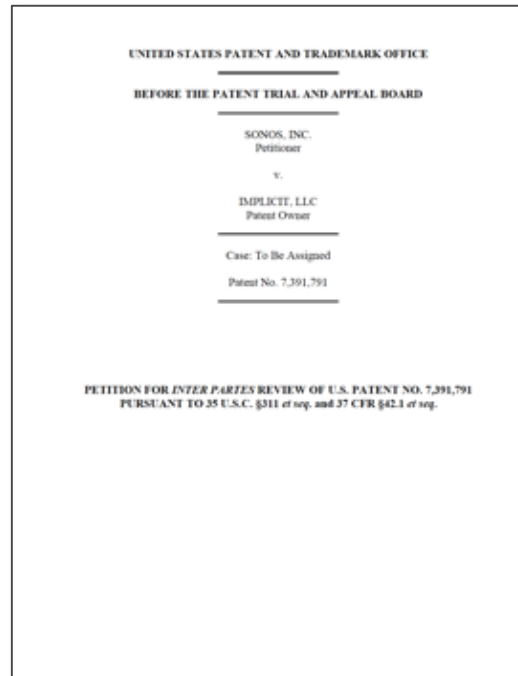
IPR2018-00767

U.S. Pat. No. 8,942,252

SONOS ESTABLISHED UNPATENTABILITY OF EACH CLAIM



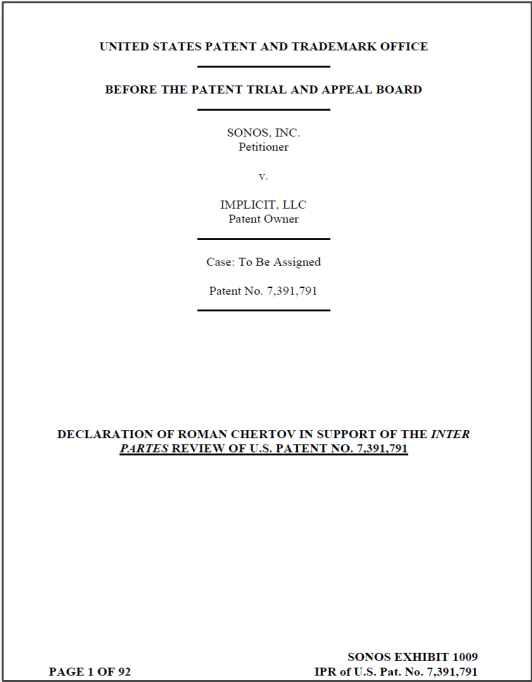
Sonos Petition
'791 Patent



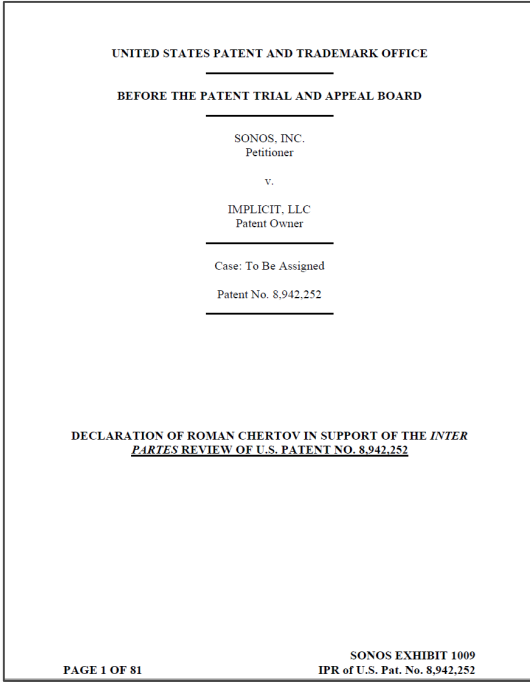
Sonos Petition
'252 Patent

- Janevski anticipates the '791 patent claims
- Janevski + any “clock synchronization” reference renders the '252 patent claims obvious

SONOS'S PETITIONS ARE BASED ON EVIDENCE



Dr. Chertov Declaration
'791 Patent
Ex.1009



Dr. Chertov Declaration
'252 Patent
Ex.1009

➤ Sonos's petitions rely on Dr. Chertov's expert testimony regarding the invalidity of the patents

Implicit's attack on the prior art fails

- It is unsupported attorney-argument
- Prior art invalidates all claims

Implicit's swear-behind defense fails

- It fails legally
- It fails substantively

IMPLICIT FAILED TO REBUT SONOS'S PETITIONS WITH EVIDENCE



Elbit Systems of America, LLC
v. Thales Visionix, Inc.,
881 F.3d 1354, 1359
(Fed. Cir. 2018)

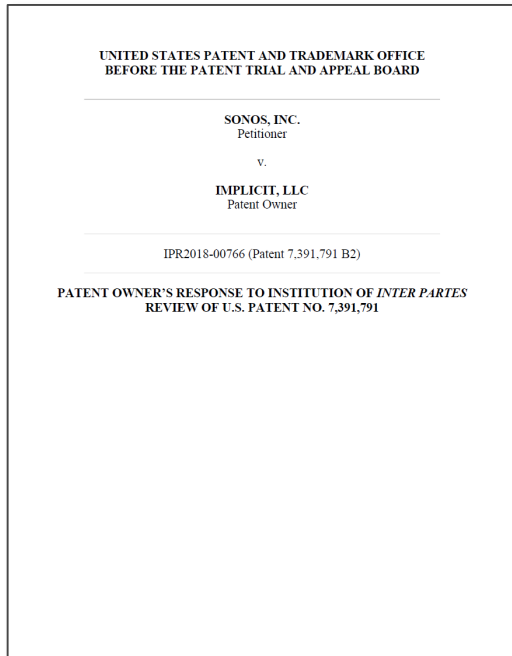
“Elbit fails to present any evidence supporting this contention beyond attorney argument . . .

‘[A]ttorney argument is not evidence’ and cannot rebut other admitted evidence.”

IMPLICIT CHALLENGES JANEVSKI'S DISCLOSURE OF "DEVICE TIME"



23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:



1. Janevski Fails to Disclose the "Device Time" Limitations

Implicit POR
'791 Patent

IPR2018-00766, -00767 EXHIBIT 1028

Implicit POR, pp. 32-33. 6

JANEVSKI DISCLOSES “DEVICE TIME”

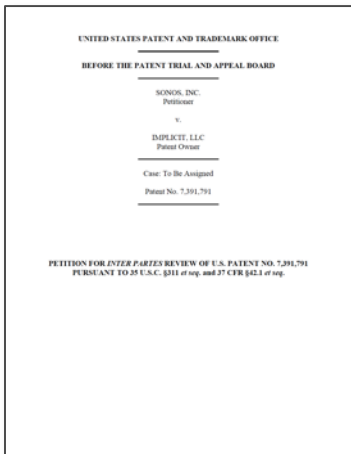


23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:

A. “device time”

The ‘791 Patent states that a rendering device’s “device time” is “the time as indicated by a designated clock (e.g., system clock) of the rendering device.”

Ex.1001, 2:16-17. Consistent with this disclosure, Petitioner proposes that the term “device time” be construed here as “a time indicated by any clock of a given rendering device.”



Sonos Petition
‘791 Patent

JANEVSKI DISCLOSES "DEVICE TIME"



23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:

Each PVR has a "time count" provided by the PVR's "video timer":

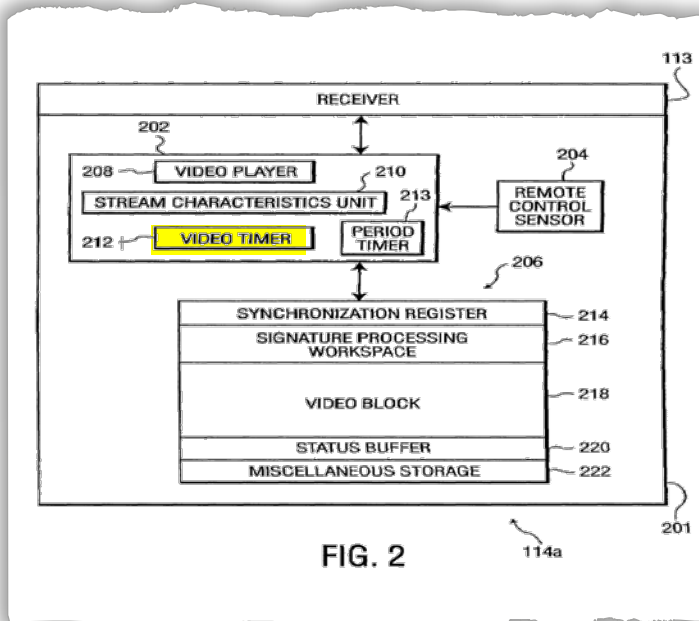


FIG. 2 114a 201

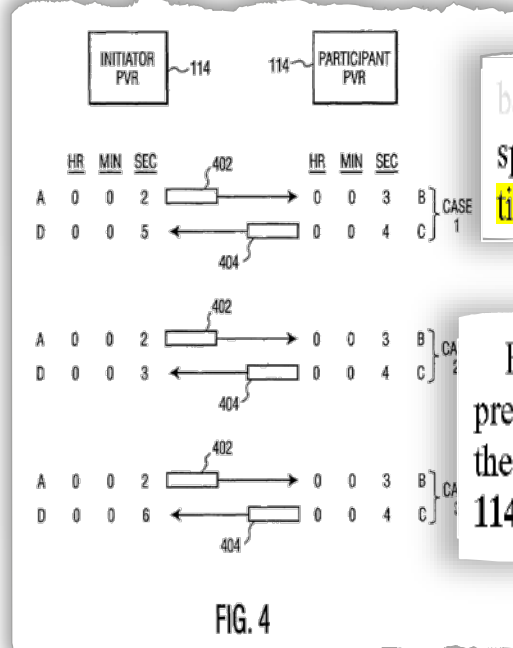


FIG. 4

backs. When any PVR fast forwards or rewinds, this correspondingly and synchronously advances or rolls back the time count of its respective video timer.

FIG. 4 depicts a possible message flow design in the present invention to determine the misalignment, if any, in the respective timings of the video timers 212 of two PVRs 114a, b, so that the timers can be synchronized. For sim-



23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:



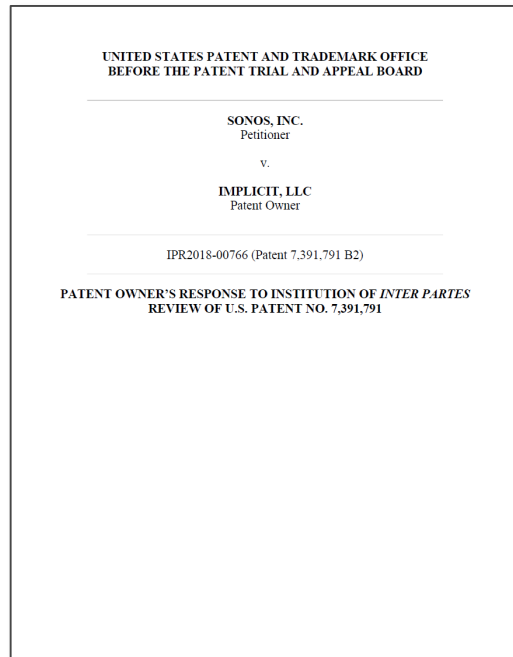
Dr. Roman Chertov
Sonos’s Expert Witness

102. Further, Janevski discloses that each PVR has a “time count” provided by the PVR’s “video timer.” In my opinion, the PVR’s “video timer” amounts to a clock of the PVR, and the “time count” provided by the “video timer” amounts to the claimed “device time” that is in a “time domain” of the PVR. *Id.* at FIGs. 2 & 4, 7:51-62, 8:39-10:3.

IMPLICIT ARGUES THAT OUTPUT OF “VIDEO TIMER” IS NOT “DEVICE TIME”



23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:



The Petitioner has not shown that the video timer data is a device time. The video timer is not linked to any clock of the PVR or a device within the DVR. It is undisputedly not a system clock, and it is noteworthy that Janevski calls it a “timer,” as opposed to a clock. The purpose of a device clock is to synchronize the operations on the device, and Janevski does not disclose that the video timer performs that functionality.

Implicit POR
'791 Patent

IPR2018-00766, -00767 EXHIBIT 1028

Implicit POR, p. 33. 10

JANEVSKI'S "VIDEO TIMER" IS A CLOCK THAT OUTPUTS "DEVICE TIME"

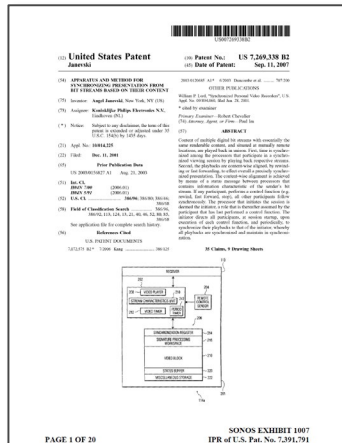


Dr. Roman Chertov
Sonos's Expert Witness

102. Further, Janevski discloses that each PVR has a "time count" provided by the PVR's "video timer." In my opinion, the PVR's "video timer" amounts to a clock of the PVR, and the "time count" provided by the "video timer" amounts to the claimed "device time" that is in a "time domain" of the PVR. *Id.* at FIGs. 2 & 4, 7:51-62, 8:39-10:3.

FIG. 4 depicts a possible message flow design in the present invention to determine the misalignment, if any, in the respective timings of the video timers 212 of two PVRs 114a, b, so that the timers can be synchronized. For sim-

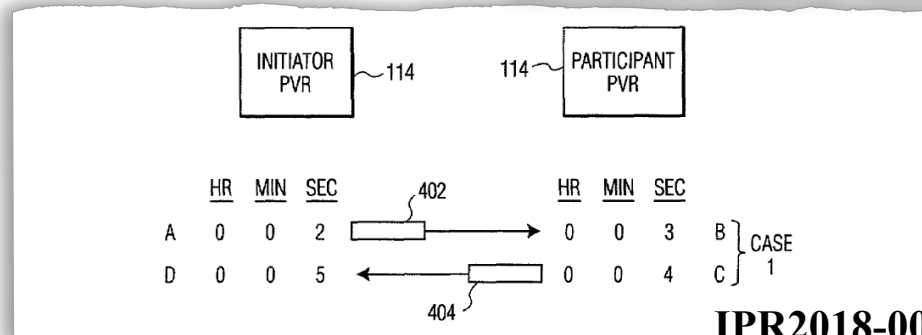
Time synchronization can be implemented in many different known ways. Distributed processors (nodes) in a network can broadcast their respective clock values periodically to maintain synchronization. "Fault-Tolerant Clock



Janevski

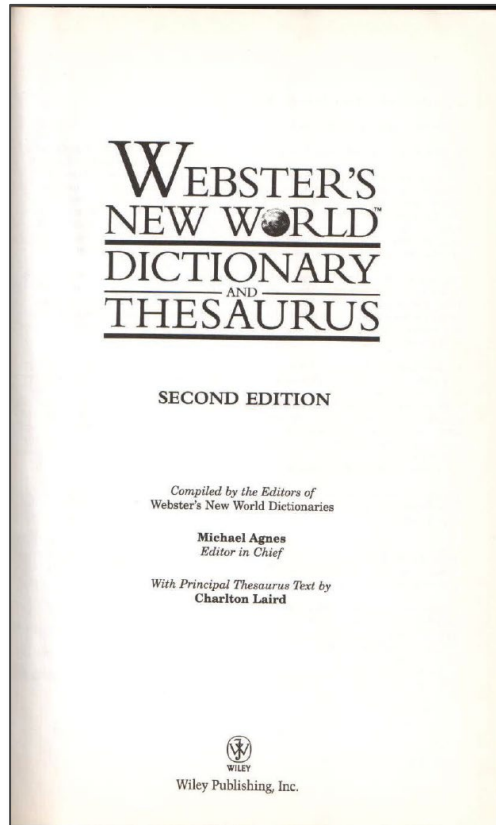
Ex.1007

Page 11 of 103



IPR2018-00766, -00767 EXHIBIT 1028

JANEVSKI'S "VIDEO TIMER" IS A CLOCK THAT OUTPUTS "DEVICE TIME"

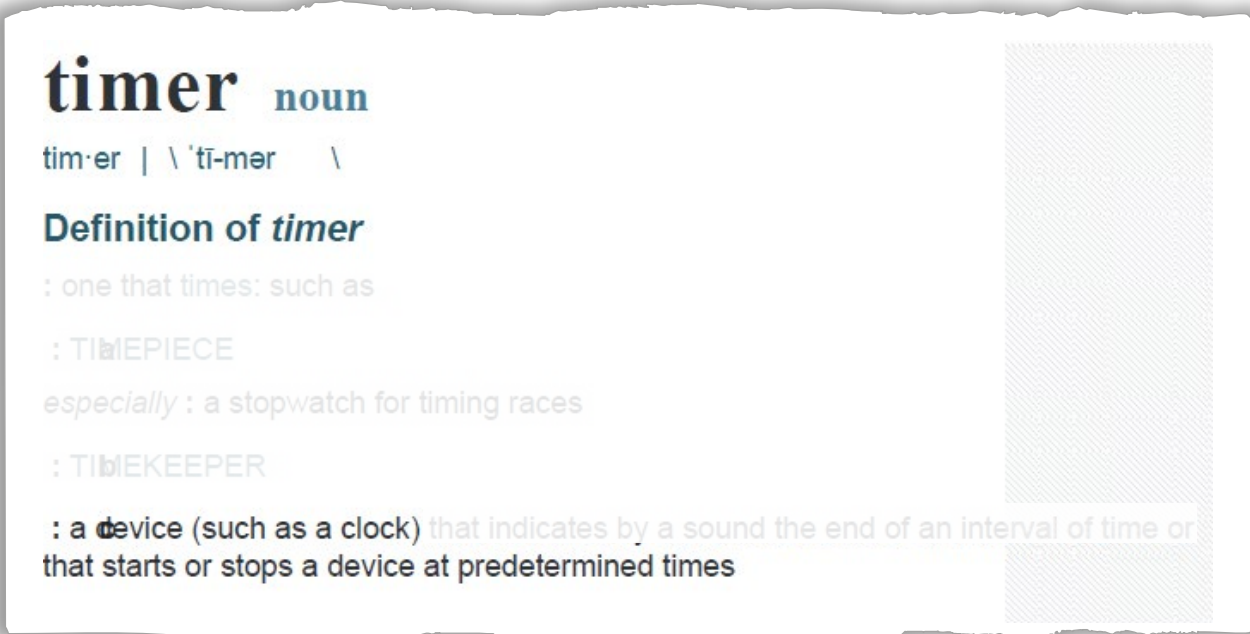
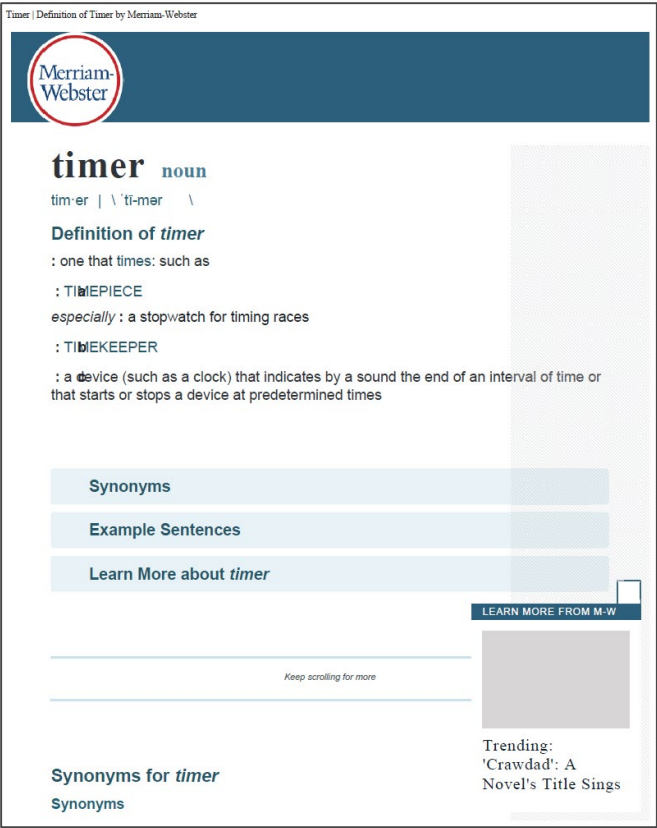


Webster's New World Dictionary
"clock"
Ex.1023

clock¹ (klāk) *n.* [ME *clokke*, orig., clock with bells < ML *clocca*, bell] a device for measuring and indicating time,

clock¹ *n.* timepiece, timekeeper, chronometer, timer, alarm clock, cuckoo clock, electric clock, grandfather clock, pendulum clock, atomic clock, digital clock, clock radio, hour-glass, stopwatch, sundial, wristwatch; see also WATCH 1. —around the clock continuously, continually, twenty-four hours a day; see REGULARLY.

JANEVSKI'S "VIDEO TIMER" IS A CLOCK THAT OUTPUTS "DEVICE TIME"



Merriam-Webster Online Dictionary
"timer"
Ex.1024

JANEVSKI'S "VIDEO TIMER" IS A CLOCK THAT OUTPUTS "DEVICE TIME"

LITIGATION CONSTRUCTION	IPR CONSTRUCTION
"[a] time indicated by a designated clock of the [master/slave] device"	"a time indicated by any clock of a given rendering device"



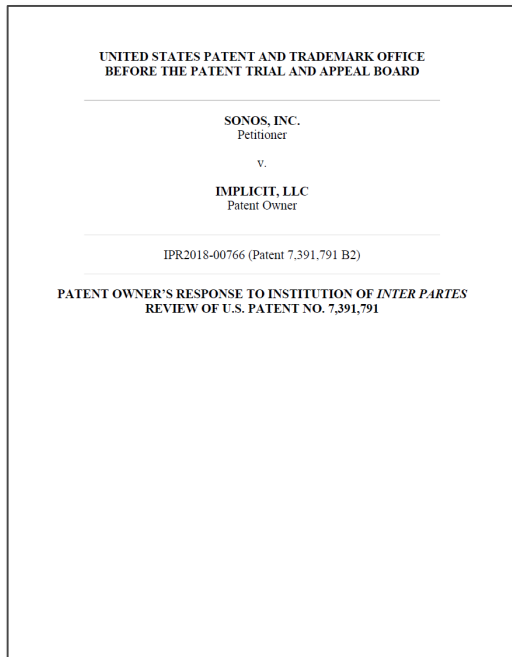
Dr. Roman Chertov
Sonos's Expert Witness

102. Further, Janevski discloses that each PVR has a "time count" provided by the PVR's "video timer." In my opinion, the PVR's "video timer" amounts to a clock of the PVR, and the "time count" provided by the "video timer" amounts to the claimed "device time" that is in a "time domain" of the PVR. *Id.* at FIGs. 2 & 4, 7:51-62, 8:39-10:3.

IMPLICIT CHALLENGES JANEVSKI'S DISCLOSURE OF "TIME DOMAIN"



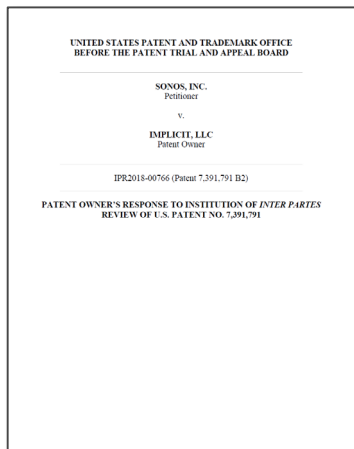
23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:



2. Janevski Fails to Disclose the "Time Domain" Limitations

Implicit POR
'791 Patent

JANEVSKI DISCLOSES THE “TIME DOMAIN” ELEMENTS



Implicit POR
'791 Patent



Dr. Roman Chertov
Sonos's Expert Witness

The “time domain” terms should be construed as “the way a device clock tracks time,” which Implicit also proposed in the co-pending litigation. Exhibit

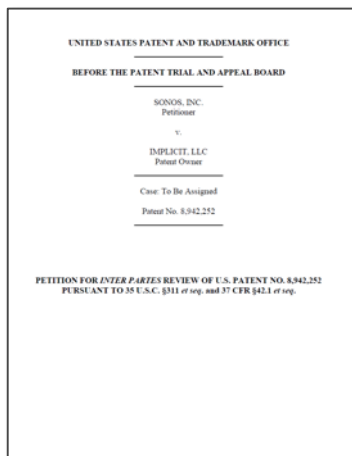
* * * *

Claims. The Petition does not show how the initiator and participant PVRs in Janevski track device time relative to one another. The video timer does not indicate a device time, but how long the PVR has been playing the video for. The query

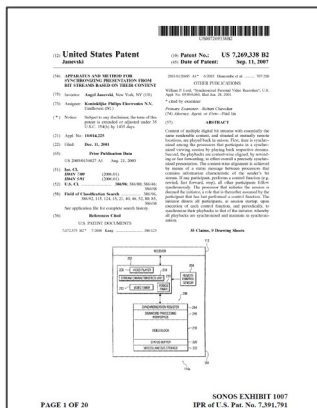
107. In my opinion, Janevski discloses claim element 23.2. For instance, Janevski discloses an example message flow for exchanging “synchronization messages” between the “initiator” and “participant” PVRs that include information regarding the PVRs’ respective “time counts.” *Id.* at FIG. 4, 8:39-10:3. It is my opinion that this disclosure amounts to the claimed functionality of “exchanging time domain information between the master and one or more slave devices” under the “broadest reasonable construction” set forth in Sonos’s Petition.

IPR2018-00766, -00767 EXHIBIT 1028

JANEVSKI DISCLOSES THE “TIME DOMAIN” ELEMENTS



Sonos Petition
'791 Patent



Janevski

Janevski discloses a message flow for exchanging “synchronization messages” between “initiator” and “participant” PVRs that include information regarding the PVRs’ respective “time counts.” Ex.1007, FIG. 4, 8:39-10:3. This disclosure

In fact, as noted above, Janevski’s message flow for exchanging “synchronization messages” between the “initiator” and “participant” PVRs disclosed is nearly identical to the preferred message flow for exchanging “time domain information” that is disclosed in the ‘791 Patent. Ex.1009, ¶¶81-86, 108.

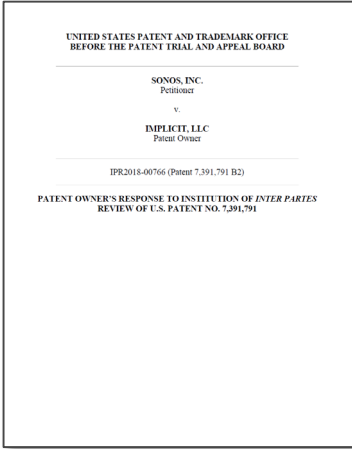
In the embodiment depicted herein, time synchronization is performed by the initiator PVR 114a individually with each participant PVR 114b, and involves sending an originating synchronization message 402 from the initiator PVR 114a to a participant PVR 114b and sending a reply synchronization message 404 from the participant PVR 114b to the initiator PVR 114a. It is assumed that the transmission

The time misregistration, TM, between the respective video timers 212 of the initiator and participant PVRs 114a, b is given by the formula:

$$TM = \frac{1}{2}[(A+D)-(C+B)] \tag{1}$$

- The petition included an alternative obviousness ground for claim 1 to the extent Implicit disputed whether Janevski inherently disclosed the “source time domain” element
- Implicit did not dispute this
- Implicit’s “objective evidence” is deficient in any event

IMPLICIT DISPUTES OBVIOUSNESS OF THE '252 PATENT



Implicit POR
'252 Patent

1. The Petition Fails to Show a *Prima Facie* Case

JANEVSKI + ANY "CLOCK SYNCHRONIZATION" REFERENCE RENDERS '252 CLAIMS OBVIOUS

US007269338B2

(12) United States Patent
Janevski

(16) Patent No.: US 7,269,338 B2
(45) Date of Patent: Sep. 11, 2007

(54) APPARATUS AND METHOD FOR SYNCHRONIZING PRESENTATION FROM BIT STREAMS BASED ON THEIR CONTENT

(75) Inventor: Angel Janevski, New York, NY (US)

(73) Assignee: Koninklijke Philips Electronics N.V., Eindhoven (NL)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 1455 days.

(21) Appl. No.: 10/614,225

(62) Filed: Dec. 11, 2001

(65) Prior Publication Data
US 2003/015827 A1 Aug. 21, 2003

(51) Int. Cl. (2006.01) H04N 7/00 (2006.01)
U.S. Cl. (2006.01) 386/96; 386/90; 386/46; 386/98

(58) Field of Classification Search
386/92; 113; 124; 15; 21; 43; 46; 52; 300; 85; 386/98

See application file for complete search history.

References Cited
U.S. PATENT DOCUMENTS
7,072,575 B2 * 7/2006 Kang

38 Claims, 9 Drawing Sheets

SONOS EXHIBIT 1007
IPR of U.S. Pat. No. 7,391,791

PAGE 1 OF 20

Janevski
Ex.1007

Fault-Tolerant Clock Synchronization for Distributed Systems with High-Mechanism Delay Variation*

Abstract

Each network clock synchronization is a distributed system in which a master clock provides a time reference to all other clocks in the network. In this paper, we propose a fault-tolerant clock synchronization algorithm that is able to tolerate clock drift, clock skew, and clock jitter. The algorithm is based on a distributed time synchronization protocol that uses a fault-tolerant consensus algorithm. The algorithm is able to tolerate clock drift, clock skew, and clock jitter. The algorithm is based on a distributed time synchronization protocol that uses a fault-tolerant consensus algorithm.

1

SONOS EXHIBIT 1011
IPR of U.S. Pat. No. 8,842,252

PAGE 1 OF 12

Azevedo
Ex.1010

Network Time Protocol (Version 3) Specification, Implementation and Analysis

Abstract

This document describes the Network Time Protocol (NTP), specifies its formal structure and implementation, and provides an analysis of its performance. The NTP is a distributed system that provides a time reference to all other clocks in the network. The NTP is based on a distributed time synchronization protocol that uses a fault-tolerant consensus algorithm.

SONOS EXHIBIT 1011
IPR of U.S. Pat. No. 8,842,252

PAGE 1 OF 12

Mills
Ex.1011

Time Synchronization Over Networks Using Convex Clusters

Abstract

This paper presents a general time synchronization algorithm that is able to tolerate clock drift, clock skew, and clock jitter. The algorithm is based on a distributed time synchronization protocol that uses a fault-tolerant consensus algorithm. The algorithm is able to tolerate clock drift, clock skew, and clock jitter. The algorithm is based on a distributed time synchronization protocol that uses a fault-tolerant consensus algorithm.

SONOS EXHIBIT 1012
IPR of U.S. Pat. No. 8,842,252

PAGE 1 OF 13

Berthaud
Ex.1012

United States Patent
Edison

Abstract

This document describes a method for clock synchronization in a distributed system. The method involves a master clock providing a time reference to all other clocks in the network. The method is based on a distributed time synchronization protocol that uses a fault-tolerant consensus algorithm.

SONOS EXHIBIT 1013
IPR of U.S. Pat. No. 8,842,252

PAGE 1 OF 13

Edison
Ex.1013

SONOS ESTABLISHED A MOTIVATION TO COMBINE WITH EVIDENCE



Dr. Roman Chertov
Sonos's Expert Witness

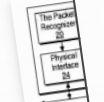
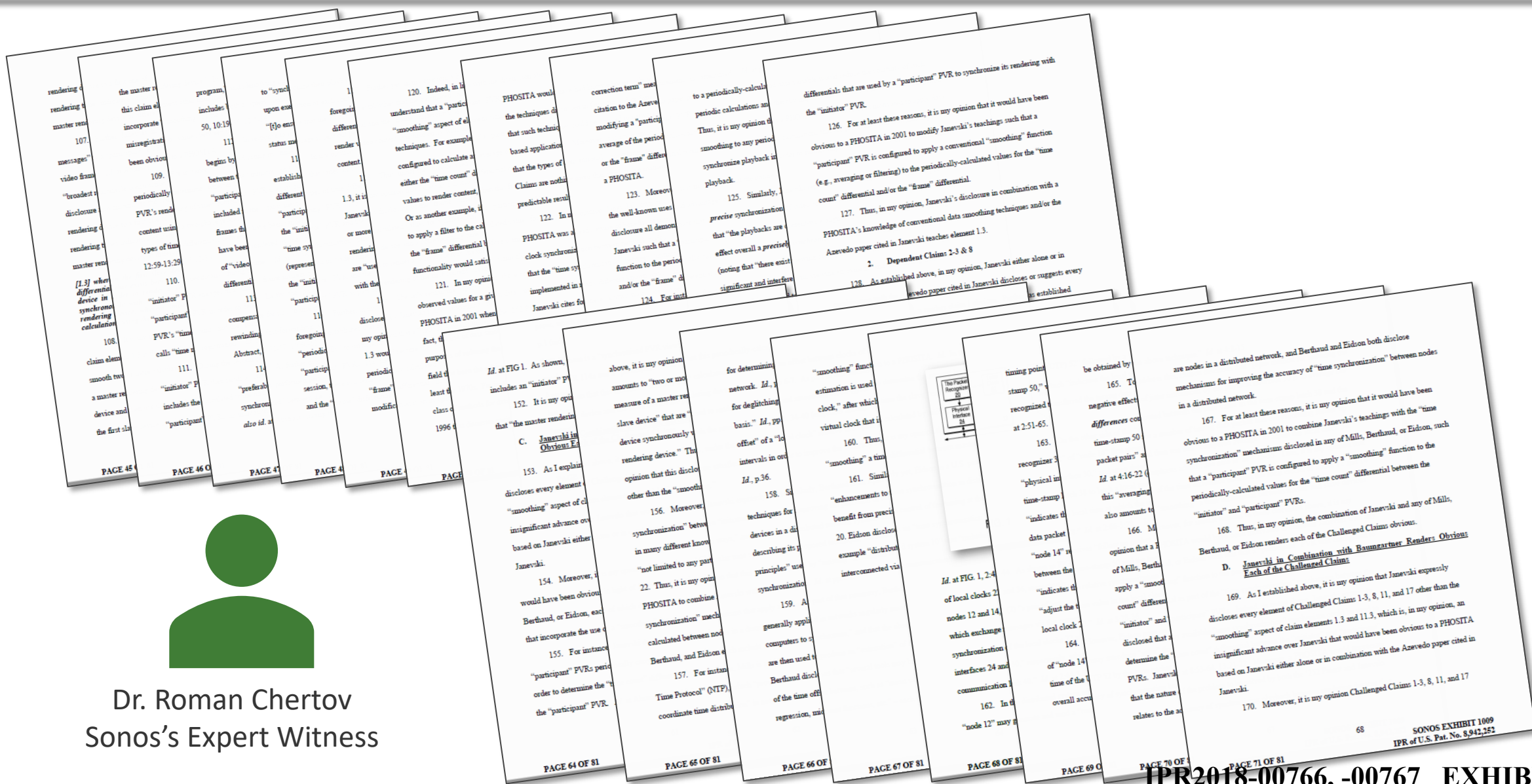
153. As I explained above, it is my opinion that Janevski expressly discloses every element of Challenged Claims 1-3, 8, 11, and 17 other than the “smoothing” aspect of claim elements 1.3 and 11.3, which is, in my opinion, an insignificant advance over Janevski that would have been obvious to a PHOSITA based on Janevski either alone or in combination with the Azevedo paper cited in Janevski.

154. Moreover, it is my opinion that Challenged Claims 1-3, 8, 11, and 17 would have been obvious in light of Janevski as combined with either Mills, Berthaud, or Eidson, each of which disclose “time synchronization” mechanisms that incorporate the use of a “smoothing” function.

SONOS ESTABLISHED A MOTIVATION TO COMBINE WITH EVIDENCE



Dr. Roman Chertov
Sonos's Expert Witness

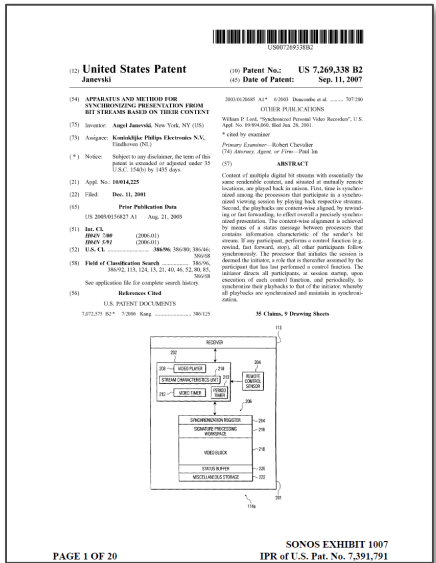


IPR2018-00766, -00767 EXHIBIT 1028

SONOS ESTABLISHED A MOTIVATION TO COMBINE WITH EVIDENCE

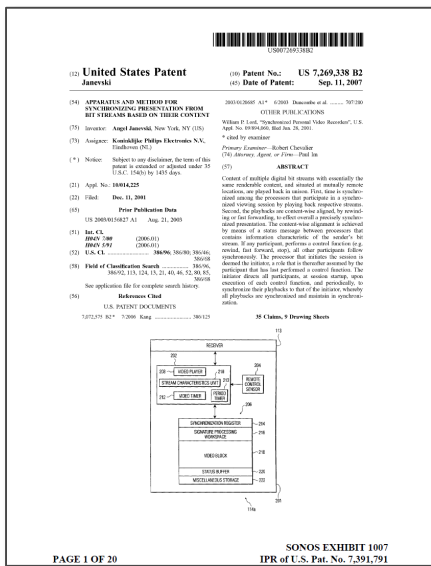
Moreover, the present invention, however, is not limited to any particular time synchronization method.

Time synchronization can be implemented in many different known ways. Distributed processors (nodes) in a network can broadcast their respective clock values periodically to maintain synchronization. "Fault-Tolerant Clock Synchronization for Distributed Systems with High Message Delay Variation", Azevedo, Marcelo Moraes de, et. al., Irvine, Calif. (1995). Synchronization messages may be relayed between source and destination processors, where relaying nodes discard messages recognized as coming from a faulty node. "Communication Protocols for Fault-Tolerant Clock Synchronization in Not-Completely Connected Networks", Pfluegl, Manfred J. et. al., Irvine, Calif. (1992).



Janevski
Ex.1007

SONOS ESTABLISHED A MOTIVATION TO COMBINE WITH EVIDENCE



Janevski
Ex.1007

(57)

ABSTRACT

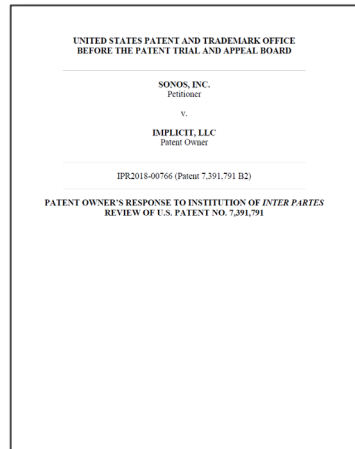
Content of multiple digital bit streams with essentially the same renderable content, and situated at mutually remote locations, are played back in unison. First, time is synchronized among the processors that participate in a synchronized viewing session by playing back respective streams. Second, the playbacks are content-wise aligned, by rewinding or fast forwarding, to effect overall a precisely synchronized presentation. The content-wise alignment is achieved

To achieve precise synchronization, the present invention compares corresponding content or “landmarks” of pairs of video playbacks to be synchronized, determines video replay “distance” between the landmark pairs, and slows down or speeds up selected playbacks in accordance with these distances.

The present invention provides a system that allows two or more people with personal video recorders (PVRs) to precisely synchronize their time-shifted viewing.

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT MISCONSTRUES LEGAL STANDARD OF OBVIOUSNESS



Implicit POR
'252 Patent

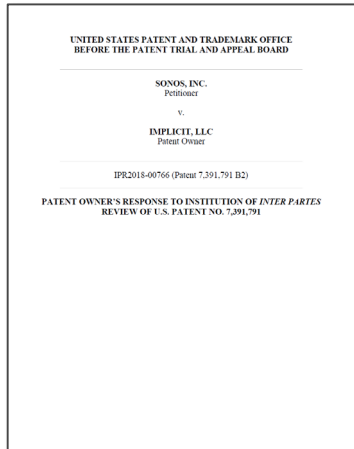
Because the “smoothing a rendering time” differential is not disclosed in the prior art, the Petition had to establish that “smoothing a rendering time” limitation was an insignificant enough difference to render obvious the invention as a whole. *KSR Int’l Co. v. Teleflex Inc.*, 550 U.S. 398, 405 (2007). The Petition did not clear



KSR Int’l Co. v. Teleflex Inc.,
550 U.S. 398 (2007)

“Section 103(a) forbids issuance of a patent when ‘the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious . . . to a person having ordinary skill in the art to which said subject matter pertains.’ ”

IMPLICIT'S "TEACHING AWAY" ARGUMENT IS LEGALLY AND FACTUALLY FLAWED



Implicit POR
'252 Patent

Moreover, Janevski itself teaches away from using device-clock-based smoothing techniques such as those in the secondary references. Janevski expressly cites Azevedo as a technique in which distributed processors in network can "broadcast their respective clock values periodically to maintain synchronization." Janevski, at col. 8 ll. 53–64. But instead of using that technique (or an analogous

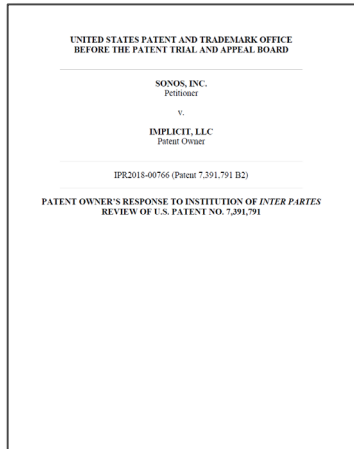


*Arctic Cat Inc. v. Bombardier
Recreational Prod. Inc.,*
876 F.3d 1350 (Fed. Cir. 2017)

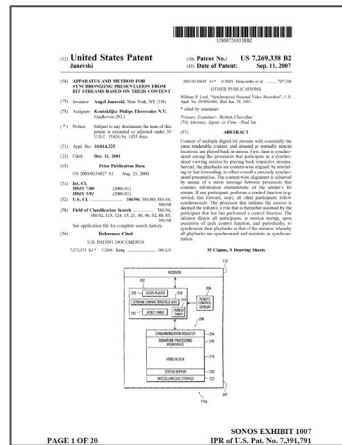
"Prior art teaches away when 'a person of ordinary skill, upon reading the reference, would be discouraged from following the path set out in the reference, or would be led in a direction divergent from the path that was taken by the applicant.'"

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S "TEACHING AWAY" ARGUMENT IS LEGALLY AND FACTUALLY FLAWED



Implicit POR
'252 Patent



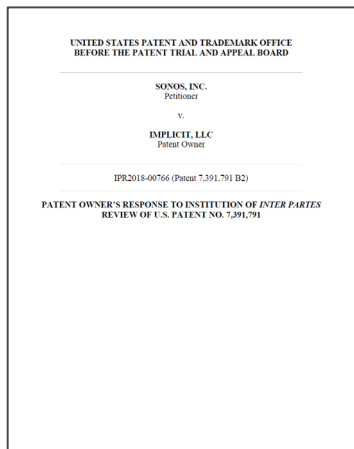
Janevski
Ex.1007

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Time synchronization can be implemented in many different known ways. Distributed processors (nodes) in a network can broadcast their respective clock values periodically to maintain synchronization. “Fault-Tolerant Clock Synchronization for Distributed Systems with High Message Delay Variation”, Azevedo, Marcelo Moraes de, et. al., Irvine, Calif. (1995). Synchronization messages may be relayed between source and destination processors, where relaying nodes discard messages recognized as coming from a faulty node. “Communication Protocols for Fault-Tolerant Clock Synchronization in Not-Completely Connected Networks”, Pfluegl, Manfred J. et. al., Irvine, Calif. (1992).

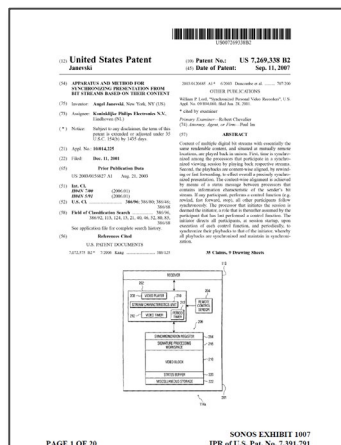
IPR2018-00766, -00767 EXHIBIT 1028

EVIDENCE SHOWS HOW A POSITA WOULD HAVE APPLIED "SMOOTHING"

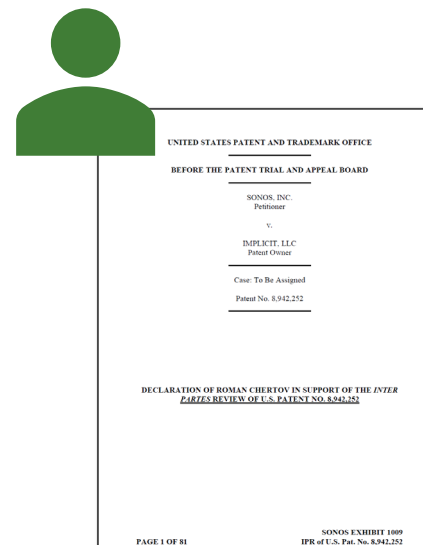


Implicit POR
'252 Patent

Challenged Claims. But there is no indication in any of the references of how a skilled artisan would have applied the smoothing techniques from the secondary references to Janevski to obtain a working system that smoothed a rendering time differential.



Janevski
Ex.1007

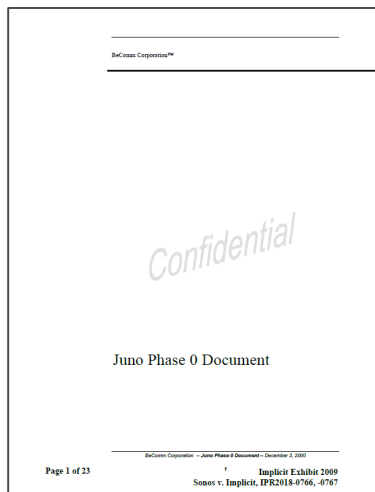


Dr. Roman Chertov Declaration
Ex.1009

IPR2018-00766, -00767 EXHIBIT 1028

- No evidence of *long-felt* but unmet need
- No evidence that purported licenses exist or have a *nexus* with the claims
- No evidence that commercial success has a *nexus* with the claims

IMPLICIT FAILS TO ESTABLISH LONG-FELT NEED



Juno Phase 0
(Dec. 2000)
Ex.2009

Synchronization

When multiplexing to multiple adapters, Juno will make a best effort to keep the playback at the Adapters synchronized. Both Jupiter and BeComm recognize that true synchronization is an unsolved computer science problem, but a best effort will be made in this regard.

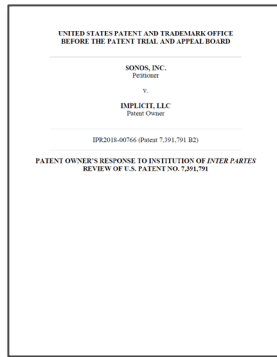


Ecolochem, Inc. v. S. California Edison Co.,
227 F.3d 1361
(Fed. Cir. 2000)

One-year time between identification of problem and patent solution was found to be a “shortly-felt requirement” rather than a “long-felt need”

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S LICENSING "EVIDENCE" IS DEFICIENT



Implicit POR
'252 Patent

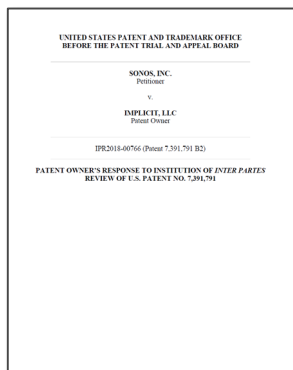


Edward Balassanian
Lead Inventor & Founder

The licensing of that technology, including by Intel, further reflects that it is not technology that would have been obvious when the '252 Patent was effectively filed. The patents flowing from BeComm's innovations, of which the Patent is a part, were later licensed for significant value beginning in 2007 to companies such as Intel, Microsoft, Google, Cisco, HP, AMD, Apple, and others. Ex. 2001, at ¶ 10.

10. I am the inventor on over 25 issued U.S. Patents that stem from my work at BeComm. Companies such as Microsoft, Apple, Google, Intel, AMD, Cisco, HP, Palo Alto Networks, and a number of other companies have licensed that patent portfolio for significant value.

IMPLICIT FAILS TO ESTABLISH NEXUS FOR COMMERCIAL SUCCESS



Implicit POR
'252 Patent

Intel's willingness to pay significant value for the technology of the Juno project is another indicator of nonobviousness. The prospect to BeComm for the

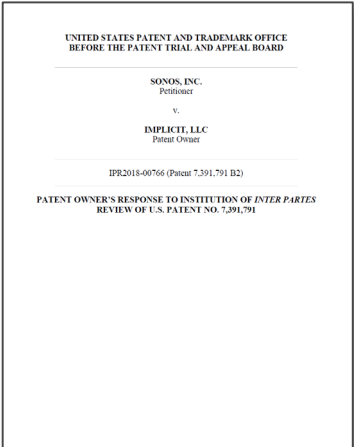
Companies that use the synchronization technology of the Challenged Claims have also achieved (and continue to achieve) significant commercial success. That includes Sonos. The purpose of Sonos is to "fill every home with music." Ex. 2089,



ABT Sys., LLC v. Emerson Elec. Co.,
797 F.3d 1350
(Fed. Cir. 2015)

"[R]eliance on commercial success is undermined as a matter of law by [patentee's] failure to introduce evidence related to the nexus."

JANEVSKI DISCLOSES “MASTER DEVICE TIME” OF ‘252 CLAIM 2



Implicit POR
'252 Patent



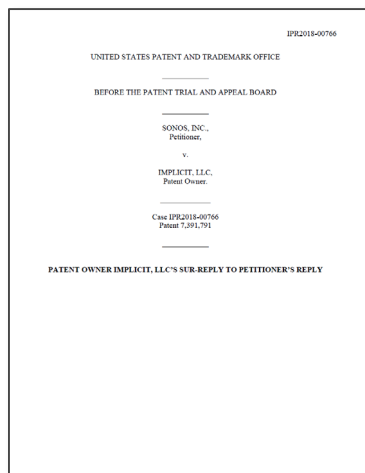
Dr. Roman Chertov
Sonos's Expert Witness

b. Janevski Does Not Disclose a “Master Device Time”

Janevski does not disclose a master device time. The Petitioner has not shown that the query time stamp is a master device time. Janevski does not indicate if the query time stamp contains a time indicated by a designated clock of any particular device let alone the initiator device. The reference simply states that the initiator device “inserts . . . a query time stamp” into a message, Janevski, col. 10 ll. 17–24— but it does not state that the initiator’s device time is within that time stamp.

129. In my opinion, Janevski discloses claim element 2.0. For instance, as discussed above, Janevski discloses that the “initiator” PVR (which amounts to the claimed “master rendering device”) periodically sends each “participator” PVR a “status message” that contains an indication of the “initiator” PVR’s “time into the [video] program” as well as a “query time stamp” for “a frame that the initiator has just played or has recently played.” *Id.* at Abstract, 7:36-50, 10:19-35, 12:5-36. It is my opinion that this disclosure amounts to the claimed functionality of sending “a master device time at which the master rendering device renders content.”

DR. CHERTOV'S OPINIONS ON "RENDERING TIME" ARE ENTIRELY CONSISTENT



Implicit Sur-Reply
'791 Patent

not to answer those questions based on “scope.” *Id.* at 144:1–145:13. But Dr. Chertov’s conflicting application of the “rendering time” terms weighs heavily on Dr. Chertov’s credibility regarding the Implicit Source Code. The conflicting testimony (and Sonos’s corresponding discovery tactics) cast significant doubt on all of Dr. Chertov’s testimony in favor of Sonos. Implicit thus respectfully requests that the Board decline to credit any of Dr. Chertov’s testimony proffered in support of Sonos, whether in support of invalidity or in support of their positions on Implicit’s Source Code.



Dr. Roman Chertov
Sonos's Expert Witness

24. The construction of "rendering time" I have applied and used in formulating my opinions is "a time measure of the amount of content that has already been rendered by a given rendering device." I understand that Implicit and Dr. Hashmi have not challenged this construction of "rendering time."

25. When applying this construction of "rendering time" to the Implicit Source Code, it is my opinion that the Implicit Source Code fails to practice any of the "rendering time" limitations of the Challenged Claims of the '791 Patent.



Dr. Roman Chertov
Sonos's Expert Witness

IX. CLAIM CONSTRUCTION

69. My opinions regarding the issue of patentability of the '791 Patent are based upon the "broadest reasonable constructions" proposed in Sonos's Petition for *Inter Partes* Review of the '791 Patent (the "Petition"), which I have reproduced in tabular form below.

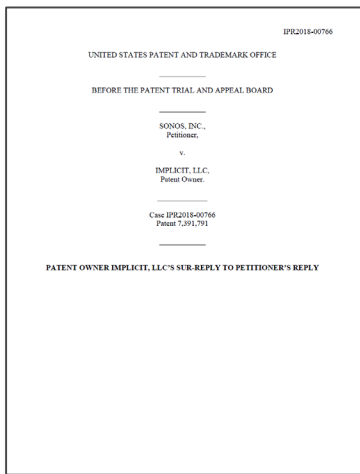
Claim Element(s)	Broadest Reasonable Construction
"device time"	"a time indicated by any clock of a given rendering device"
"rendering time"	"a time measure of the amount of content that has already been rendered by a given rendering device"

103. Further yet, Janevski discloses that each PVR keeps track of the amount of content in a given video program that has already been rendered by the PVR in terms of "the time or frame into the program." In my opinion, this "time or frame into the program" maintained by a PVR amounts to the claimed "rendering time." *Id.* at 1:65-2:5, 7:41-50.

DR. CHERTOV'S OPINIONS ON "RENDERING TIME" ARE ENTIRELY CONSISTENT



Dr. Roman Chertov
Sonos's Expert Witness



Implicit Sur-Reply
'791 Patent

103. Further yet, Janevski discloses that each PVR keeps track of the amount of content in a given video program that has already been rendered by the PVR in terms of "the time or frame into the program." In my opinion, this "time or frame into the program" maintained by a PVR amounts to the claimed "rendering time." *Id.* at 1:65-2:5, 7:41-50.

that term in the Petition. In seeking review, Sonos and Dr. Chertov contended that the "time or *frame into the program*," when applying the claims to Janevski to assert invalidity, "amounts to the claimed 'rendering time.'" Paper No. 1, at 39 (emphasis added)

- Implicit's swear behind does not address the ***claimed*** subject matter
- Implicit's swear behind lacks ***independent*** corroboration
- Implicit's swear behind improperly relies on ***incorporated*** material

IMPLICIT'S SWEAR BEHIND DOES NOT ADDRESS THE CLAIMED SUBJECT MATTER



Fina Oil & Chem. Co. v. Ewen,
123 F.3d 1466, 1473
(Fed. Cir. 1997)

“Conception is the touchstone to determining inventorship.”



Coleman v. Dines,
754 F.2d 353, 359
(Fed. Cir. 1985)

To establish conception, a party must show possession of **every feature** recited in the **claim**, and **every limitation** of the **claim** must have been known to the inventor at the time of the alleged conception.

- Balassanian states that he and Bradley “originally conceived of the inventions set forth in the Claims of the Patents and they were actually reduced to practice before December 11, 2001”

Ex. 2001, ¶16

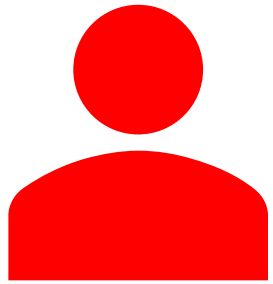
- To make this declaration, Balassanian must have had some understanding of what the claims of the patents mean

- Balassanian repeatedly testified during his deposition that he could not, and would not, provide his understanding of “the inventions set forth in the Claims of the Patents”

Ex.1019, 20:16-22:24; 26:5-16; 36:3-19; 39:18-41:12; 44:22-45:3;
47:6-49:20; 50:11-22; 51:22-52:4; 53:1-24; 165:9-166:10

- Balassanian was asked for *his* understanding of the claimed invention, *not* a lawyer’s understanding

BALASSANIAN REFUSED TO PROVIDE HIS UNDERSTANDING OF THE CLAIMS



Edward Balassanian
Lead Inventor & Founder

Q. What inventions set forth in the Claims of the Patents did you conceive of?

A. **The specific inventions detailed in the claims.**

Q. What are those inventions?

A. **They are what the claims state they are. And I am not going to construe claims for you.**

Q. Do you have any understanding of how the Claims of the Patents are construed?

A. **I do not purport to understand claim construction.**

Q. Have you read the claims?

A. **At some point, yes, I have.**

Q. And did you understand the claims when you read them?

A. **My understanding of the claims is simply as a layman. I do not have a legal perspective on the claims.**

Q. Can you tell me what that understanding is?

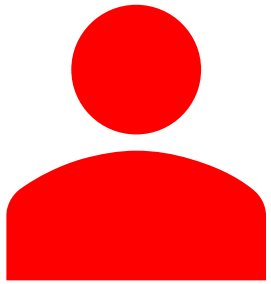
A. **Not without reading the claims.**

Q. Okay. Having just read Claim 1 of the 791 patent, can you tell me your understanding of the invention of Claim 1?

A. **Claim 1 is, specifically, what Claim 1 says. And for me to say anymore than that would mean I'm construing what it means. And I am not going to do that. I am not a lawyer.**

IPR2018-00766, -00767 EXHIBIT 1028

BALASSANIAN IMPROPERLY CONNECTED CONCEPTION TO SPECIFICATION



Edward Balassanian
Lead Inventor & Founder

Q. And so when you said: "According to the invention that Mr. Bradley and I conceived of earlier in 2001." Are you referring to the claims of the 791 and the 252 patents?

A. No, I'm referring to the general concept of synchronizing audio and video content.

Q. So, anytime throughout this declaration when you're testifying that tests and demos synchronize content according to the invention, am I correct in saying that you are not referring to the claims in the invention?

A. In this particular sentence, I am saying that the demonstration synchronized content according to the invention in my references in regards to the concept of synchronizing multimedia content in the network. I am not speaking to the invention as claimed in the patent or any other references to invention in here. Unless you want to specifically call them out and I can tell you what I think I meant by them.

- Unable to formulate an understanding of “the inventions set forth in the Claims of the Patents,” Balassanian cannot competently testify regarding any conception of the ***claimed inventions*** of the patents at issue, let alone when such conception occurred
- For this reason alone, the Board should reject Implicit's swear behind

- Implicit's swear behind does not address the *claimed* subject matter
- Implicit's swear behind lacks *independent* corroboration
- Implicit's swear behind improperly relies on *incorporated* material

IMPLICIT'S SWEAR BEHIND LACKS INDEPENDENT CORROBORATION



Mahurkar v. C.R. Bard, Inc.,
79 F.3d 1572, 1577
(Fed. Cir. 1996)

It is well established that when a party seeks to prove conception through an inventor's testimony, the party must proffer evidence, "in addition to [the inventor's] own statements and documents," corroborating the inventor's testimony.

IMPLICIT'S SWEAR BEHIND LACKS INDEPENDENT CORROBORATION



Chen v. Bouchard,
347 F.3d 1299, 1309
(Fed. Cir. 2003)

The requirement of independent corroboration exists to prevent an inventor from “describ[ing] his actions in an unjustifiably self-serving manner.”



Medichem, S.A. v. Rolabo, S.L.,
437 F.3d 1157, 1171-72
(Fed. Cir. 2006)

“Even the most credible inventor testimony is a *fortiori* required to be corroborated by independent evidence.”

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S SWEAR BEHIND LACKS INDEPENDENT CORROBORATION



In re NTP, Inc.,
654 F.3d 1279, 1291
(Fed. Cir. 2011)

The sufficiency of the proffered corroboration is determined by a “rule of reason” analysis.



Cooper v. Goldfarb,
154 F.3d 1321, 1330
(Fed. Cir. 1998)

But, even under the “rule of reason” analysis, the “evidence of corroboration must not depend solely on the inventor himself.”

IMPLICIT'S SWEAR BEHIND LACKS INDEPENDENT CORROBORATION

- Implicit's swear behind relies exclusively on the declaration and documents of *inventor* Balassanian, who is also Implicit's founder, sole member, and manager

Ex. 2001, ¶¶2-6

- Without any independent corroboration, the Board should reject Implicit's swear behind

IMPLICIT'S SWEAR BEHIND IS NEARLY IDENTICAL TO THE ONE REJECTED IN APATOR

FACTS IN APATOR	FACTS HERE
Patent owner attempted to swear eighteen days behind prior art's filing date.	Patent owner has attempted to swear seven days behind prior art's filing date.
Patent owner proffered an inventor declaration in support of its swear behind.	Patent owner has proffered an inventor declaration in support of its swear behind.
In his declaration, the inventor cited to several different documents, such as emails, a presentation, an image file, and numerous drawings, related to his invention.	In his declaration, the inventor has cited to several different documents, such as video file, internal literature, and certain test packages, related to his invention.
Patent owner attempted to corroborate inventor testimony with documents, but the documents only provide corroboration with help from the inventor's testimony.	Patent owner has attempted to corroborate inventor testimony with documents, but the documents only provide corroboration with help from the inventor's testimony.
Inventor's declaration and documents were not independently corroborated.	Inventor's declaration and documents have not been independently corroborated.

IMPLICIT'S SWEAR BEHIND LACKS INDEPENDENT CORROBORATION

- With respect to the source code written by Guy Carpenter, Implicit presents no evidence (other than Balassanian's uncorroborated testimony) that the inventors communicated the invention to Carpenter

Ex. 2019, Ins. 23-25 ("Owner: Guy Carpenter"); Ex.2017 (same); Ex.2020 (same).



KAYAK Software Corp. v. IBM Corp.,
IPR2016-00608, 2017WL3425957
(P.T.A.B. Aug. 7, 2017)

The record is devoid of evidence that Carpenter's work inured to the benefit of the inventors and thus the Board should not rely on the code for conception or RTP.

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S SWEAR BEHIND LACKS INDEPENDENT CORROBORATION

- As an expert hired by Implicit for this IPR to analyze source code and compare it to the Challenged Claims, Dr. Hashmi cannot corroborate Balassanian's invention testimony

Ex. 2080, ¶¶1-3



Tavory v. NTP, Inc.,
297 F. App'x 976, 979
(Fed. Cir. 2008)

Weaver v. Houchin,
467 F. App'x 878, 881
(Fed. Cir. 2012)

Effective corroborative testimony must be based on the ***personal*** observations of an ***independent*** witness who recognized and appreciated the claimed invention ***at the time the work was done.***

IPR2018-00766, -00767 EXHIBIT 1028

- Implicit's swear behind does not address the *claimed* subject matter
- Implicit's swear behind lacks *independent* corroboration
- Implicit's swear behind improperly relies on *incorporated* material

IMPLICIT'S SWEAR BEHIND IMPROPERLY RELIES ON INCORPORATED MATERIAL



ZTE (USA), Inc. v. Elec. and Telecomm. Research Inst.,
IPR2015-00028, Paper 12 at 7
(P.T.A.B. Mar. 20, 2015)



Cisco Sys., Inc. v. C-Cation Tech., LLC,
IPR2014-00454, Paper 12 at 9
(P.T.A.B. Aug. 29, 2014)
IBG LLC v. Trading Tech., Inc.,
CBM2016-00054, Paper 36 at 3-4
(P.T.A.B. Apr. 7, 2017)

“A brief must make all arguments accessible to the judges, rather than ask them to play archeologist with the record.”

When a party fails to adequately explain its reasoning in its principal brief, and instead merely cites to a declaration or claim chart exhibit, the Board routinely finds an improper incorporation by reference.

IPR2018-00766, -00767 EXHIBIT 1028

Here, Implicit made no attempt in its *POR* to tie its source code to the actual limitations of the Challenged Claims.

- Instead, Implicit defers to its expert declaration (Ex. 2080) and accompanying claim chart (Ex. 2081)

See POR at pp. 15, 25, 28-31

As such, Implicit has improperly incorporated these arguments by reference into its POR in contravention of 37 C.F.R. §§42.6(a)(3), §42.24(b)(2)

- The code fails to synchronize between a master and a slave
- The code fails to meet “rendering time”

THE '791 CLAIMS REQUIRE SYNCHRONIZING BETWEEN MASTER AND SLAVE

1. A method for synchronizing a rendering of a content provided by a source at one or more devices which are nodes of a network, the content having a rendering time, the method comprising:

designating one of the one or more devices a master device, the master device having a master device time and a master rendering time;

designating remaining devices among one of the one or more devices as at least one slave device, the at least one slave device having a slave device time and a slave rendering time;

receiving the content for rendering by the master and at least one slave device;

sending from the master device to the at least one slave device an indication of when the master device renders content corresponding to the master rendering time;

determining a master device time domain, a slave device time domain, and a source time domain;

determining whether a time domain differential exists between the master rendering time, the slave rendering time; and

adjusting, based on the received indication, the rendering of the content at the at least one slave device within the slave device time domain and in proportion to the time domain differential when present to account for variation between when the master device and the at least one slave device to render content that should be rendered at the same time.

16. A method for synchronizing rendering of content at devices which are nodes of network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:

designating one of the devices as a master device having a master rendering time and the one or more slave devices having a slave rendering time;

sending to each device content to be rendered at that device synchronized with the content sent to the other devices;

sending from the master device to the one or more slave devices a master device time corresponding to the master rendering time of the master device; and upon receiving the sent master device time at the one or more slave devices,

exchanging time domain information between the master and one or more slave devices;

determining at least one time domain differential between the master rendering time and the slave rendering time between the master device and the one or more slave devices;

adjusting the rendering of the content at the one or more slave devices to account for a difference in the slave rendering time and the master rendering time calculated based on the master device time adjusted for a difference in time domains of the one or more slave devices and the master device.

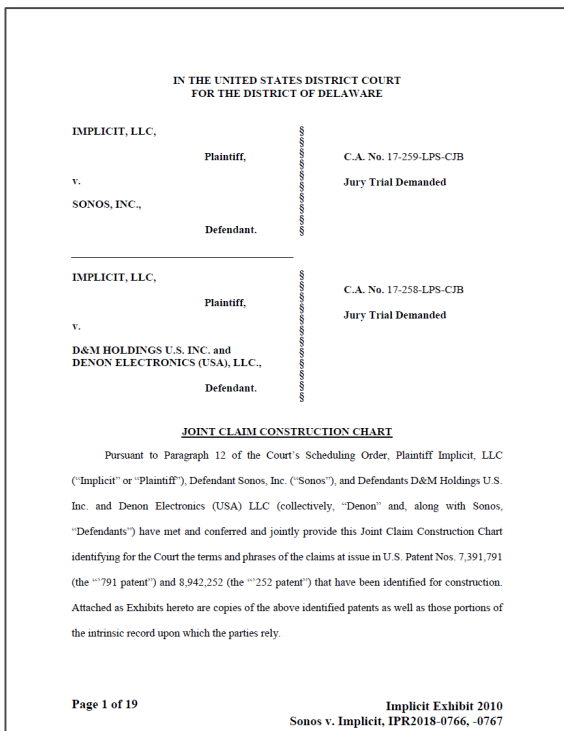
23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:

designating one of the devices as a master device having a master rendering time and the one or more slave devices having a slave rendering time; and for each slave device, exchanging time domain information between the master and one or more slave devices;

calculating a time domain difference between the master rendering time of the master device and the slave rendering time of the slave device based on a master device time adjusted for a difference in time domains of the slave device and the master device; and

rendering content at the slave device to account for the calculated time domain difference.

THE '791 CLAIMS REQUIRE SYNCHRONIZING BETWEEN MASTER AND SLAVE



Joint Claim Construction Chart
Ex. 2010

Further, the parties state that they have stipulated to the following constructions for the following claim terms:

- '791 patent, claims 1-3, 6-9, 12, 16, 19, 23-25: the preambles are limiting
- "master device time" / "slave device time" / "device time" of a "slave" means "time indicated by a designated clock of the [master/slave] device"

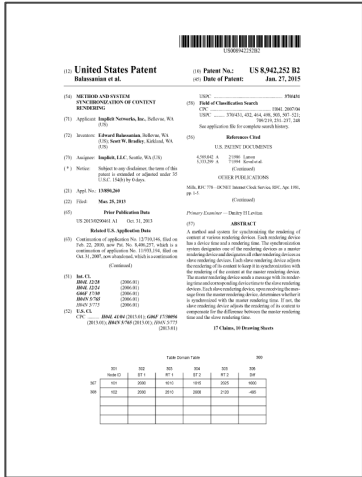
The parties jointly and respectfully request that, if the Court deems it appropriate, the Court include these stipulated constructions in its claim construction order.

The parties respectfully submit the following chart setting forth their proposed constructions and intrinsic evidence for the claim terms proposed for construction.

Proposed Claim Constructions and Intrinsic Evidence^{1,2,3,4}

Term/Phrase	Patents/Claims	Plaintiff's Proposed Construction and Intrinsic Evidence	Defendants' Proposed Construction and Intrinsic Evidence
"time domain"	'791 patent, claims 1-3, 6-9, 12, 16, 19, 23-25	"the way a device clock tracks time"	"a reference of time" '791 patent at 1:36-40

THE '252 CLAIMS REQUIRE SYNCHRONIZING BETWEEN MASTER AND SLAVE

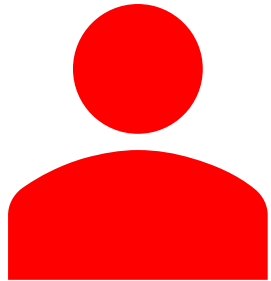


'252 Patent

1. A method, comprising:
a master rendering device rendering a first content stream;
and
sending, from the master rendering device to a first one of a plurality of slave devices, a plurality of master rendering times indicative of statuses of the rendering the first content stream at the master rendering device at different times;
wherein the first slave device is configured to smooth a rendering time differential that exists between the master rendering device and the first slave device in order to render a second content stream at the first slave device synchronously with the rendering of the first content stream at the master rendering device, wherein smoothing the rendering time differential includes calculations using the plurality of master rendering times.

11. A method, comprising:
receiving, at a slave device, a particular content stream;
receiving, at the slave device from a master rendering device, a plurality of master rendering times indicative of status of rendering a different content stream at the master rendering device;
the slave device determining a smoothed rendering time differential that exists between the master rendering device and the slave device, wherein the determining is based on calculations using the plurality of master rendering times and a plurality of slave rendering times corresponding to rendering the particular content stream at the slave device; and
based on the smoothed rendering time differential, the slave device rendering the particular content stream synchronously with the master rendering device rendering the different content stream.

INTENDED PURPOSE IS SYNCHRONIZING BETWEEN MASTER AND SLAVE



Edward Balassanian
Lead Inventor & Founder

47. The description of those test packages matches my memory of participating in tests and demonstrations of Strings that operated for its intended purpose to synchronize the rendering of audio and video in accordance with the inventions set forth in the Claims of the Patents.

56. The file system metadata information for the "fightclubrgb.avi" in that folder indicates that the movie was last modified on September 7, 2001, Exhibit 2077, at 21, which is consistent with my memory of about when I remember participating in tests and demonstrations of the synchronization that began to use the Fight Club trailer. The file contains the Fight Club content whose rendering we synchronized prior to December 11, 2001, starting in the September, 2001 time period. During this time, we tested and demonstrated synchronized rendering of content in the "fightclubrgb.avi" file across multiple devices. The testing and demonstrating of the system worked for its intended purpose to synchronize the rendering of the audio and video across a master device and one or slave devices.

68. I participated in multiple tests and demonstrations of the Strings Audio Player to synchronize audio between two devices during this period, prior to December 11, 2001. The software worked for its intended purpose to synchronize the audio playback between a master device and at least one slave device. With regard to the synchronization functionality of the Claims of the Patents, the Strings Audio Player operated like the demonstration in the earlier test/demo package, reflected in the audioplayerapp.rule file above.

IMPLICIT'S CODE FAILS TO SYNCHRONIZE BETWEEN MASTER AND SLAVE



Dr. Roman Chertov
Sonos's Expert Witness

60. The Implicit Source Code makes clear that the configurations relied on by Dr. Hashmi in his Expert Declaration were designed such that the designated “master” device would function to receive media content from a source, render the received media content, and then send the received media content to the designated “slave” device(s) only after the “master” device had rendered that media content. *See, e.g.*, Ex. 2065; Ex. 2072. In other words, the configurations relied on by Dr. Hashmi were specifically designed to have the designated “master” device begin rendering media content *before* even sending that media content to the designated “slave” device(s) for rendering – which means that these configurations were also specifically designed to have the “master” device begin rendering media content before the “slave” device(s) could ever begin rendering that media content as opposed to having the designated “master” and “slave” devices render content simultaneously. *See id.* Because of this design, it is my opinion that the Implicit Source Code does not practice a method of synchronizing rendering of content between the “master” and “slave” rendering devices as required by the ‘791 Patent.

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S CODE FAILS TO SYNCHRONIZE BETWEEN MASTER AND SLAVE

```
1 <!DOCTYPE RULES PUBLIC "-//BECOMM/DTD Rules V0.9//EN" "" [
2
3 ]>
4 <RULES>
5 <RULE>
6 <DESCRIPTION xml:lang="en">Port 9012 : unsync A+</DESCRIPTION>
7 <PREDICATE value="query:(Content-Type=="TCP/Decode/Output" OR
8 Content-Type=="UDP/Decode/Output") AND Network-Port-Local==9012"/>
9 <ROUTE>
10 <STEP>
11 <BEAD name="framer"/>
12 <EDGE name="decode"/>
13 <SEED
14 value="namespace:Content-Type='audio/pcm',AudioContext=pcmcontext:"/>
15 </STEP>
16 <STEP>
17 <BEAD name="fanout"/>
18 <EDGE name="decode"/>
19 <SEED value="namespace:FanoutCount=2"/>
20 </STEP>
21 </ROUTE>
22 </RULE>
23 <RULE>
24 <DESCRIPTION xml:lang="en">Fanout0 : master audio</DESCRIPTION>
25 <PREDICATE value="query:FanoutIndex==0 AND
26 Network-Port-Local==9012"/>
27 <ROUTE>
28 <STEP>
29 <BEAD name="speaker"/>
30 <EDGE name="encode"/>
31 </STEP>
32 </ROUTE>
33 </RULE>
34 <RULE>
35 <DESCRIPTION xml:lang="en">9012 Fanout1: broadcast</DESCRIPTION>
36 <PREDICATE value="query:FanoutIndex==1 AND
37 Network-Port-Local==9012"/>
38 <ROUTE>
39 <STEP>
40 <BEAD name="framer"/>
41 <EDGE name="decode"/>
42 <SEED
43 value="namespace:Content-Type='audio/pcm',AudioContext=pcmcontext:"/>
44 </STEP>
45 <STEP>
46 <BEAD name="fanout"/>
47 <EDGE name="decode"/>
48 <SEED value="namespace:FanoutCount=2"/>
49 </STEP>
50 </ROUTE>
51 </RULE>
```

Page 1 of 3
Implicit Exhibit 2065
Sonos v. Implicit, IPR2018-0766, -0767

pcmserveraudio.rule
Ex. 2065

```
73 <DESCRIPTION xml:lang="en">Fanout0 : master audio</DESCRIPTION>
74 <PREDICATE value="query:FanoutIndex==0 AND
75 Network-Port-Local==9013"/>
76 <ROUTE>
77 <STEP>
78 <BEAD name="speaker"/>
79 <EDGE name="encode"/>
80 <SEED value="namespace:RenderClock=sampleclock:MASTER"/>
81 </STEP>
```

```
84 <RULE>
85 <DESCRIPTION xml:lang="en">9013 Fanout1: broadcast</DESCRIPTION>
86 <PREDICATE value="query:FanoutIndex==1 AND
87 Network-Port-Local==9013"/>
88 <ROUTE>
89 <STEP>
90 <BEAD name="clocksync"/>
91 <EDGE name="encode"/>
92 <SEED value="namespace:RenderClock=sampleclock:"/>
93 </STEP>
94 <STEP>
95 <BEAD name="framer"/>
96 <EDGE name="decode"/>
97 <SEED
98 value="namespace:Content-Type='audio/pcm',AudioContext=pcmcontext:"/>
99 </STEP>
100 <STEP>
101 <BEAD name="fanout"/>
102 <EDGE name="decode"/>
103 <SEED value="namespace:FanoutCount=2"/>
104 </STEP>
105 </ROUTE>
106 </RULE>
```

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S CODE FAILS TO SYNCHRONIZE BETWEEN MASTER AND SLAVE



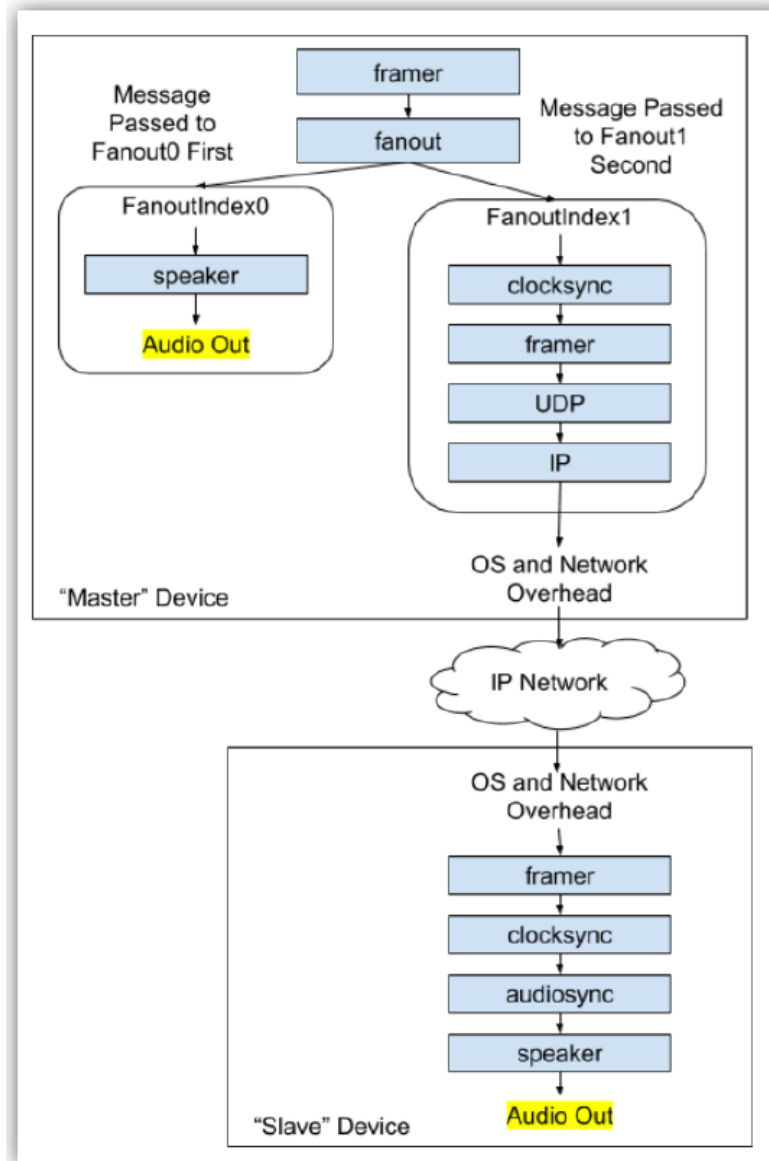
Dr. Roman Chertov
Sonos's Expert Witness

65. Based on my evaluation, it is my opinion that the Implicit Source Code was designed to run the `Fanout0` path in `pcmserveraudio.rule` to completion before starting the `Fanout1` path. This understanding is based on several different aspects of the code.

71. I further note that, even if the Implicit Source Code was designed to run the `Fanout1` path in the `pcmserveraudio.rule` file in parallel with the `Fanout0` path (which I have seen no evidence of), this does not change my opinion that the “master” device begins rendering an audio frame before sending that audio frame to the “slave” device – and thus before the “slave” could ever begin rendering that audio frame. There are several reasons for this.

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S CODE FAILS TO SYNCHRONIZE BETWEEN MASTER AND SLAVE



IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S GOAL WAS SYNCHRONIZING BETWEEN SLAVES

Using Strings to Compose Applications from Reusable Components

BeCom Corporation
info@becom.com
October 4, 2001

The past decade in technology has evidenced an explosive growth in the proliferation of new web and network services, digital data and content, new and increasingly diverse computing devices and new wired and wireless networks for data communication. With the billions investments in hardware infrastructure, software infrastructure and network infrastructure, not only do end users want to see new applications, but ISVs and OEMs must deliver these applications in a way that assures managements demands for an ever-increasing return on investment. As a result, this changing landscape has imposed a critical challenge on software developers and IT managers to improve time to market and reduce development costs. To leverage this existing investment in hardware, network and software infrastructure, applications must be developed to capitalize on new revenue opportunities while also reducing existing operating expenses. The key observation made by many is that reuse of technology assets to compose new applications is critical to addressing these challenges.

There are, however, several issues that make reuse difficult to achieve. First, reuse is an easily misunderstood concept, as it is not simply using previously engineered or acquired technology asset more than once. It requires reuse engineering that prepares technology assets to be reusable. Second, identifying what software components can be reused is a confusing process, as traditional design approaches tend to prevent reuse outside the narrow scope or domain in which they were initially developed. Finally, software engineering techniques and methodologies alone are insufficient tools for developers to achieve true reuse. Even with the best development methodologies and architectural techniques, taking a traditional approach to development can reduce the potential for reusing technology assets or limit the type of potential reuse, without later additional re-engineering.

To address these issues requires an application framework that promotes reuse at every level: from fine-grained application components to large-scale back-end systems. The lack of a solid application framework for reuse has prevented it from being widely accepted and implemented. Such a framework needs to yield solutions that are *dynamic* enough to adapt reusable components to changing networks, media types, and device types at runtime in unanticipated ways, *distributed* so as to leverage services within the WAN and LAN, and *efficient* enough to run on network servers and resource-constrained embedded devices alike. While traditional techniques (e.g., object-oriented design and component systems with the appropriate extensions to support client/server systems) and methodologies (e.g., UML that aids in the specification and design of systems) have various strengths, they have two inherent limitations that prevent the developer from creating solutions that are truly reusable. These limitations are as follows:

Limitation 1: Imperatively Defined Configuration Intelligence
In traditional application development, the software developer explicitly identifies a set of required services and specifies how and when to interface with them. This necessitates that developers have a priori knowledge of all services that are to be used directly by their component. This reassignment of configuration intelligence with application logic transcends from the so-called "main loop" of the application down to granular components composing the application. While conventional OO component techniques enable the developer to be oblivious of an algorithm's implementation details, they are still required to know the interfaces, and to write code that depends on those interfaces to meet the goals of the application. Consequently, it is difficult to chain together services to achieve a desired goal without explicitly specifying all possible configurations in advance. Therefore, in practice, imperatively defining the configuration intelligence limits the application to a static set of predefined interactions.

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Page 1 of 10

Page 1 of 10

Implicit Exhibit 2021

Sonos v. Implicit, IPR2018-0766, -0767

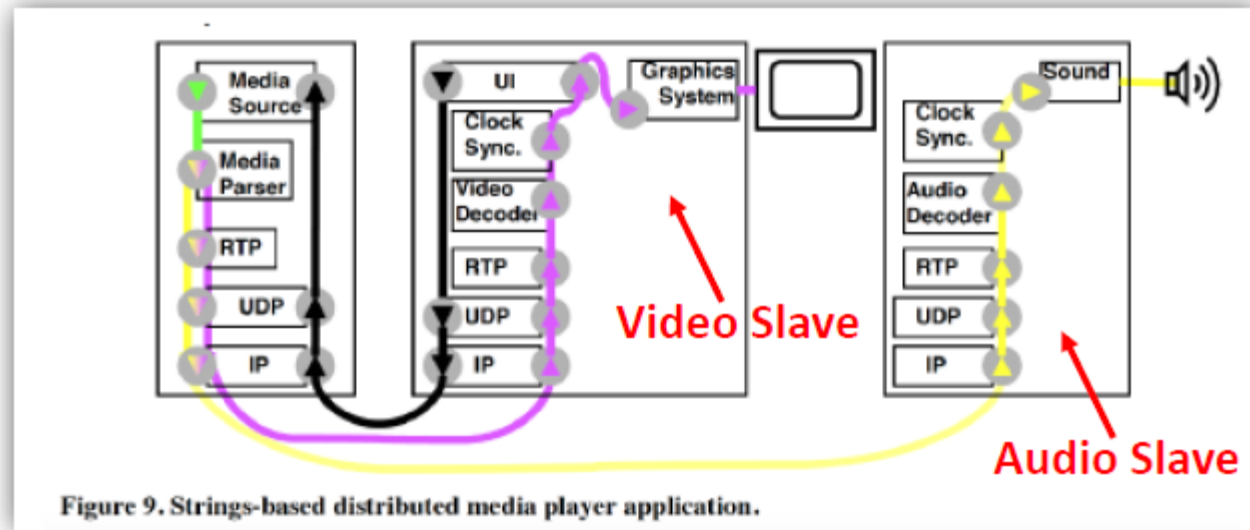
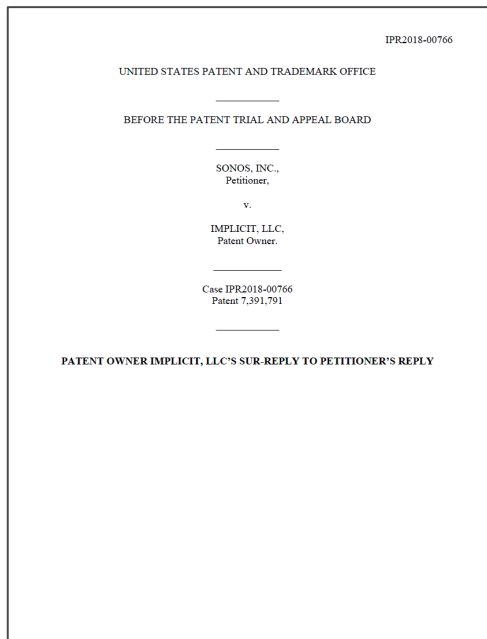


Figure 9. Strings-based distributed media player application.

Strings Whitepaper
Ex. 2021

IPR2018-00766, -00767 EXHIBIT 1028

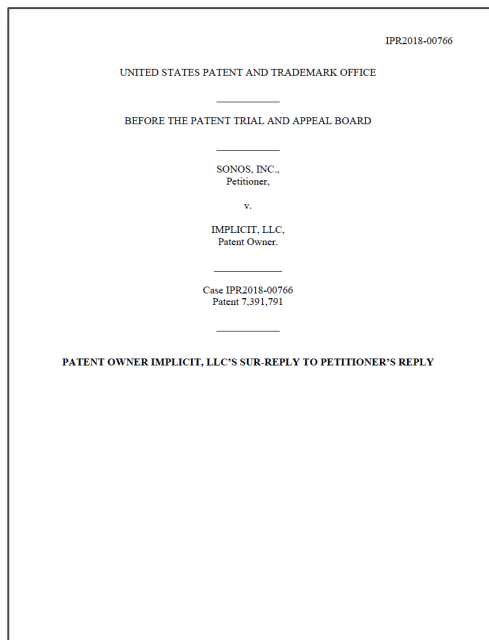
IMPLICIT'S SUR-REPLY ON "SYNCHRONIZATION"



Implicit Sur-Reply
'791 Patent

Sonos's second position—that the source code does not provide actual "synchronization," which all the claims allegedly require—simply lacks credibility. Substantial contemporaneous documents and source code show "synchronization," examples including:

IMPLICIT'S SUR-REPLY ON "SYNCHRONIZATION"

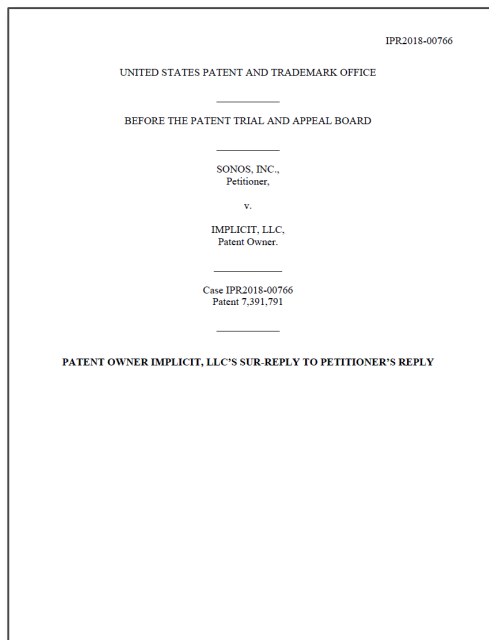


Implicit Sur-Reply
'791 Patent

opinion also does not match the facts from the source code. Dr. Chertov opines that the “master” and “slave” will not play in “sync” because the source code always has the “master” play the content first (on Fanout 0) and the “slave” plays second (on Fanout 1), and that a scenario in which the slave plays first is “impossible.” Exhibit

2094, at 26:14–217. But the source code contains embodiments in which the slave is the local device on Fanout 0 (and thus “plays first”) and the master is a remote device on Fanout 1 (and thus plays second). Exhibit 2063; Exhibit 2067. In those

IMPLICIT'S SUR-REPLY ON "SYNCHRONIZATION"



Implicit Sur-Reply
'791 Patent

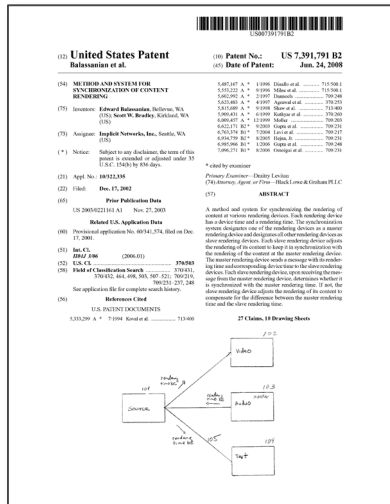
Moreover, nothing in the Patents requires the level of synchronization Sonos asserts. The '791 Patent claims only state: "A method for synchronizing a rendering of a content" in the preamble and "to render content that should be rendered at the same time" in the body (claim 1). Other claims are similar. *E.g.*, claim 23 ("synchronizing" in the preamble; "rendering content... to account for the calculated time domain difference" in the body). No claim requires absolute synchronization (something only theoretically possible). Nor does the specification. Sonos's reading of the claims would exclude essentially all real-world devices.

- The code fails to synchronize between a master and a slave
- The code fails to meet “rendering time”

IMPLICIT'S CODE FAILS TO MEET "RENDERING TIME" LIMITATIONS

DETAILED DESCRIPTION

A method and system for synchronizing the rendering of content at various rendering devices is provided. In one embodiment, each rendering device has a device time and a rendering time. The device time is the time as indicated by a designated clock (e.g., system clock) of the rendering device. The rendering time is the time represented by the amount of content that has been rendered by that rendering device. For example, if a rendering device is displaying 30 frames of video per second, then the rendering time will be 15 seconds after 450 frames are displayed. The rendering time of content



'791 Patent

IMPLICIT'S CODE FAILS TO MEET "RENDERING TIME" LIMITATIONS



Dr. Atif Hashmi
Implicit's Expert Witness

16. For the purposes of my analysis in this declaration, I applied the claim constructions that Dr. Chertov applied (Sonos Exhibit 1009 in the IPR of the '791

- Q. So what construction of rendering time did you use?
- A. So I used the construction that is on page 28 of Sonos Exhibit 1009 for the 791 patent, which states, "A **time measure** of the amount of content that has already been rendered by a given rendering device."

IMPLICIT'S CODE FAILS TO MEET "RENDERING TIME" LIMITATIONS



CONTAINS PROTECTIVE ORDER MATERIAL

U.S. Patent No. 7,391,791	Application of Claim Language to Source Code
Claim 1	
[1 Preamble] A method for synchronizing a rendering of a content provided by a source at one or more devices which are nodes of a network, the content having a rendering time, the method comprising:	<p>Source Code Folders:</p> <pre>SOFTWARE_STRUCTURE = \test\demo\rules\ SOFTWARE_CODE = \beads\</pre> <p>The Implicit Source Code specifies a distributed system consisting of a plurality of devices that are nodes of a network ("devices"). These devices execute a method for synchronizing rendering of content provided by a source, where each device maintains a rendering time corresponding to the device time when the device renders content.</p> <p>An architecture of one such distributed system comprising a plurality of devices is defined in files videomulti.rule, videoclient.rule, spavideo.rule, pcmaudioserver.rule, syncaudio.rule, and timesync.rule.¹²</p>
[1a] designating one of the one or more devices a master device, the master device having a	The distributed system architecture described in file videomulti.rule designates one of the network devices as a master device having a master device time ³ and a master clock that provides the master rendering times. ^{4,5}

¹ These files are contained in folder SOFTWARE_STRUCTURE\

² Another similar distributed system that renders synchronized content streams is described by the rules files in folder \test\audiosync\

³ See file SOFTWARE_STRUCTURE\audiosync\package\package\timesync.rule

⁴ Defined at lines 6 to 133 in file SOFTWARE_STRUCTURE\videomulti.rule

⁵ See lines 10 to 15 in file SOFTWARE_STRUCTURE\videomulti.rule

Dr. Atif Hashmi
'791 Claim Chart
Ex.2081

content stream.⁹ This master rendering clock `IAudioClock` generates a plurality of master rendering times that are indicative of the statuses of the rendering the combined audio and video content stream by the master rendering device at different times.

As described above, the master device sends to the slave devices an indication of when the master device renders content corresponding to the master rendering time. Through invoking the `clocksync` bead, the mater device encodes a master rendering time that indicates when the master device renders.^{78,79} The master device then sends

Each slave rendering device has a slave rendering time. Specifically, each slave device runs a `clocksync` bead that maintains a rendering clock `RenderClock` that tracks the rendering time for the slave device.²⁹ Furthermore, each slave device runs

The slave rendering device runs an `audiosync`^{116,117} bead that calculates, adjusts, and smoothens a rendering time differential between the master rendering device and the slave rendering devices. The `audiosync` bead implements a function `AudioSync_MessageHandler`.¹¹⁸ Function `AudioSync_MessageHandler` obtains the master rendering time¹¹⁹ and the slave rendering time¹²⁰ and calls function

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S CODE FAILS TO MEET "RENDERING TIME" LIMITATIONS

I, Roman Chertov, declare and state as follows:

1. I have been retained as an expert witness for the *Inter Partes* Review ("IPR") of both U.S. Patent No. 7,391,791 (the "'791 Patent") (Ex.1001) and U.S. Patent No. 8,942,252 (the "'252 Patent"), which have been initiated by Sonos, Inc. ("Sonos") against Implicit, LLC ("Implicit").

2. I previously submitted two Declarations dated March 9, 2018 (referred to herein as my "Opening Declarations") in connection with Sonos's Petitions for IPR of the '791 and '252 Patents.

3. I understand that, on September 19, 2018, the Patent Trial and Appeal Board (or "the Board" for short) instituted IPRs of both the '791 Patent and the '252 Patent.

4. I further understand that, on December 18, 2019, Implicit filed its Patent Owner's Responses (or "PORs" for short) in the IPRs of both the '791 Patent and the '252 Patent along with a common set of Exhibits, which include source code produced by Implicit and an Expert Declaration of Atif Hashmi, Ph.D (Ex. 2080-2082) in which Dr. Hashmi expressed his opinion that the source code produced by Implicit practices the Challenged Claims of both the '791 Patent and the '252 Patent.

5. I have now been asked to review the source code produced by Implicit and the Expert Declaration of Dr. Hashmi and then respond to Dr. Hashmi's

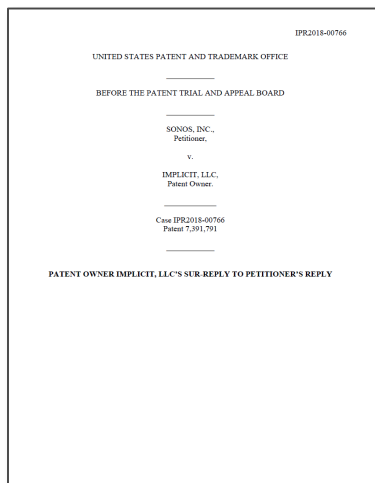
Page 1 of 45
SONOS EXHIBIT 1022
IPR OF U.S. Pat. No. 7,391,791

Dr. Chertov Reply Declaration
Ex.1022

29. In the Claim Chart for the '791 Patent that is included as part of his Expert Declaration, Dr. Hashmi identifies certain aspects of the Implicit Source Code that allegedly practice the "rendering time" limitations of the Challenged Claims of the '791 Patent. However, I disagree that any of these aspects of the Implicit Source Code evidence that "a time measure of the amount of content that has already been rendered by a given rendering device" was maintained at either a "master" device or a "slave" device, or that such a "time measure" was otherwise used in the manner required by the Challenged Claims of the '791 Patent.

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S "RENDERING TIME" ALLEGATIONS ARE A MOVING TARGET



Implicit Sur-Reply
'791 Patent

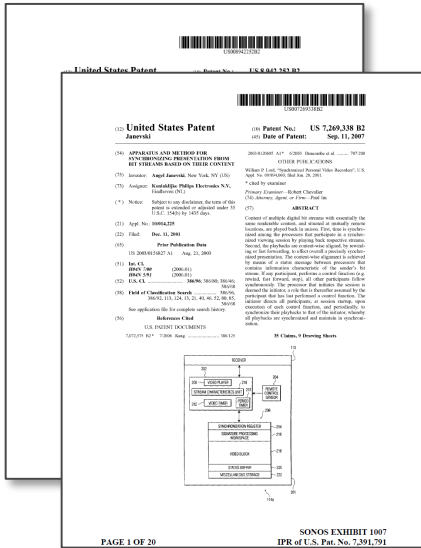
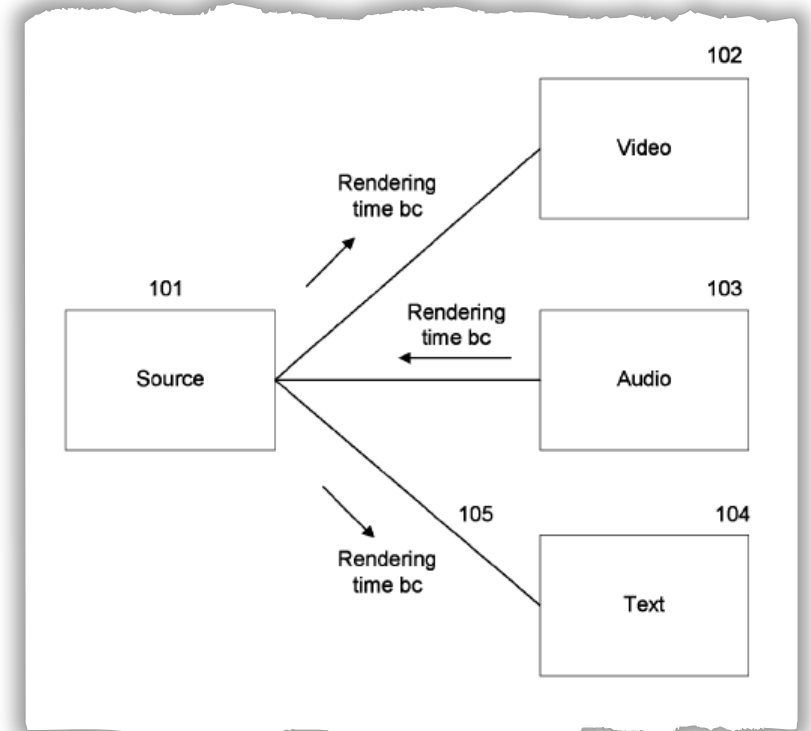
The "Sample" variable within the `sampleclock` data structures identified by Dr. Hashmi is one form of a "rendering time," as Dr. Chertov and Sonos applied that term in the Petition. In seeking review, Sonos and Dr. Chertov contended that

that term. The structures identified by Dr. Hashmi are used to return an "epoch" value that incorporates the amount of time that has elapsed in the content at a particular device. That incorporated value, referred to as "delta" in the Source Code, is the number of samples that have been rendered in the content stream (`sampleclock->Sample`) divided by the frequency of the stream (`sampleclock->Frequency`), scaled to the desired time units (e.g., seconds or milliseconds, depending on the `sampleclock->Divisor` variable).

IMPLICIT PATENT OVERVIEW

Various rendering devices, however, may have different time domains that make the rendering of the multimedia presentation in a synchronized manner difficult. For example,

video and audio rendering devices may have system clocks that operate at slightly different frequencies. As a result, the video and audio content will gradually appear to the person viewing the presentation to be out of synchronization. The rendering of content in a synchronized manner is made even more difficult because some rendering devices may have multiple time domains. For example, an audio rendering device may have a system clock and a clock on a digital signal processing ("DSP") interface card. In such a case, the combination of clocks may result in the presentation becoming even more quickly out of synchronization.



'791 Patent
'252 Patent

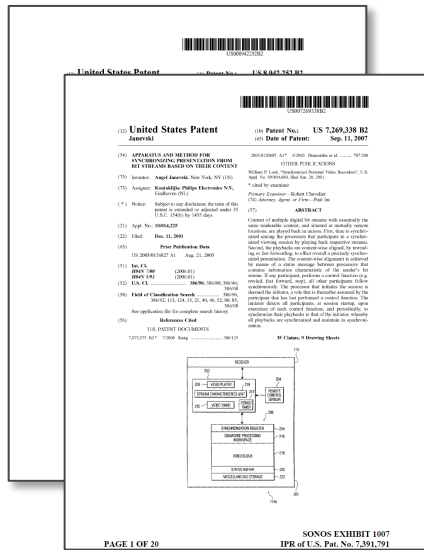
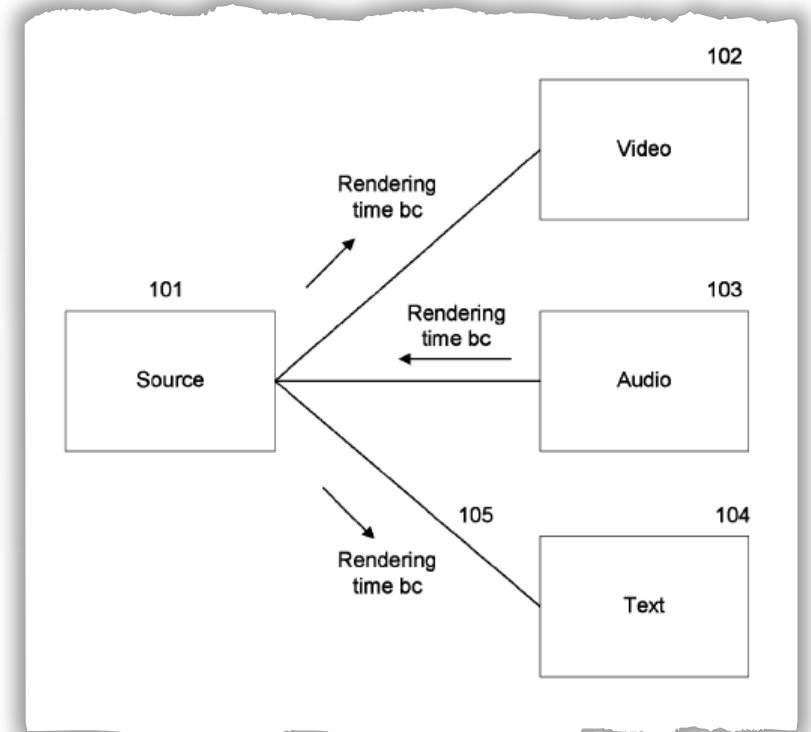
IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT PATENT OVERVIEW

embodiment, each rendering device has a device time and a rendering time. The device time is the time as indicated by a

rendering time. The device time is the time as indicated by a designated clock (e.g., system clock) of the rendering device.

The rendering time is the time represented by the amount of content that has been rendered by that rendering device. For



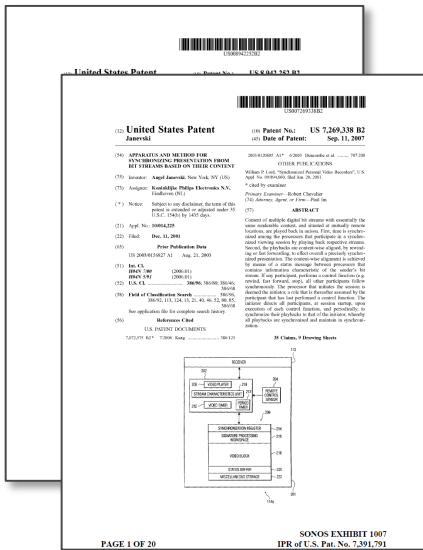
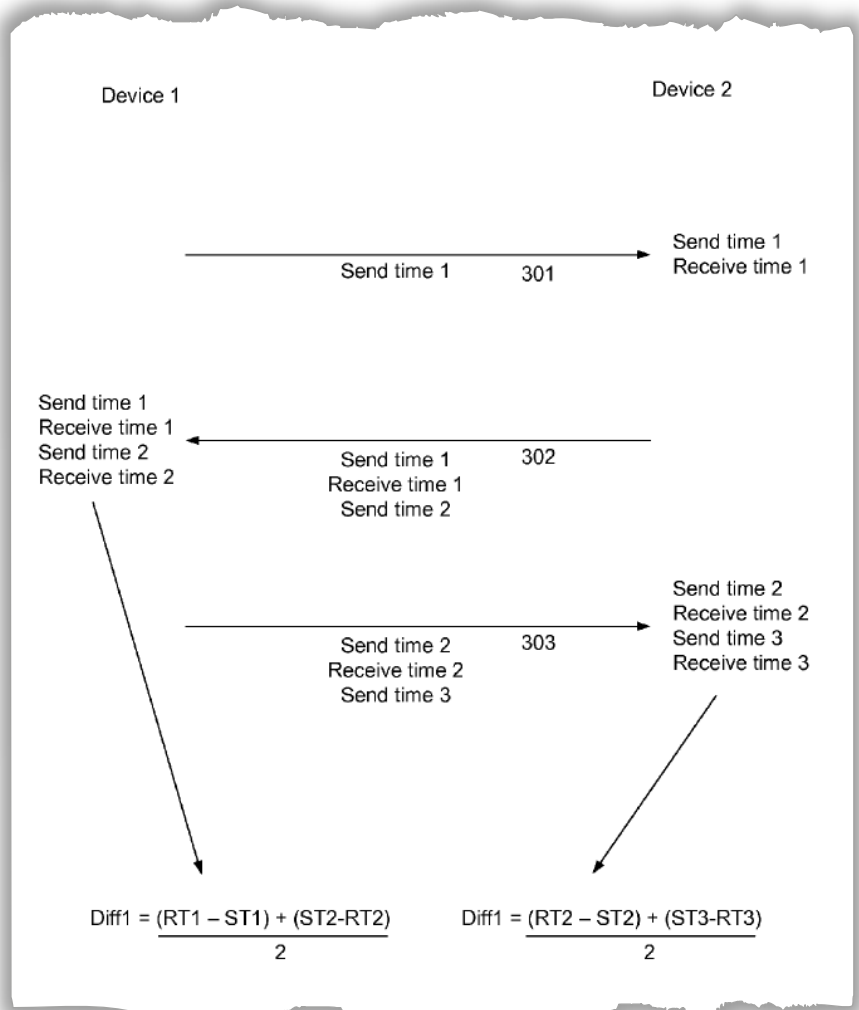
'791 Patent
'252 Patent

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT PATENT OVERVIEW

The synchronization system in one embodiment factors in the differences in the time domains of the various rendering devices when evaluating synchronization. The rendering devices exchange device time information so that the rendering devices can account for the differences in the time domains of the other rendering devices. Each rendering device may send to the other rendering devices a time domain message that includes its current device time (i.e., send time) along with the time it received the last time domain message {i.e., receive time} from each of the other rendering devices and the send time of that last time domain message. When a

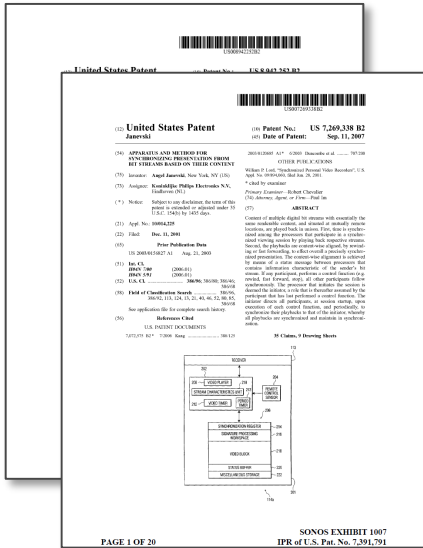
and the send time of that last time domain message. When a rendering device receives such a time domain message, it calculates the time differential between its time domain and the time domain of the sending rendering device. In one embodiment, the synchronization system calculates the time domain differential by combining the difference in send and receive times for the last messages sent to and received from another device in a way that helps factor out the transmission time of the messages. A slave rendering device can then use



'791 Patent
'252 Patent

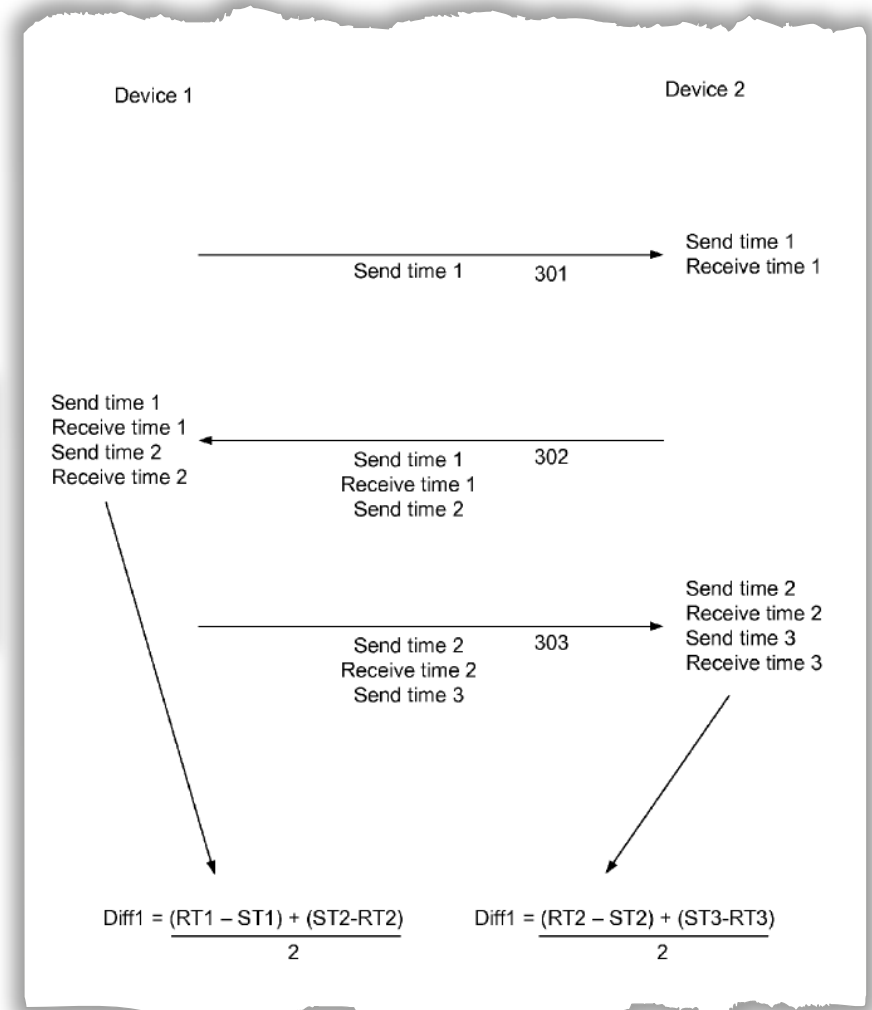
IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT PATENT OVERVIEW



'791 Patent
'252 Patent

the time domain differential. In block 707, the component smoothes the time domain differential. The time domain differential can be smoothed using various techniques such as averaging the last several time domain differentials using a



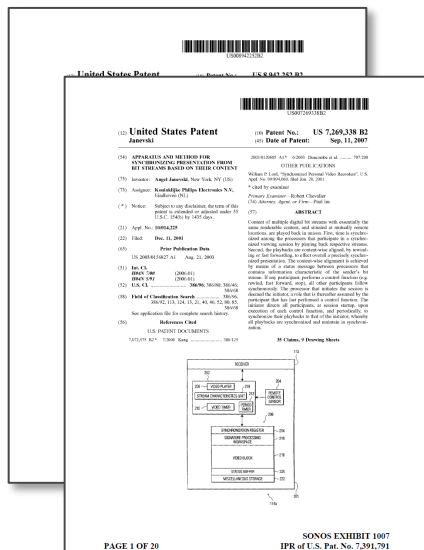
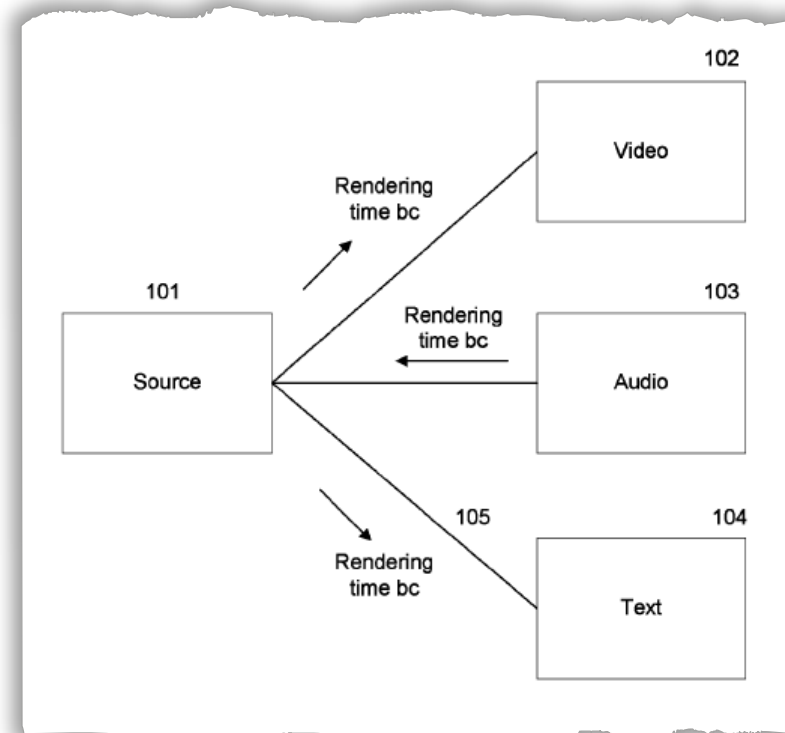
IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT PATENT OVERVIEW

devices. Each slave rendering device adjusts the rendering of its content to keep it in synchronization with the rendering of the content at the master rendering device. The master ren-

designated as slave rendering devices. After the source starts sending the content to the rendering devices, the audio rendering device broadcasts a master rendering time message with its master device time and master rendering time to the slave rendering devices on a periodic basis. In this example,

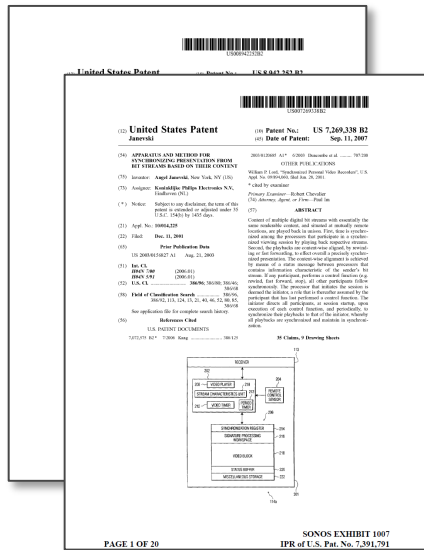
the slave rendering devices. Upon receiving the master rendering time message, the slave rendering devices convert the master device time to their own time domains and then calculate the difference between their slave rendering time and the master rendering time at a certain point in time. In one



'791 Patent
'252 Patent

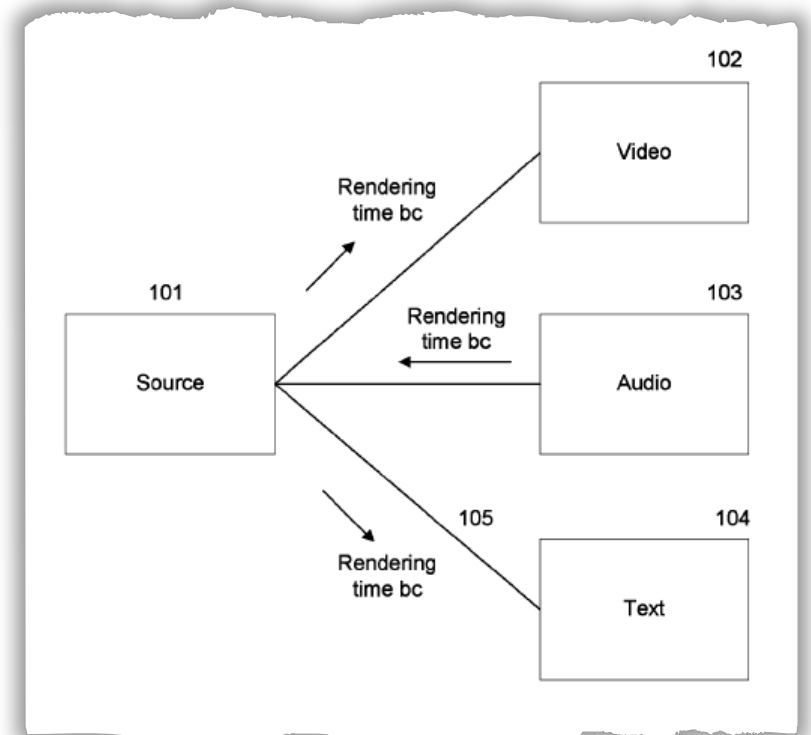
IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT PATENT OVERVIEW



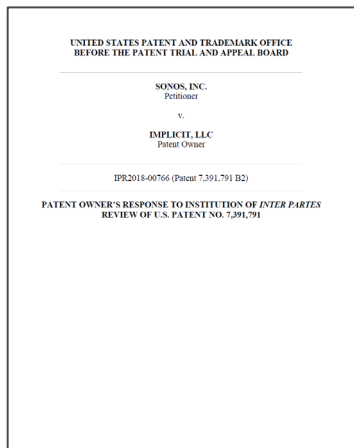
'791 Patent
'252 Patent

at a calculated start of sending as the point in time. The slave rendering devices then adjusts the rendering as appropriate to compensate for the difference. The rendering device adjusts



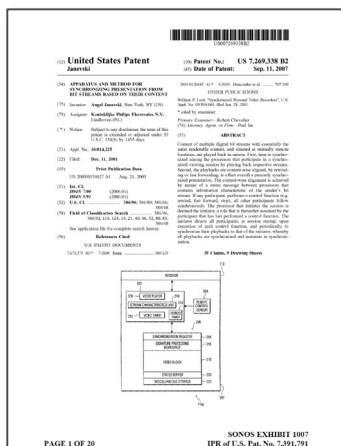
IPR2018-00766, -00767 EXHIBIT 1028

A "TIME DOMAIN" IS NOT LIMITED TO A "DEVICE CLOCK"



Implicit POR
'791 Patent

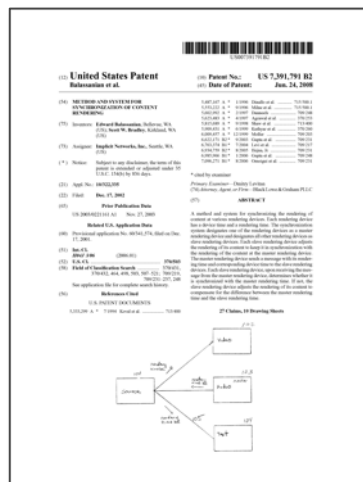
The "time domain" terms should be construed as "the way a device clock tracks time," which Implicit also proposed in the co-pending litigation. Exhibit



'791 Patent
Ex.1001

Various rendering devices, however, may have different time domains that make the rendering of the multimedia presentation in a synchronized manner difficult. For example, video and audio rendering devices may have system clocks that operate at slightly different frequencies. As a result, the video and audio content will gradually appear to the person viewing the presentation to be out of synchronization. The rendering of content in a synchronized manner is made even more difficult because **some rendering devices may have multiple time domains.** For example, an audio rendering device may have a system clock and a clock on a digital signal processing ("DSP") interface card. In such a case, the combination of clocks may result in the presentation becoming even more quickly out of synchronization.

JANEVSKI ANTICIPATES CHALLENGED CLAIM 1



'791 Patent

1. A method for synchronizing a rendering of a content provided by a source at one or more devices which are nodes of a network, the content having a rendering time, the method comprising:

designating one of the one or more devices a master device, the master device having a master device time and a master rendering time;

designating remaining devices among one of the one or more devices as at least one slave device, the at least one slave device having a slave device time and a slave rendering time;

receiving the content for rendering by the master and at least one slave device;

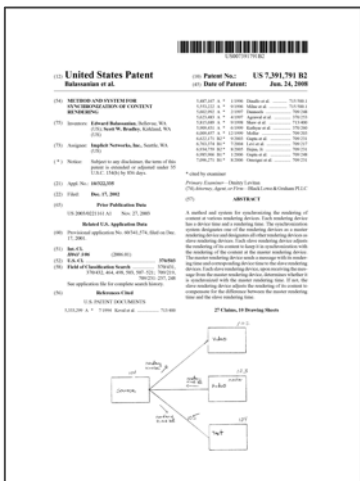
sending from the master device to the at least one slave device an indication of when the master device renders content corresponding to the master rendering time;

determining a master device time domain, a slave device time domain, and a source time domain;

determining whether a time domain differential exists between the master rendering time, the slave rendering time; and

adjusting, based on the received indication, the rendering of the content at the at least one slave device within the slave device time domain and in proportion to the time domain differential when present to account for variation between when the master device and the at least one slave device to render content that should be rendered at the same time.

JANEVSKI ANTICIPATES CHALLENGED CLAIM 16

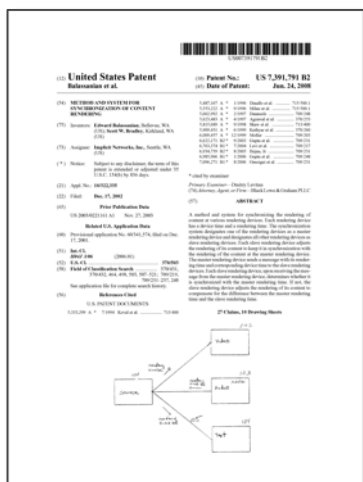


'791 Patent

16. A method for synchronizing rendering of content at devices which are nodes of network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:

- designating one of the devices as a master device having a master rendering time and the one or more slave devices having a slave rendering time;
- sending to each device content to be rendered at that device synchronized with the content sent to the other devices;
- sending from the master device to the one or more slave devices a master device time corresponding to the master rendering time of the master device; and upon receiving the sent master device time at the one or more slave devices,
- exchanging time domain information between the master and one or more slave devices;
- determining at least one time domain differential between the master rendering time and the slave rendering time between the master device and the one or more slave devices;
- adjusting the rendering of the content at the one or more slave devices to account for a difference in the slave rendering time and the master rendering time calculated based on the master device time adjusted for a difference in time domains of the one or more slave devices and the master device.

JANEVSKI ANTICIPATES CHALLENGED CLAIM 23



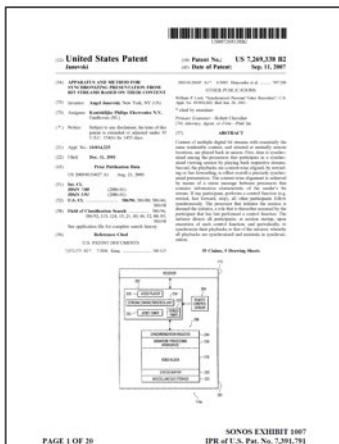
'791 Patent

23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising: designating one of the devices as a master device having a master rendering time and the one or more slave devices having a slave rendering time; and for each slave device, exchanging time domain information between the master and one or more slave devices; calculating a time domain difference between the master rendering time of the master device and the slave rendering time of the slave device based on a master device time adjusted for a difference in time domains of the slave device and the master device; and rendering content at the slave device to account for the calculated time domain difference.

JANEVSKI ANTICIPATES CHALLENGED CLAIM 23



23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:



Janevski
Ex.1007

1. Field of the Invention

The present invention relates generally to digital image playback, and more particularly to techniques for synchronizing playback of two or more digital streams based on renderable content of those streams.

The system 110 preferably has two communication networks associated therewith. The first is an Internet network 118 that interconnects the PVRs 114a, b located at the two different locations (e.g. House 1 and House 2). The Internet network 118 supplies the means 119 for communicating information between the PVRs 114a, b such that synchronization may be achieved. The second communication net-

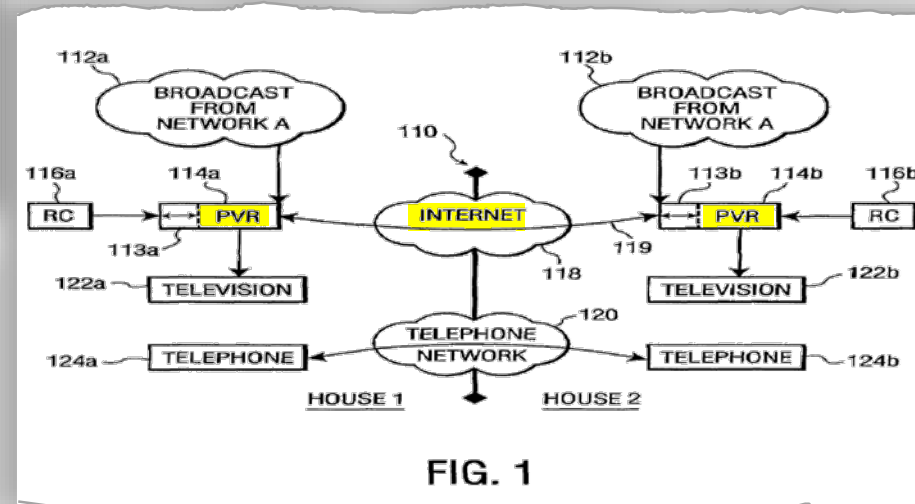


FIG. 1

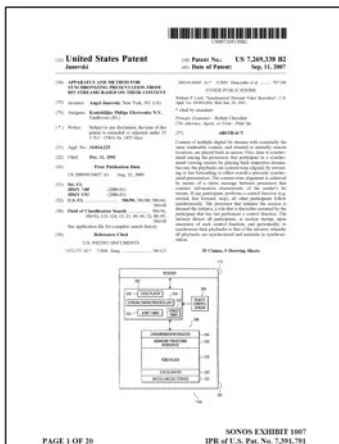
IPR2018-00766, -00767 EXHIBIT 1028

JANEVSKI ANTICIPATES CHALLENGED CLAIM 23



23. A method for synchronizing rendering of content at devices which are nodes of a network, each device having a device time and a rendering time, the device time of a device being in a time domain of the device, the method comprising:

Each PVR keeps track of “the time or frame into the program”:



Janevski
Ex.1007

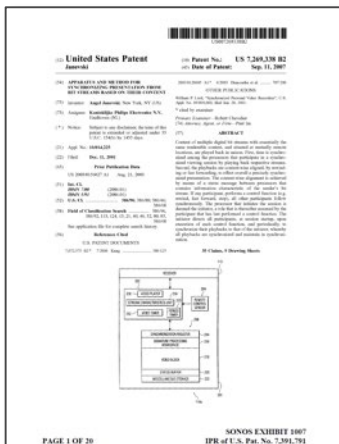
PVR 114a that initiated the session. The status message is also transmitted with each command that is broadcasted in response to a participant performing a control function. The status message includes an indication of the program being watched, the current mode of watching (e.g., normal play, fast forward, pause), an indication of the time into the program, and information characteristic of content of a digital bit stream from which playback to the message sender is being generated. The characteristic information is

to update the synchronization. Within the message is an identifier of the program being watched or to be watched, an indicator of the mode of watching (e.g. normal play, fast forward, pause, etc.), and the time or frame into the program. The time or frame allows the recipient PVR to synchronize its replay with that of the sending PVR, by comparing the time or frame in the message with its own the current time or frame.

JANEVSKI ANTICIPATES CHALLENGED CLAIM 23



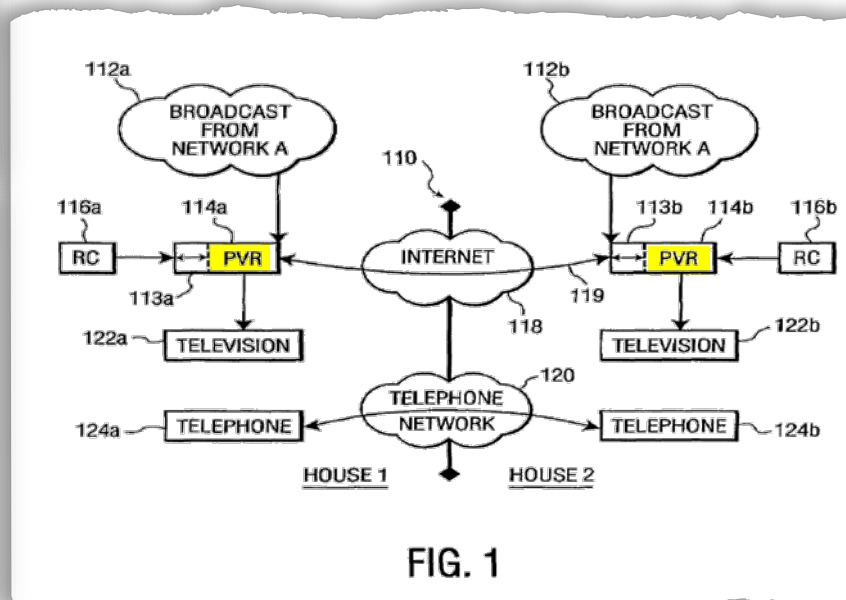
designating one of the devices as a master device having a master rendering time and the one or more slave devices having a slave rendering time; and for each slave device,



Janevski
Ex.1007

Referring to FIG. 1, an example of a synchronized PVR viewing system 110 in accordance with the present invention is illustrated. As illustrated in FIG. 1, broadcasts 112a, b of

PVRs 114a, b. Hereinafter, the suffix “a” refers to the “initiator”, and the suffix “b” refers to a “participant”, in a synchronized viewing session. Initially, the “initiator” is the PVR that starts the session, although that role is handed off to any PVR that, as directed by its user, performs a control function (e.g., stop, pause, fast forward, reverse). All other PVRs participating in the session are “participants”.

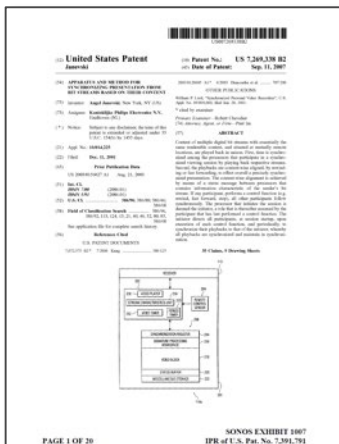
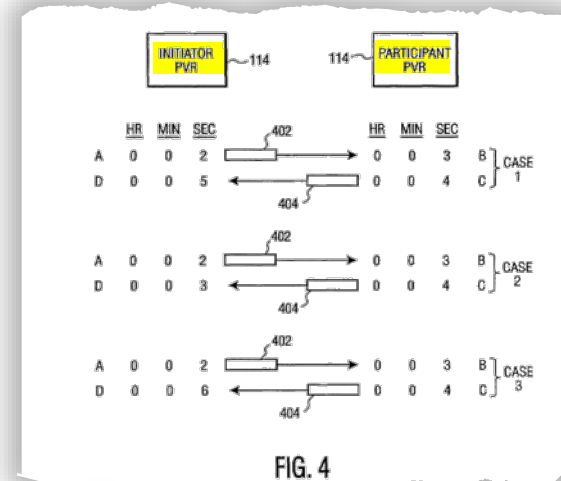


JANEVSKI ANTICIPATES CHALLENGED CLAIM 23



exchanging time domain information between the master and one or more slave devices;

Time synchronization can be implemented in many different known ways. Distributed processors (nodes) in a network can broadcast their respective clock values periodically to maintain synchronization. "Fault-Tolerant Clock Synchronization for Distributed Systems with High Message Delay Variation", Azevedo, Marcelo Moraes de, et. al., Irvine, Calif. (1995). Synchronization messages may be relayed between source and destination processors, where relaying nodes discard messages recognized as coming from a faulty node. "Communication Protocols for Fault-Tolerant



Janevski
Ex.1007

FIG. 4 depicts a possible message flow design in the present invention to determine the misalignment, if any, in the respective timings of the video timers 212 of two PVRs 114a, b, so that the timers can be synchronized. For sim-

In the embodiment depicted herein, time synchronization is performed by the initiator PVR 114a individually with each participant PVR 114b, and involves sending an originating synchronization message 402 from the initiator PVR 114a to a participant PVR 114b and sending a reply synchronization message 404 from the participant PVR 114b to the initiator PVR 114a. It is assumed that the transmission

IPR2018-00766, -00767 EXHIBIT 1028

1. A method, comprising:
a master rendering device rendering a first content stream;
and
sending, from the master rendering device to a first one of a plurality of slave devices, a plurality of master rendering times indicative of statuses of the rendering the first content stream at the master rendering device at different times;
wherein the first slave device is configured to smooth a rendering time differential that exists between the master rendering device and the first slave device in order to render a second content stream at the first slave device synchronously with the rendering of the first content stream at the master rendering device, wherein smoothing the rendering time differential includes calculations using the plurality of master rendering times.

11. A method, comprising:
receiving, at a slave device, a particular content stream;
receiving, at the slave device from a master rendering device, a plurality of master rendering times indicative of status of rendering a different content stream at the master rendering device;
the slave device determining a smoothed rendering time differential that exists between the master rendering device and the slave device, wherein the determining is based on calculations using the plurality of master rendering times and a plurality of slave rendering times corresponding to rendering the particular content stream at the slave device; and
based on the smoothed rendering time differential, the slave device rendering the particular content stream synchronously with the master rendering device rendering the different content stream.

JANEVSKI + ANY "CLOCK SYNCHRONIZATION" REFERENCE RENDERS '252 CLAIMS OBVIOUS

39th IEEE Workshop on Fault-Tol. Proc. and Heter. Syst., College Station, TX, June 19-21 (Appears in Fault-Tol. Proc. and Heter. Syst., D. Zilberstein and D. Arzouby, eds., IEEE Comp. Soc. Press, pp. 269-277)

Fault-Tolerant Clock Synchronization for Distributed Systems with High Message Delay Variation

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University of California, Irvine
Irvine, CA 92717

Abstract

Fault-tolerant clock synchronization is an important requirement in many distributed systems, especially in time-critical and safety-critical applications. Frequently, interactive convergence algorithms are used for fault-tolerant clock synchronization, providing advantages such as fully distributed operation, low message exchange overhead, and simplicity of implementation. This paper presents the measured performance of three interactive convergence clock synchronization algorithms. Our experiments were conducted in a distributed UNIX environment featuring high message delay variations, which poses severe constraints on the clock synchronization tightness that may be achieved. The algorithms that were tested are: FTMA (fault-tolerant midpoint algorithm) [1], AEFTMA (adaptive exponential averaging fault-tolerant midpoint algorithm) [2], and SWA (sliding window algorithm) [3]. Our experimental results indicate that SWA outperforms the other algorithms in this environment, being able to achieve tighter synchronization under different simulated fault conditions. The superiority of SWA can be attributed to its high degree of fault tolerance, combined with its ability to deal gracefully with much longer than expected delays on faults.

1: Introduction

In distributed systems, computers cooperate to provide the expected functionality to a given application. Some tasks that are often found in such systems are: synchronizing activities that occur at different points of the system, enforcing events in time, enforcing deadlines, and measuring elapsed time. A system with one or more of these requirements must use proper synchronization mechanisms to establish an agreed upon global time scale among its components. Particularly in the case of safety-critical applications, synchrony must be maintained in spite of the presence of faults in the system.

Frequently, fault-tolerant clock synchronization is achieved via interactive convergence algorithms in which nodes exchange their clock values and determine their correction terms at regular intervals. This paper presents the measured performance of three interactive convergence algorithms: the sliding window algorithm (SWA) [3], the fault-tolerant midpoint algorithm (FTMA) [1], and the adaptive exponential averaging fault-tolerant midpoint algorithm (AEFTMA) [2]. The measurements were carried out with an application-level implementation running in a distributed UNIX environment [4]. This environment poses some practical constraints to clock synchronization, in particular, a high variation in the

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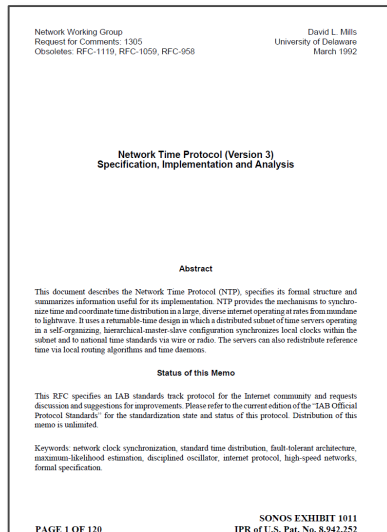
Azevedo
Ex.1010

AEFTMA in the previous round. Instead of using $CORR_{FTMA}^r$ as the correction term during the r th resynchronization interval, AEFTMA computes its correction term by $CORR_{AEFTMA}^r = \beta(r) \cdot CORR_{FTMA}^r + (1 - \beta(r))CORR_{AEFTMA}^{r-1}$.

$\beta(r)$ is a weight factor in effect during the r th resynchronization interval, such that $0 \leq \beta(r) \leq 1$. The weight factor "smooths" the clock correction term $CORR_{AEFTMA}^r$, introducing a lagging mechanism between adjacent rounds. This approach follows the assumption that correction terms of similar magnitude are expected at every resynchroniza-

IPR2018-00766, -00767 EXHIBIT 1028

JANEVSKI + ANY "CLOCK SYNCHRONIZATION" REFERENCE RENDERS '252 CLAIMS OBVIOUS



Mills
Ex.1011

```
peer.dispersion ← min( $\epsilon_0 + \epsilon_\sigma$ , NTP.MAXDISPERSE);  
end clock-filter procedure
```

The peer.offset and peer.delay variables represent the clock offset and roundtrip delay of the local clock relative to the peer clock. Both of these are precision quantities and can usually be averaged over long intervals in order to improve accuracy and stability without bias accumulation (see Appendix H). The peer.dispersion variable represents the maximum error due to measurement error, skew-error accumulation and sample variance. All three variables are used in the clock-selection and clock-combining procedures to select the peer clock(s) used for synchronization and to maximize the accuracy and stability of the indications.

4.2. Clock-Selection Procedure

The clock-selection procedure uses the peer variables Θ , Δ , E and τ and is called when these variables

IPR2018-00766, -00767 EXHIBIT 1028

JANEVSKI + ANY "CLOCK SYNCHRONIZATION" REFERENCE RENDERS '252 CLAIMS OBVIOUS



Berthaud
Ex.1012

Then, an estimation is produced from that set of observation(s), and several tools may be used in the estimation evaluation process, such as: mean value, weighted average, linear regression, midpoint functions, etc. (see [8] and [9] for an ex-

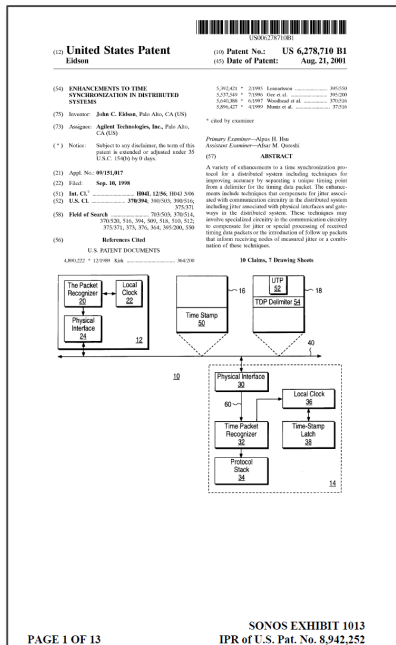
At last, the estimation is used to determine the amount by which a slave should adjust its local clock. To reflect the adjustment, it can either modify its local clock, either produce a virtual clock that is calculated by adding that amount to the local clock. The new value output by the system is then considered valid until the next synchronization round. In both cases, the adjustment can be discrete [2], [4], [5], [8], or can be introduced continuously [3], [8].

JANEVSKI + ANY "CLOCK SYNCHRONIZATION" REFERENCE RENDERS '252 CLAIMS OBVIOUS

systems. More particularly, this invention relates to enhancements to time synchronization in distributed systems.

One method for reducing the negative effects of jitter introduced by the physical interface 30 is to average the differences computed between the time value in the time-stamp latch 38 and the time-stamp 50 for a number of timing data packet and corresponding follow up packet pairs. This computed average may then be used to adjust the local clock 36.

For example, the time packet recognizer 20 may generate a timing data packet once per second along with a corresponding follow up packet. The time packet recognizer 32 latches a time value from the local clock 36 upon detection of each UTP of the received timing data packets and then computes a difference between the latched time value and the time-stamp contained in the corresponding follow up packet. These differences are then averaged for, for example, 10 timing data packets, and the averaged result is then used to adjust the local clock 36. The averaging may be performed by the time packet recognizer 32 or by processor associated with the protocol stack 34.



Edison
Ex.1013

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S CODE FAILS TO SYNCHRONIZE BETWEEN MASTER AND SLAVE

```
1 /Users/implicit/Desktop/Source Code/200.../rules/pcmserveraudio.rule Page 1/3
2 Saved: 10/28/01, 4:35:40 PM Printed for: Implicit
3
4 <!DOCTYPE RULES PUBLIC "-//BECOMM//DTD Rules V0.9//EN" '' [
5 ]>
6 <RULES>
7   <RULE>
8     <DESCRIPTION xml:lang="en">Port 9012 : unsync A+B</DESCRIPTION>
9     <PREDICATE value="query:(Content-Type=='TCP/Decode/Output' OR
10    Content-Type=='UDP/Decode/Output') AND Network-Port-Local==9012"/>
11     <ROUTE>
12       <STEP>
13         <BEAD name="framer"/>
14         <EDGE name="decode"/>
15       </STEP>
16       <SEED
17         value="namespace:Content-Type='audio/pcm',AudioContext=pcmcontext:"/>
18     </ROUTE>
19   </RULE>
20   <RULE>
21     <DESCRIPTION xml:lang="en">Fanout0 : master audio</DESCRIPTION>
22     <PREDICATE value="query:FanoutIndex==0 AND
23    Network-Port-Local==9012"/>
24     <ROUTE>
25       <STEP>
26         <BEAD name="speaker"/>
27         <EDGE name="encode"/>
28       </STEP>
29     </ROUTE>
30   </RULE>
31   <RULE>
32     <DESCRIPTION xml:lang="en">9012 Fanout1: broadcast</DESCRIPTION>
33     <PREDICATE value="query:FanoutIndex==1 AND
34    Network-Port-Local==9012"/>
35     <ROUTE>
36       <STEP>
37         <BEAD name="framer"/>
38         <EDGE name="encode"/>
39       </STEP>
40     </ROUTE>
41   </RULE>
42   <RULE>
43     <DESCRIPTION xml:lang="en">9012 Fanout2: broadcast</DESCRIPTION>
44     <PREDICATE value="query:FanoutIndex==2 AND
45    Network-Port-Local==9012"/>
46     <ROUTE>
47       <STEP>
48         <BEAD name="framer"/>
49         <EDGE name="encode"/>
50       </STEP>
51     </ROUTE>
52   </RULE>
53   <RULE>
54     <DESCRIPTION xml:lang="en">9012 Fanout3: broadcast</DESCRIPTION>
55     <PREDICATE value="query:FanoutIndex==3 AND
56    Network-Port-Local==9012"/>
57     <ROUTE>
58       <STEP>
59         <BEAD name="framer"/>
60         <EDGE name="encode"/>
61       </STEP>
62     </ROUTE>
63   </RULE>
64   <RULE>
65     <DESCRIPTION xml:lang="en">9012 Fanout4: broadcast</DESCRIPTION>
66     <PREDICATE value="query:FanoutIndex==4 AND
67    Network-Port-Local==9012"/>
68     <ROUTE>
69       <STEP>
70         <BEAD name="framer"/>
71         <EDGE name="encode"/>
72       </STEP>
73     </ROUTE>
74   </RULE>
75   <RULE>
76     <DESCRIPTION xml:lang="en">9012 Fanout5: broadcast</DESCRIPTION>
77     <PREDICATE value="query:FanoutIndex==5 AND
78    Network-Port-Local==9012"/>
79     <ROUTE>
80       <STEP>
81         <BEAD name="framer"/>
82         <EDGE name="encode"/>
83       </STEP>
84     </ROUTE>
85   </RULE>
86   <RULE>
87     <DESCRIPTION xml:lang="en">9012 Fanout6: broadcast</DESCRIPTION>
88     <PREDICATE value="query:FanoutIndex==6 AND
89    Network-Port-Local==9012"/>
90     <ROUTE>
91       <STEP>
92         <BEAD name="framer"/>
93         <EDGE name="encode"/>
94       </STEP>
95     </ROUTE>
96   </RULE>
97   <RULE>
98     <DESCRIPTION xml:lang="en">9012 Fanout7: broadcast</DESCRIPTION>
99     <PREDICATE value="query:FanoutIndex==7 AND
100    Network-Port-Local==9012"/>
101    <ROUTE>
102      <STEP>
103        <BEAD name="framer"/>
104        <EDGE name="encode"/>
105      </STEP>
106    </ROUTE>
107  </RULES>
108 </>
109
110 Page 1 of 3 Implicit Exhibit 2065
111 Sonos v. Implicit, IPR2018-0766, -0767
```

pcmserveraudio.rule
Ex. 2065

/Users/implicit/Desktop/Source Code/200.../rules/pcmserveraudio.rule Page 1/3
Saved: 10/28/01, 4:35:40 PM Printed for: Implicit

```
1 <!DOCTYPE RULES PUBLIC "-//BECOMM//DTD Rules V0.9//EN" '' [
2
3 ]>
4 <RULES>
5   <RULE>
6     <DESCRIPTION xml:lang="en">Port 9012 : unsync A+B</DESCRIPTION>
7     <PREDICATE value="query:(Content-Type=='TCP/Decode/Output' OR
8    Content-Type=='UDP/Decode/Output') AND Network-Port-Local==9012"/>
9     <ROUTE>
10      <STEP>
11        <BEAD name="framer"/>
12        <EDGE name="decode"/>
13      </STEP>
14      <SEED
15        value="namespace:Content-Type='audio/pcm',AudioContext=pcmcontext:"/>
16    </ROUTE>
17  </RULE>
18  <RULE>
19    <DESCRIPTION xml:lang="en">Fanout0 : master audio</DESCRIPTION>
20    <PREDICATE value="query:FanoutIndex==0 AND
21    Network-Port-Local==9012"/>
22    <ROUTE>
23      <STEP>
24        <BEAD name="speaker"/>
25        <EDGE name="encode"/>
26      </STEP>
27    </ROUTE>
28  </RULE>
29  <RULE>
30    <DESCRIPTION xml:lang="en">9012 Fanout1: broadcast</DESCRIPTION>
31    <PREDICATE value="query:FanoutIndex==1 AND
32    Network-Port-Local==9012"/>
33    <ROUTE>
34      <STEP>
35        <BEAD name="framer"/>
36        <EDGE name="encode"/>
37      </STEP>
38    </ROUTE>
39  </RULE>
40  <RULE>
41    <DESCRIPTION xml:lang="en">9012 Fanout2: broadcast</DESCRIPTION>
42    <PREDICATE value="query:FanoutIndex==2 AND
43    Network-Port-Local==9012"/>
44    <ROUTE>
45      <STEP>
46        <BEAD name="framer"/>
47        <EDGE name="encode"/>
48      </STEP>
49    </ROUTE>
50  </RULE>
51  <RULE>
52    <DESCRIPTION xml:lang="en">9012 Fanout3: broadcast</DESCRIPTION>
53    <PREDICATE value="query:FanoutIndex==3 AND
54    Network-Port-Local==9012"/>
55    <ROUTE>
56      <STEP>
57        <BEAD name="framer"/>
58        <EDGE name="encode"/>
59      </STEP>
60    </ROUTE>
61  </RULE>
62  <RULE>
63    <DESCRIPTION xml:lang="en">9012 Fanout4: broadcast</DESCRIPTION>
64    <PREDICATE value="query:FanoutIndex==4 AND
65    Network-Port-Local==9012"/>
66    <ROUTE>
67      <STEP>
68        <BEAD name="framer"/>
69        <EDGE name="encode"/>
70      </STEP>
71    </ROUTE>
72  </RULE>
73  <RULE>
74    <DESCRIPTION xml:lang="en">9012 Fanout5: broadcast</DESCRIPTION>
75    <PREDICATE value="query:FanoutIndex==5 AND
76    Network-Port-Local==9012"/>
77    <ROUTE>
78      <STEP>
79        <BEAD name="framer"/>
80        <EDGE name="encode"/>
81      </STEP>
82    </ROUTE>
83  </RULE>
84  <RULE>
85    <DESCRIPTION xml:lang="en">9012 Fanout6: broadcast</DESCRIPTION>
86    <PREDICATE value="query:FanoutIndex==6 AND
87    Network-Port-Local==9012"/>
88    <ROUTE>
89      <STEP>
90        <BEAD name="framer"/>
91        <EDGE name="encode"/>
92      </STEP>
93    </ROUTE>
94  </RULE>
95  <RULE>
96    <DESCRIPTION xml:lang="en">9012 Fanout7: broadcast</DESCRIPTION>
97    <PREDICATE value="query:FanoutIndex==7 AND
98    Network-Port-Local==9012"/>
99    <ROUTE>
100     <STEP>
101       <BEAD name="framer"/>
102       <EDGE name="encode"/>
103     </STEP>
104   </ROUTE>
105 </RULES>
106 </>
```

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S CODE FAILS TO SYNCHRONIZE BETWEEN MASTER AND SLAVE



Dr. Roman Chertov
Sonos's Expert Witness

63. The Fanout0 path only includes a single bead, which is the speaker bead. Ex. 2065 at ln. 73-82. When the decoded PCM audio frame is passed to this speaker bead, it causes the “master” device to render the decoded PCM audio frame by writing it to the “master” device’s audio output device via a `Speaker_DeviceWrite` function. *Id.*; Ex. 2051 at ln. 512-526. Further, immediately before writing the PCM audio frame to the “master” device’s audio output device, the speaker bead calls an `Update` function via the `ISampleClock` interface structure. This causes an update of a `SAMPLECLOCK` data structure at the “master” device, which involves copying the current system time of the “master” device into the `Time` variable of the `SAMPLECLOCK` data structure in a manner similar to that described above. Ex. 2051 at ln. 484-506.³

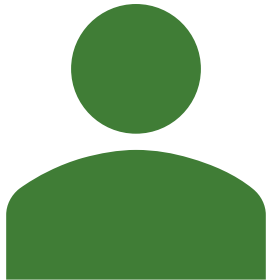
Chertov Dec. (Ex.1022) ¶63; Sonos Reply, p. 18.

```
73 |         <DESCRIPTION xml:lang="en">Fanout0 : master audio</DESCRIPTION>
74 |         <PREDICATE value="query:FanoutIndex==0 AND
... | Network-Port-Local==9013"/>
75 |         <ROUTE>
76 |             <STEP>
77 |                 <BEAD name="speaker"/>
78 |                 <EDGE name="encode"/>
79 |                 <SEED value="namespace:RenderClock=sampleclock:MASTER"/>
80 |             </STEP>
```

*pcmserveraudio.rule (Ex.2065);
Sonos Reply, p. 18.*

IPR2018-00766, -00767 EXHIBIT 1028

IMPLICIT'S CODE FAILS TO SYNCHRONIZE BETWEEN MASTER AND SLAVE



Dr. Roman Chertov
Sonos's Expert Witness

64. In turn, the Fanout1 path is comprised of four beads that are run in sequential order. Ex. 2065 at ln. 84-107. A high-level summary of the operations performed by this sequence of beads when the Fanout1 path is run is as follows:

- First, when the decoded PCM audio frame is passed to the `clocksync` bead, it functions to determine a “master epoch” for the decoded PCM audio frame in the manner described above – which involves reading the `SAMPLECLOCK` data structure updated by the `speaker` bead in the `Fanout0` path and then passing that `SAMPLECLOCK` data structure into the `EpochGet` function defined in the `sampleclock.c` file – and then encode that “master epoch” into a message that is to be sent to the “slave” device. Ex. 2065 at ln. 88-92; Ex. 2020 at ln. 280-292; Ex. 2086 at ln. 475-545.
- Second, the `framer` bead functions to encode the previously-decoded PCM audio frame into the message that is to be sent to the “slave” device. Ex. 2065 at ln. 93-96; Ex. 2047.
- Third, the `UDP` bead functions to format the message according to the User Datagram Protocol (UDP). Ex. 2065 at ln. 97-101; Ex. 2053. The same bead also sends a UDP message via `UdpSend` function, which will transmit the message over an IP network to a “slave” device having an IP address of 10.1.1.55. Ex. 2065 at ln. 102-105;
- Fourth, the `IP` bead is responsible for “holding (and sharing) the IP addressing information so the `Pstcp` and `PsUdp` protocols can access it.” Ex. 1026 at ln. 14-16.

Chertov Dec. (Ex.1022) ¶164; Sonos Reply, p. 18.

```
84 <RULE>
85 <DESCRIPTION xml:lang="en">9013 Fanout1: broadcast</DESCRIPTION>
86 <PREDICATE value="query:fanoutindex==1 AND
... Network-Port-Local==9013"/>
87 <ROUTE>
88 <STEP>
89 <BEAD name="clocksync"/>
90 <EDGE name="encode"/>
91 <SEED value="namespace:RenderClock=sampleclock:"/>
2 </STEP>
3 <STEP>
4 <BEAD name="framer"/>
5 <EDGE name="encode"/>
6 </STEP>
7 <STEP>
8 <BEAD name="UDP"/>
9 <EDGE name="encode"/>
9 <SEED
value="namespace:Network-Port-Remote=9002,Network-Address-Remote=ipv4:10.1
.1.55,Network-Port-Local=0,Network-Address-Local=0"/>
1 </STEP>
2 <STEP>
3 <BEAD name="IP"/>
4 <EDGE name="Encode"/>
5 </STEP>
6 </ROUTE>
7 </RULE>
```

pcmserveraudio.rule (Ex.2065);
Sonos Reply, p. 18.

IPR2018-00766, -00767 EXHIBIT 1028

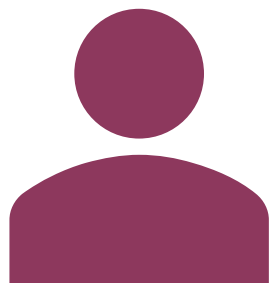


Dr. Atif Hashmi
Implicit's Expert Witness

Q. Can you point me to somewhere in your declaration, Exhibit 2080, where you offer an opinion regarding the reduction to practice?

A. **I don't offer an opinion on reduction to practice.**

*Hashmi Dep. (Ex.1020) at 19:10-15;
Sonos Reply, p. 12.*



Dr. Atif Hashmi
Implicit's Expert Witness

Q. Do you have an opinion on whether the inventions of the 791 and 252 patents were reduced to practice?

A. **I did not run the source code.**

*Hashmi Dep. (Ex.1020) at 17:17-22 (obj's. omitted) ;
Sonos Reply, p. 12.*

Q. [D]o you know for certain that the source code could be compiled and run successfully?

A. **I did not compile the code. I did not run the code. I don't have an opinion on that.**

*Hashmi Dep. (Ex.1020) at 22:2-6 ;
Sonos Reply, p. 12.*
IPR2018-00766, -00767, EXHIBIT 1028

IMPLICIT'S CODE FAILS TO MEET "RENDERING TIME" LIMITATIONS



Dr. Roman Chertov
Sonos's Expert Witness

U.S. Patent No. 7,391,791	Application of Claim Language to Source Code
master device time and a master rendering time;	<p>This master rendering device receives a combined audio and video content stream sent by a source device.⁶ Upon receiving the combined audio and video content stream, the master rendering device uses the <code>avidemux</code> bead to separate the combined audio and video stream into separate audio and video streams.^{7,8} The <code>avidemux</code> bead instantiates a master rendering clock <code>IAudioClock</code> associated with the audio content stream.⁹ This master rendering clock <code>IAudioClock</code> generates a plurality of master rendering times that are indicative of the of statuses of the rendering the combined audio and video content stream by the master rendering device at different times.</p>

31. First, I disagree with Dr. Hashmi's contention that `IAudioClock` is a "clock" that "generates" time values. To the contrary, the Implicit Source Code makes clear that `IAudioClock` is merely an interface structure that allows for modification of an instance of `SAMPLECLOCK`, which is itself a *data structure* that is periodically updated via the `IAudioClock` interface such that it contains a snapshot of the time value indicated by a "master" device's system clock. See, e.g., Ex. 2086 at ln. 64-72. Hence, neither `IAudioClock` nor `SAMPLECLOCK` is a separate "clock" that "generates" time values. Various aspects of the Implicit Source Code confirm this. For instance:

*Hashmi Claim Chart (Ex.2081) at 2;
Sonos Reply, p. 13.*

IMPLICIT'S CODE FAILS TO MEET "RENDERING TIME" LIMITATIONS



```

101 | SOS_UINT32      AudStreamIndex; /* index of audio stream */
102 | WAV_FORMAT     AudStreamFormat; /* audio format from wav */
103 | SOS_IAUDIOCONTEXT * IAudioContext; /* propagates format */
104 |
105 | SOS_ISAMPLECLOCK * IAudioClock; /* master sample clock */
106 | SOS_ISAMPLECLOCK * IVideoClock; /* slave sample clock */
107 |
108 | SOS_UINT32     AudioSampleCount; /* total samples sent */
109 | SOS_UINT32     AudioSampleBytes; /* bytes per sample */
    
```

*avidemux.c (Ex.2043);
Chertov Dec. (Ex.1022) ¶31.*

```

64 | typedef struct _SAMPLECLOCK {
65 |     SOS_CLOCK_TICK      Time;
66 |     SOS_UINT32          Sample;
67 |     SOS_UINT32          Frequency;
68 |     SOS_UINT32          Divisor;
69 |     SOS_BOOLEAN         IsSet;
70 |     SOS_LOCK *          Lock;
71 |     char *              Name;
72 | } SAMPLECLOCK;
    
```

*sampleclock.c (Ex.2086);
Chertov Dec. (Ex.1022) ¶31.*

U.S. Patent No. 7,391,791	Application of Claim Language to Source Code
master device time and a master rendering time;	<p>This master rendering device receives a combined audio and video content stream sent by a source device.⁶ Upon receiving the combined audio and video content stream, the master rendering device uses the avidemux bead to separate the combined audio and video stream into separate audio and video streams.^{7,8} The avidemux bead instantiates a master rendering clock IAudioClock associated with the audio content stream.⁹ This master rendering clock IAudioClock generates a plurality of master rendering times that are indicative of the statuses of the rendering the combined audio and video content stream by the master rendering device at different times.</p> <p>The Implicit Source Code implements a distributed system that runs on network devices including master rendering devices and slave rendering devices. Specifically, for synchronized audio and video rendering by master and slave rendering devices, the Implicit Source Code specifies a distributed system architecture in file videomulti.rule. Within file videomulti.rule, a master rendering device is defined to have a master rendering clock that provides master rendering times corresponding to when the master renders media content.¹⁰ This master rendering device is set up to receive combined audio and video content stream at port 8013.¹¹ The master rendering device receives a combined audio and video content stream at its</p>

```

484 | SOS_CLOCK_TICK now = SOS_Clock_TickGet();
    
```

* * *

```

503 | context->ISampleClock->Update(
504 |     context->ISampleClock,
505 |     now,
506 |     sample
    
```

*Hashmi Claim Chart (Ex.2081) at 2;
Sonos Reply, p. 13.*

IPR2018-00766, -00767 EXHIBIT 1028
speaker.c (Ex.2051);

Chertov Dec. (Ex.1022) ¶31.

IMPLICIT'S CODE FAILS TO MEET "RENDERING TIME" LIMITATIONS



Dr. Roman Chertov
Sonos's Expert Witness

34. To the contrary, depending on the operating system of a rendering device, the system time returned by the `SOS_Clock_TickGet ()` function would reflect the system clock's measure of the amount of time that has passed since an arbitrary start date/time such as January 1, 1970 at 00:00:00 (for Unix) or the date/time when the device's operating system started (for Windows²) – which bears no relationship to “the amount of content that has already been rendered by [the] rendering device.” Or put another way, a rendering device's system time is measured from a start date/time that is entirely independent of the rendering device's rendering of content, and as a result, the system time returned by the `SOS_Clock_TickGet ()` function provides no indication of “the amount of content that has already been rendered by [the] rendering device.”

Chertov Dec. (Ex.1022) at ¶134; Sonos Reply, p. 14.

33. Indeed, as noted above, the `Time` variable of a `SAMPLECLOCK` data structure at a “master” device is set using the `SOS_Clock_TickGet ()` function, which returns the current time value indicated by the “master” device's system clock, or in other words, the “master” device's system time. *See* Ex. 2051 at ln. 406-438, 484-506; Ex. 2086 at ln. 337-399.¹ However, a “master” device's *system time* does not provide “a time measure of the amount of content that has already been rendered by [the] rendering device.” *See, e.g.*, Ex. 2051 at ln. 406-438, Ex. 2086 at ln. 337-399.

Chertov Dec. (Ex.1022) at ¶133; Sonos Reply, p. 14. IPR2018-00766, 00767 EXHIBIT 1028

IMPLICIT'S CODE FAILS TO MEET "RENDERING TIME" LIMITATIONS



Dr. Roman Chertov
Sonos's Expert Witness



U.S. Patent No. 7,391,791	Application of Claim Language to Source Code
	<p>rendering device encodes the decoded RGB video frames using the <code>framer</code> bead.^{59,60} Specifically, function <code>Framer_EncodeHandler</code>⁶¹ of bead <code>framer</code> encodes the decoded RGB video frame. Afterwards, the master rendering device transfers the master rendering time and the encoded RGB video frame to over the IP network, which sends the encoded master rendering times and the encoded RGB video frame to a remote network port 8002 using the User Datagram Protocol ("UDP").⁶²</p> <p>As discussed earlier, the master rendering device uses the <code>avidemux</code> bead to split the combined audio and video content stream into separate audio and video streams. Once the audio and video content stream has been separated, the master rendering device uses another instance of the <code>fanout</code> bead to distribute the audio content stream to the two processing pathways.⁶³ The first processing pathway corresponding to index <code>FanoutIndex 0</code> outputs the audio content stream using the <code>speaker</code> bead to the audio output device of the master rendering device.^{64,65} Within second processing pathway,⁶⁶ the master rendering device uses the <code>clocksync</code> bead to encode the master rendering times. After encoding the master rendering time, the master rendering device encodes the PCM audio frames using the <code>framer</code> bead. Afterwards, the master rendering device transfers the master rendering time and the encoded PCM</p>

36. In his Claim Chart for the '791 Patent, Dr. Hashmi also contends that a "master" device "uses the `clocksync` bead to encode the *master rendering times*" that are sent to the "slave" devices. Ex. 2081 at p. 8-9, 11 (emphasis added). I disagree – the time value encoded by the `clocksync` bead is not a "master rendering time" as the parties have construed that term for purposes of this IPR, because it is not "a time measure of the amount of content that has already been rendered by [the] rendering device." Rather, the Implicit Source Code makes clear that the time value encoded by the `clocksync` bead at a "master" device would have been an adjusted *system time* of the "master" device – which does not provide "a time measure of the amount of content that has already been rendered by [the] rendering device" for the same reasons discussed above.

Hashmi Claim Chart (Ex.2081) at 9;
Sonos Reply, p. 14.

Chertov Dec. (Ex.1022) ¶36; Sonos Reply, p. 14.

IMPLICIT'S CODE FAILS TO MEET "RENDERING TIME" LIMITATIONS

```
280 |     if (Flags & FLAG_MASTEREPOCH) {  
281 |         SOS_UINT32 epoch;  
282 |  
283 |         SOS_DEBUGOUT_DETAIL("Encoding master epoch\n");  
284 |  
285 |         status = Context->IMasterClock->EpochGet(  
286 |             Context->IMasterClock,  
287 |             &epoch  
288 |         );  
289 |         if (SOS_SUCCEEDED(status)) {  
290 |             *ptr++ = SOS_HTOLEL(epoch);  
291 |             actualFlags |= FLAG_MASTEREPOCH;  
292 |         }  
    }
```

*clocksync.c (Ex.2020);
Chertov Dec. (Ex.1022) ¶137.*

```
475 || SampleClock_EpochGet(  
  
    * * *  
508 || epoch = sampleclock->Time - delta;
```

*sampleclock.c (Ex.2086);
Chertov Dec. (Ex.1022) ¶¶38-39.*



U.S. Patent No. 7,391,791	Application of Claim Language to Source Code
	<p>rendering device encodes the decoded RGB video frames using the <code>framer</code> bead.^{59,60} Specifically, function <code>Framer_EncodeHandler</code>⁶¹ of bead <code>framer</code> encodes the decoded RGB video frame. Afterwards, the master rendering device transfers the master rendering time and the encoded RGB video frame to over the IP network, which sends the encoded master rendering times and the encoded RGB video frame to a remote network port 8002 using the User Datagram Protocol ("UDP").⁶²</p> <p>As discussed earlier, the master rendering device uses the <code>avidemux</code> bead to split the combined audio and video content stream into separate audio and video streams. Once the audio and video content stream has been separated, the master rendering device uses another instance of the <code>fanout</code> bead to distribute the audio content stream to the two processing pathways.⁶³ The first processing pathway corresponding to index <code>FanoutIndex 0</code> outputs the audio content stream using the <code>speaker</code> bead to the audio output device of the master rendering device.^{64,65} Within second processing pathway,⁶⁶ the master rendering device uses the <code>clocksync</code> bead to encode the master rendering times. After encoding the master rendering time, the master rendering device encodes the PCM audio frames using the <code>framer</code> bead. Afterwards, the master rendering device transfers the master rendering time and the encoded PCM</p>

*Hashmi Claim Chart (Ex.2081) at 9;
Sonos Reply, p. 14.*