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United States Patent [19]

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Spanke

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[54] **ARRANGEMENT FOR BOUNDING JITTER IN A PRIORITY-BASED SWITCHING SYSTEM**

5,179,549 1/1993 Joos et al. 370/17

[75] Inventor: **Ronald A. Spanke, Wheaton, Ill.**

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9104624 4/1991 World Int. Prop. O. ... H04L 12/56

[73] Assignee: **AT&T Bell Laboratories, Murray Hill, N.J.**

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H. Kuwahara et al., "A Shared Buffer Memory Switch for an ATM Exchange", *IEEE Int'l. Conf. on Comms.*, vol. 1 (Jun. 11-14, 1989), pp. 4.4.1-4.4.5.

[21] Appl. No.: **897,200**

Primary Examiner—Benedict V. Safourek

Assistant Examiner—Russell W. Blum

Attorney, Agent, or Firm—Ross T. Watland

[22] Filed: **Jun. 11, 1992**

[51] Int. Cl.⁵ **H04L 12/54**

[52] U.S. Cl. **370/60; 370/79; 370/85.6**

[58] Field of Search 370/17, 60.1, 79, 84, 370/60, 85.6, 94.1, 94.2, 105.3; 375/118

[57] ABSTRACT

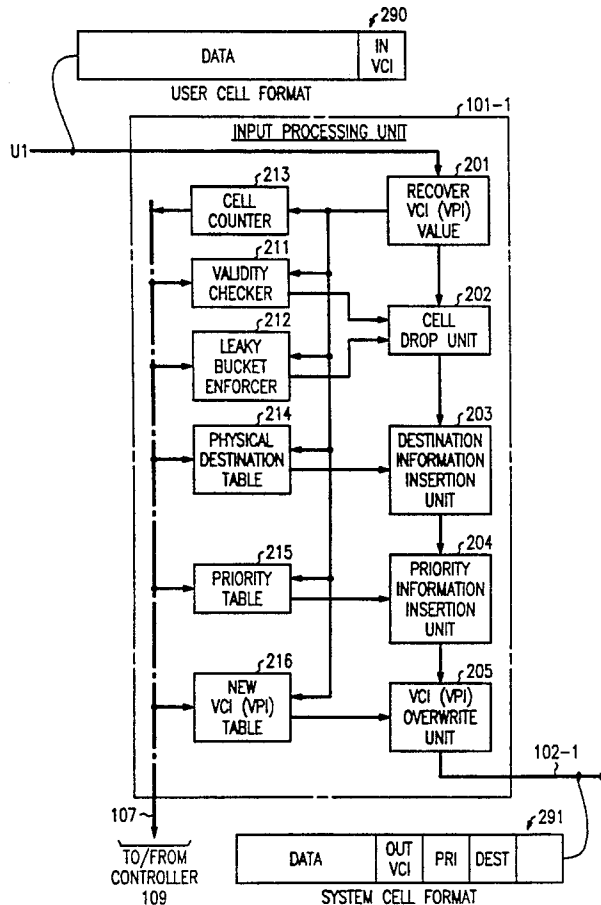
An arrangement where a switching system receives parameter(s) concerning the traffic expected on a call from a first user to a second user, the system determines a priority for the call based on the expected traffic parameter(s), and information is transmitted to the second user during the call based on the determined priority and with less than a maximum jitter. The priority is selected from a predefined priority table based on expected traffic parameters. The priority table is usable for both constant bit rate and statistical calls.

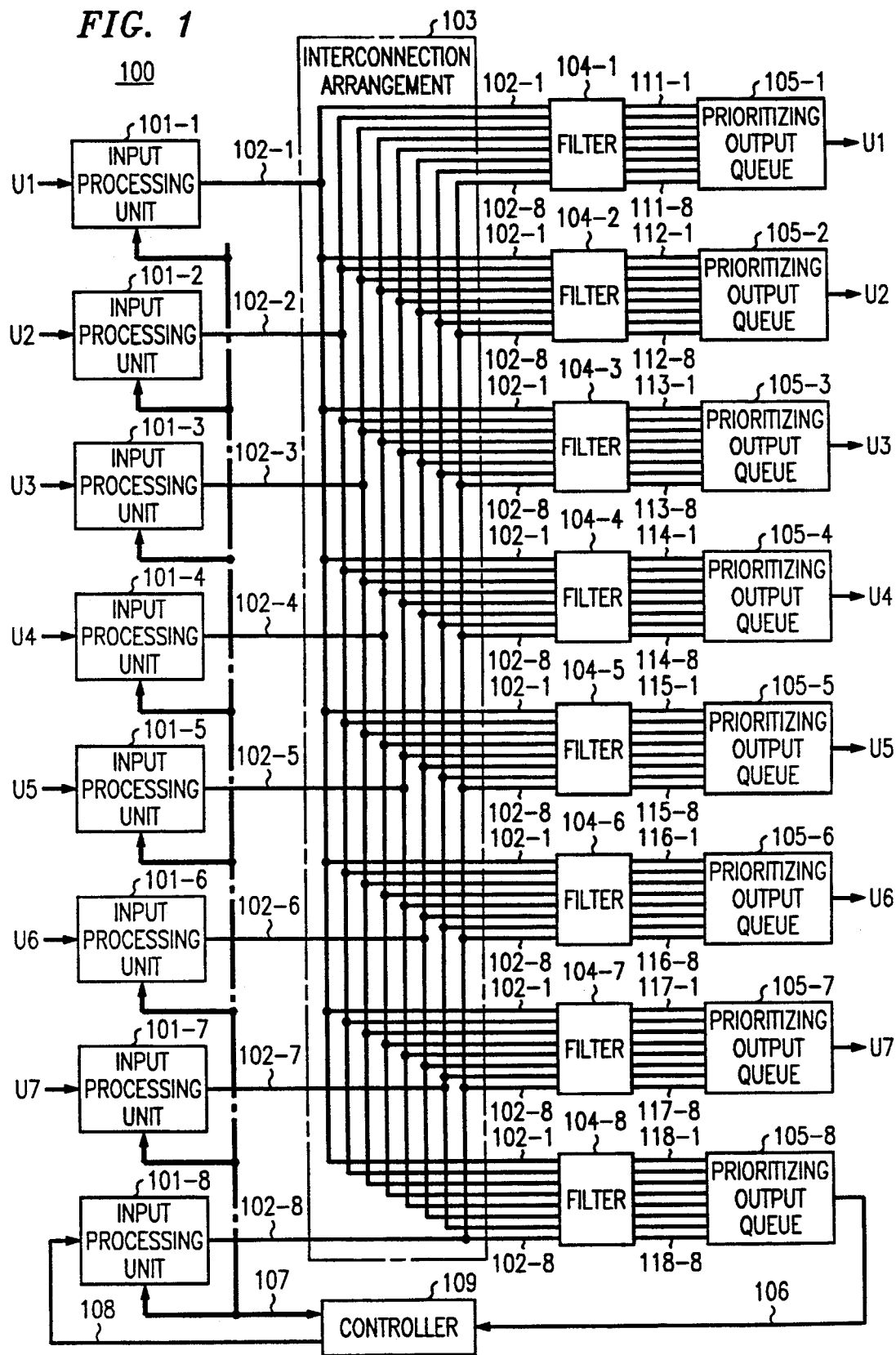
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14 Claims, 4 Drawing Sheets





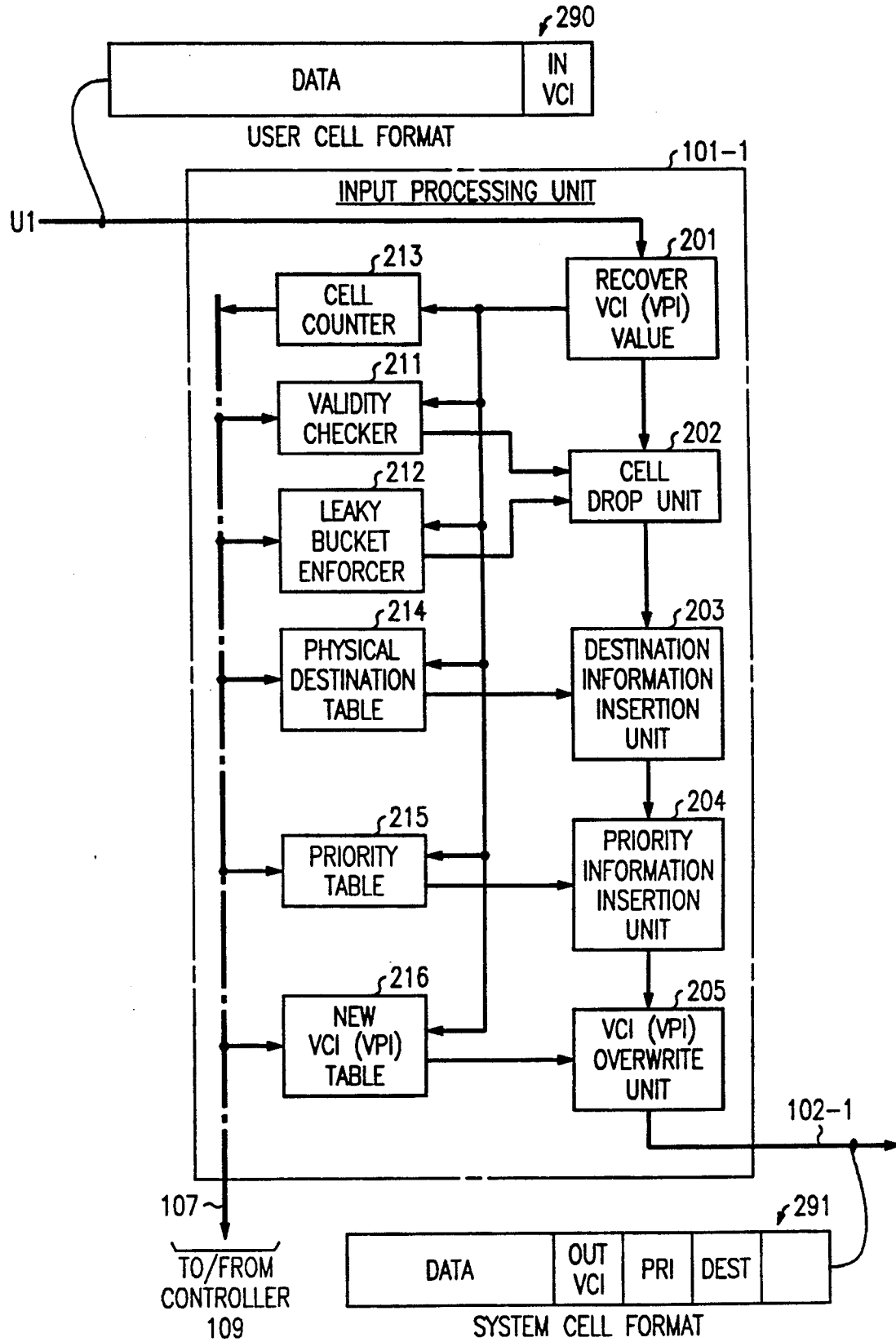


FIG. 2

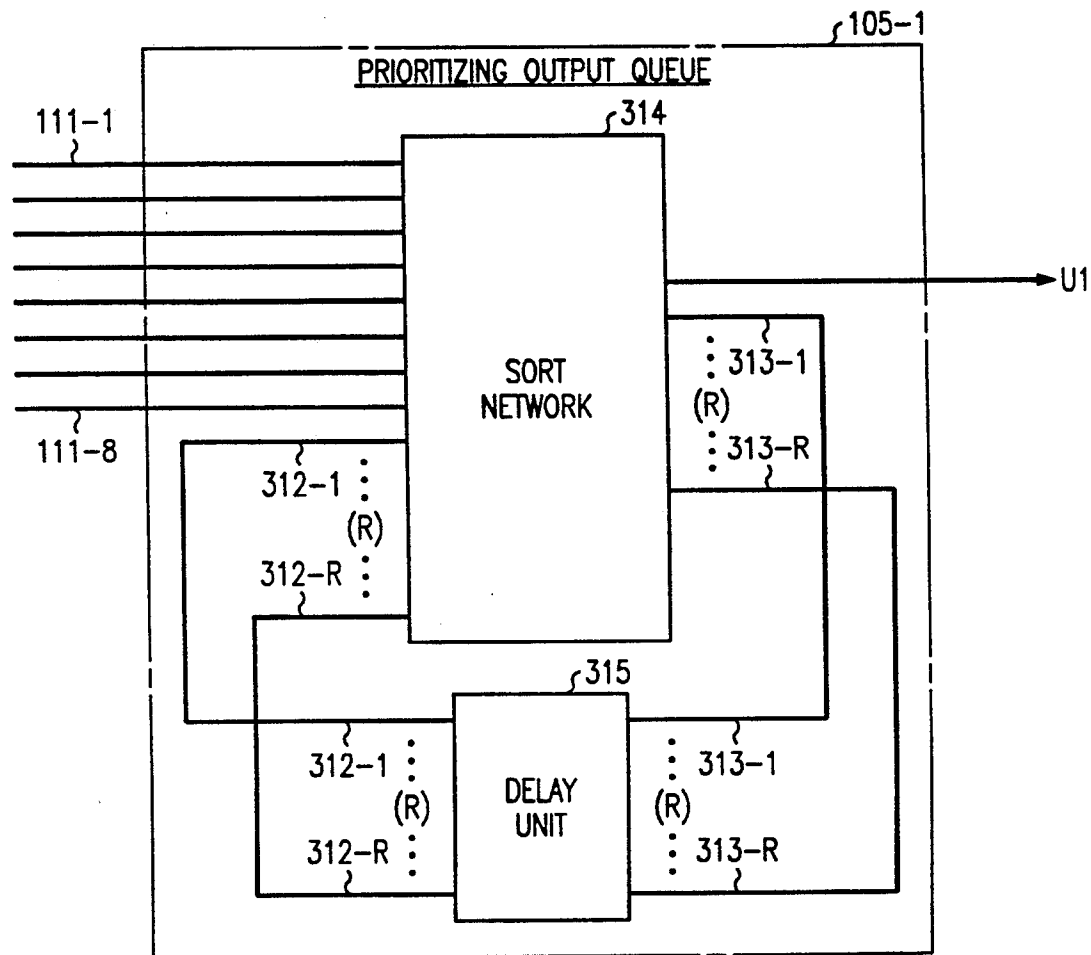


FIG. 3

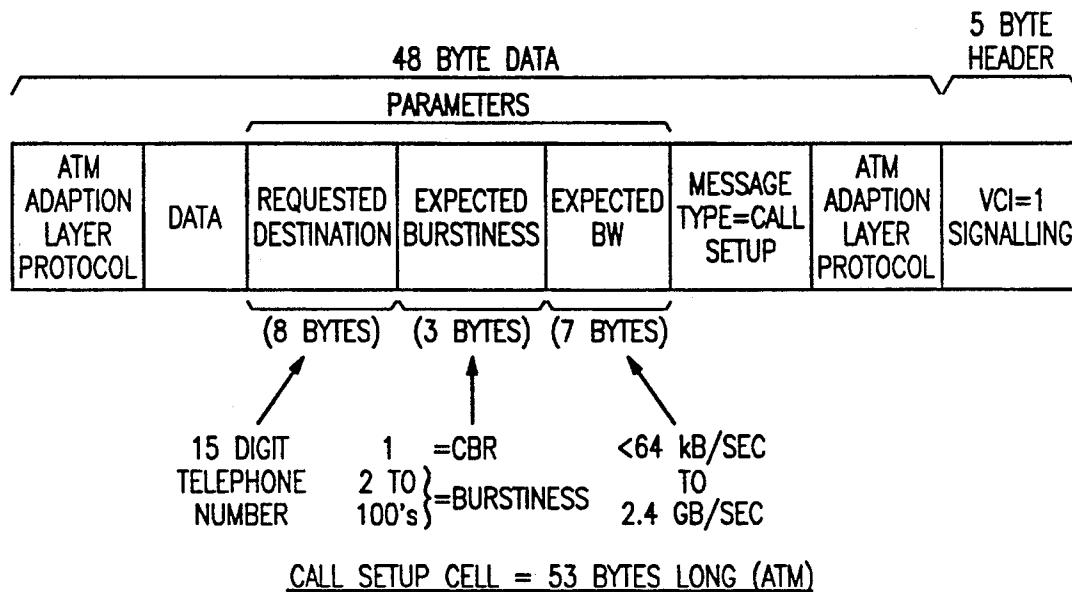
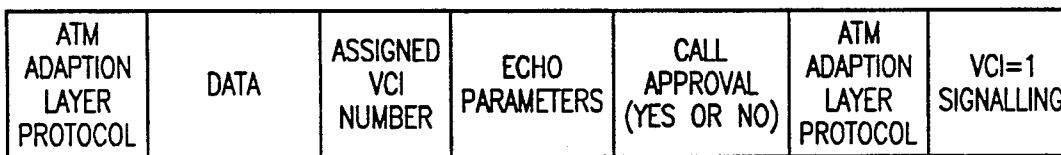


FIG. 4



SETUP RESPONSE CELL

FIG. 5

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