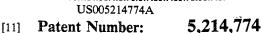


[57]

54.1

# United States Patent [19]



[45] Date of Patent: May 25, 1993

### Welsch et al.

### [54] SEGMENTED MEMORY TRANSFER AND MESSAGE PRIORITY ON SYNCHRONOUS/ASYNCHRONOUS DATA BUS

- [75] Inventors: Gary A. Welsch, Palatine; Donald A. Dorsey, Arlington Heights, both of Ill.
- [73] Assignee: Motorola, Inc., Schaumburg, Ill.
- [21] Appl. No.: 559,873
- [22] Filed: Jul. 30, 1990
- [51] Int. Cl.<sup>5</sup> ...... G06F 13/00

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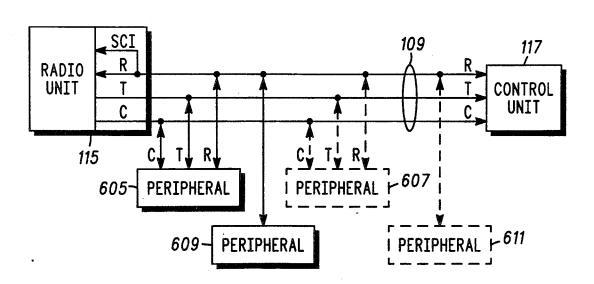
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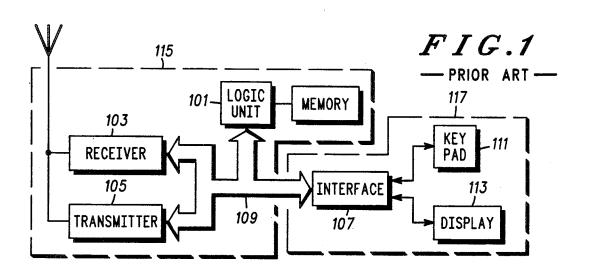
Primary Examiner-Robert L. Richardson Attorney, Agent, or Firm-Raymond A. Jenski

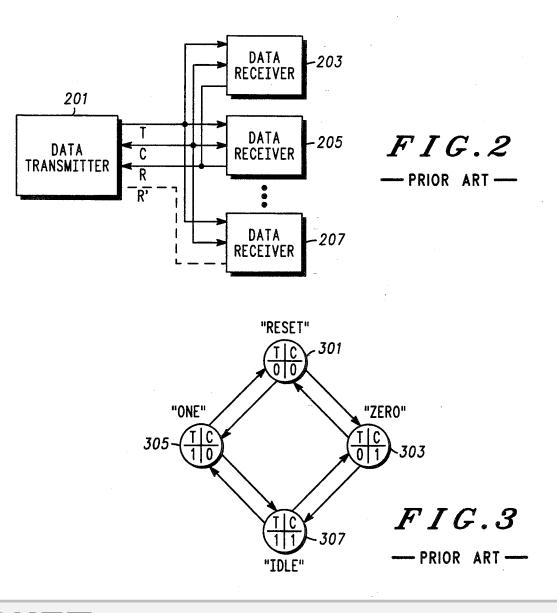
### ABSTRACT

Apparatus and method for transferring segmented memory between memory units determines a need for memory transfer and searches a memory unit segment by segment until a valid data record is found. This valid data record is conveyed to another memory unit via the asynchronous mode. If a valid data records is not found within a predetermined amount of time, a message identifying the memory segment last searched is conveyed. If no remaining valid data records are found, a message with no information is conveyed. Priority is given to asynchronous messages.

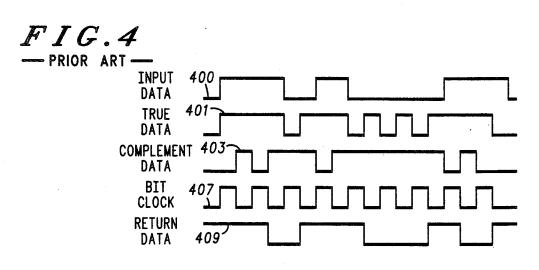
### 22 Claims, 12 Drawing Sheets





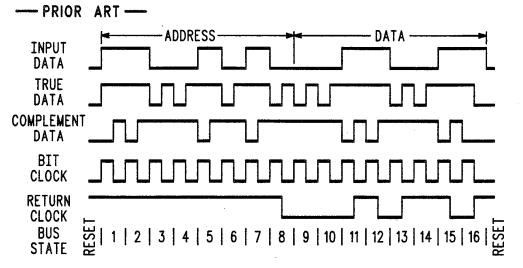


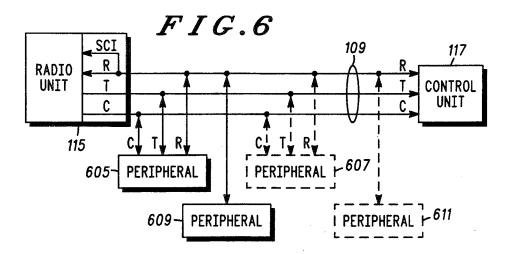
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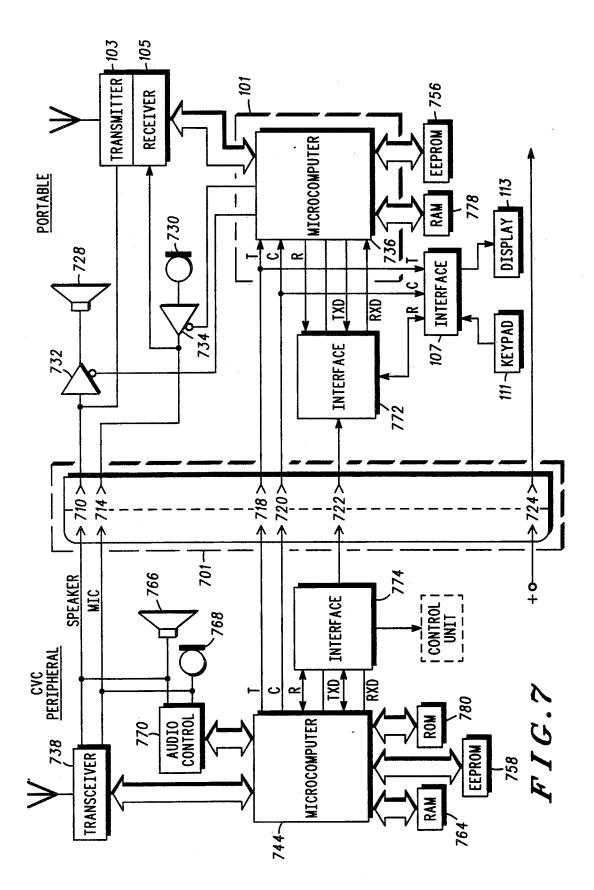


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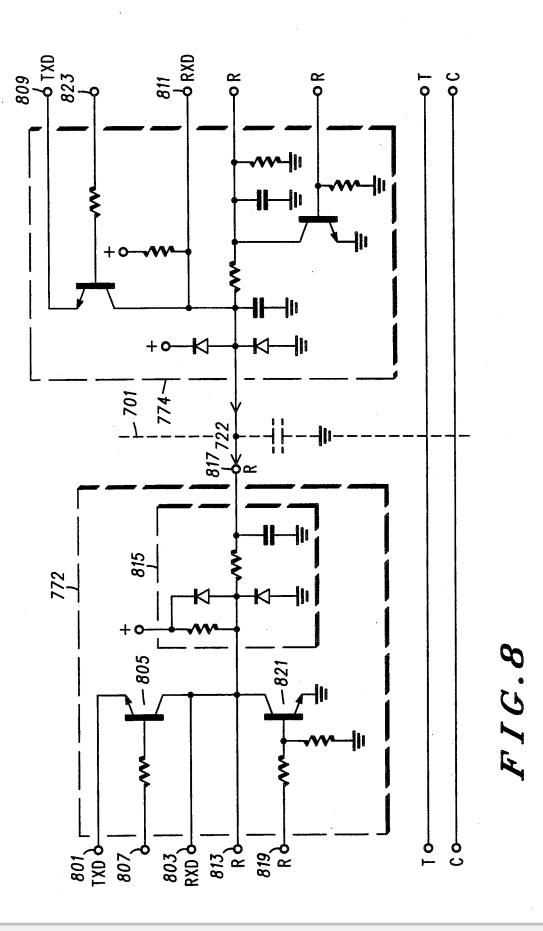




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