UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

ZTE (USA) Inc., Samsung Electronics Co., Ltd., and Samsung Electronics America, Inc.,

Petitioner

V.

Fundamental Innovation Systems International LLC,

Patent Owner.

PETITION FOR INTER PARTES REVIEW OF U.S. PATENT NO. 7,834,586 B2

Case No. IPR2018-00274

Mail Stop Patent Board Patent Trial and Appeal Board U.S. Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450



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Exhibit List

<u>Exhibit</u>	<u>Description</u>
1001	U.S. Patent 7,834,586 ("'586 patent")
1002	File History of U.S. Patent 7,834,586 ("'586 file history")
1003	U.S. Provisional Application 60/273,021 ("'021 provisional")
1004	U.S. Provisional Application 60/330,486 ("486 provisional")
1005	U.S. Patent 5,925,942 ("Theobald")
1006	U.S. Patent 6,625,738 ("Shiga")
1007	Universal Serial Bus Specification, Revision 1.1 ("USB 1.1")
1008	Universal Serial Bus Specification, Revision 2.0 ("USB 2.0")
1009	Declaration of Mr. James Geier in Support of the Petition for Inter Partes Review of U.S. Patent 7,834,586 ("Geier")
1010	U.S. Patent 6,625,790 ("Casebolt")
1011	Cypress CY7C63722/23 CY7C63742/43 enCoRe™ USB Combination Low-Speed USB & PS/2 Peripheral Controller ("Cypress datasheet")
1012	U.S. Patent 6,531,845 ("Kerai")
1013	U.S. Patent 5,506,490 ("DeMuro")
1014	TIA/EIA-644 Electrical Characteristics of Low Voltage Differential Signaling (LVDS) Interface
1015	U.S. Patent No. 5,959,601 ("'601 patent")
1016	U.S. Patent No. 6,353,334 ("'334 patent")
1017	U.S. Patent No. 6,366,128 ("'128 patent")
1018	File history of U.S. Patent No. 6,366,128 ("'128 file history")



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<u>Exhibit</u>	<u>Description</u>
1019	U.S. Patent No. 5,884,086 ("Amoni")
1020	U.S. Patent No. 6,556,564 ("Rogers")
1021	U.S. Patent No. 7,360,004 ("Dougherty")
1022	U.S. Patent Application Publication No. 2008/0272741 ("Kanamori")



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