

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

United States Patent No.: 5,802,467	§	Attorney Docket No.: 1984.092459
Inventors: Joe Andrew Salazar,	§	
Luis Molero-Castro	§	Customer No. 26290
Formerly Application No.: 535,801	§	Petitioners:
Filing Date: September 28, 1995	§	HTC Corp.;
Issue Date: September 1, 1998	§	HTC America, Inc.
Priority Date: September 28, 1995	§	
Former Group Art Unit: 2744	§	
Former Examiner: Myron Wyche	§	

For: Wireless and Wired Communications, Command, Control and Sensing
System for Sound and/or Data Transmission and Reception

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**PETITION FOR *INTER PARTES* REVIEW OF
UNITED STATES PATENT NO. 5,802,467**

Pursuant to 35 U.S.C. §§ 311-319 and 37 C.F.R. § 42, the undersigned, on behalf of and acting in a representative capacity for petitioners HTC Corporation and HTC America, Inc. (collectively “HTC,” “Petitioners” and real parties in interest), hereby petition for *inter partes* review of claims 1-7, 10, 14, 17, 23, 26-32, and 34 of U.S. Patent No. 5,802,467 (“the ‘467 patent”), issued to Joe Andrew Salazar *et al.* and currently assigned to Joe Andrew Salazar (“Salazar,” also referred to as “Applicant,” “Patent Owner,” or “Patentee”).

Inter Partes Review United States
Patent No. 5,802,467

Petitioners hereby assert that there is a reasonable likelihood that at least one of the Challenged Claims is unpatentable for at least the reasons set forth herein and respectfully request review of, and judgment against, claims 1-7, 10, 14, 17, 23, 26-32, and 34 as unpatentable under 35 U.S.C. § 103.

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2. a microprocessor for generating a plurality of control signals used to operate said system, said microprocessor creating a plurality of reprogrammable communication protocols.....	8
3. a memory device coupled to said microprocessor configured to store a plurality of parameter sets retrieved by said microprocessor so as to recreate a desired command code set, such that the memory space required to store said parameters is smaller than the memory space required to store said command code sets.....	9
4. .a memory device coupled to said microprocessor configured to store a plurality of parameter sets retrieved by said microprocessor so as to recreate based on said parameter sets a desired set of pulse signals corresponding to logical “1’s” and “0’s” as specified by a command code set	9
5. a selector controlled by said microprocessor for enabling said radio	

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