

UNITED STATES PATENT AND TRADEMARK OFFICE

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BEFORE THE PATENT TRIAL AND APPEAL BOARD

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ZTE (USA) Inc.,  
Samsung Electronics Co., Ltd., and  
Samsung Electronics America, Inc.,

Petitioner,

v.

Fundamental Innovation Systems International LLC,

Patent Owner.

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Case IPR2018-00110  
U.S. Patent No. 8,624,550

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PETITIONER'S EXHIBIT LIST

## EXHIBIT LIST

Exhibit No.	Description	Short Name
1001	U.S. Patent 8,624,550	'550 patent
1002	File History of U.S. Patent 8,624,550	'550 file history
1003	U.S. Provisional Application 60/273,021	'021 provisional
1004	U.S. Provisional Application 60/330,486	'486 provisional
1005	U.S. Patent 7,360,004	Dougherty
1006	U.S. Patent 6,625,738	Shiga
1007	Universal Serial Bus Specification, Revision 1.1, September 23, 1998	USB 1.1
1008	Universal Serial Bus Specification, Revision 2.0, April 27, 2000	USB 2.0
1009	Declaration of Mr. James Geier in Support of the Petition for Inter Partes Review of U.S. Patent 8,624,550	Geier
1010	U.S. Patent 6,625,790	Casebolt
1011	Cypress CY7C63722/23 CY7C63742/43 enCoRe™ USB Combination Low-Speed USB & PS/2 Peripheral Controller (Cypress enCoRe or Cypress Datasheet), by Cypress Semiconductor Corporation, published May 25, 2000	Cypress datasheet
1012	U.S. Patent 6,531,845	Kerai
1013	File History of U.S. Patent No. 7,360,004	Dougherty file history

Date: October 26, 2017

Respectfully submitted,

/Charles M. McMahon/  
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## CERTIFICATE OF SERVICE

I hereby certify that I sent a copy of this document and all supporting materials on October 26, 2017 to: (i) the '550 patent's correspondence address of record, Botos Churchill IP Law LLP (TNT IP LLC), 430 Mountain Avenue, Suite 401, New Providence NJ 07974, by overnight carrier; and (ii) Patent Owner's litigation counsel Elizabeth L. DeRieux (ederieux@capshawlaw.com) and Edward J. DeFranco (eddefranco@quinnemanuel.com) by e-mail.

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