



United States Patent [19]

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Banks

[45] Date of Patent: **Aug. 18, 1998**

[54] **ELECTRONIC DISPLAY SIGN**
[75] Inventor: **Archie A. Banks**, Prince Edward Island, Canada

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[73] Assignee: **CIE Research, Inc.**, Prince Edward Island, Canada

[21] Appl. No.: **422,701**

[22] Filed: **Apr. 14, 1995**

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Related U.S. Application Data

[63] Continuation of Ser. No. 406,660, Mar. 20, 1995, abandoned, which is a continuation of Ser. No. 149,714, Nov. 9, 1993, abandoned, which is a continuation of Ser. No. 809,670, Dec. 18, 1991, abandoned.

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[51] Int. Cl.⁶ **B41J 2/105; B41J 2/01; B41J 3/00; B41J 29/38**

[57] **ABSTRACT**

[52] U.S. Cl. **345/82; 345/1; 345/4; 345/903**

An electronic display sign constructed around a system bus architecture is disclosed. The electronic display sign is preferably a modular construction wherein a number of modules are connected together to form a large display sign which is capable of displaying images at rates which exceed thirty frames a second. Display data is formatted and transmitted to display modules by a sign controller which resides on one of the panels. Each display panel is preferably provided with its own power source.

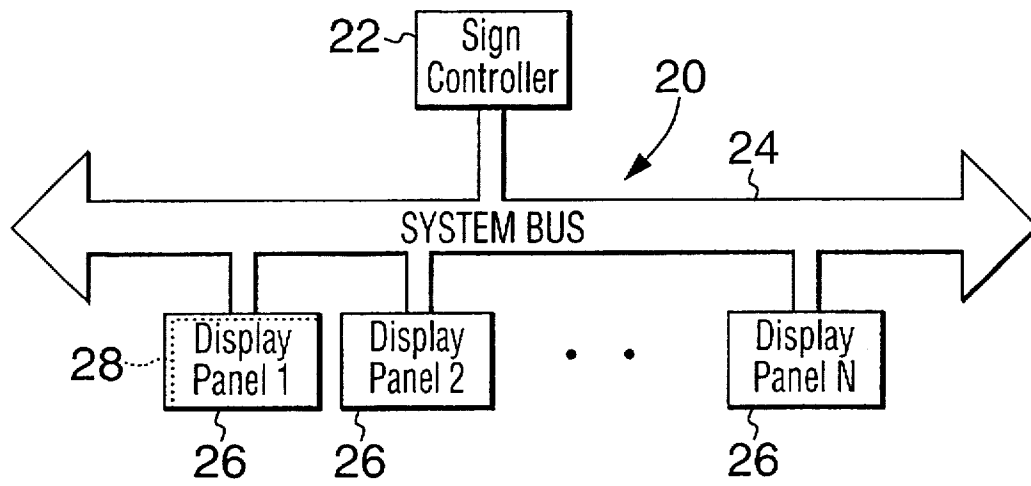
[58] Field of Search 345/82, 1, 2, 4-6, 345/903; B41J 2/105, 2/01, 3/00, 29/56

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30 Claims, 7 Drawing Sheets



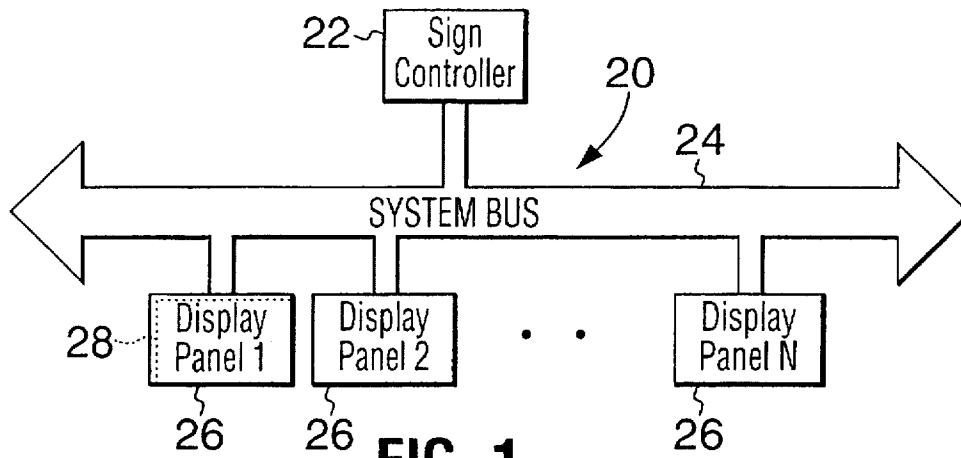


FIG. 1

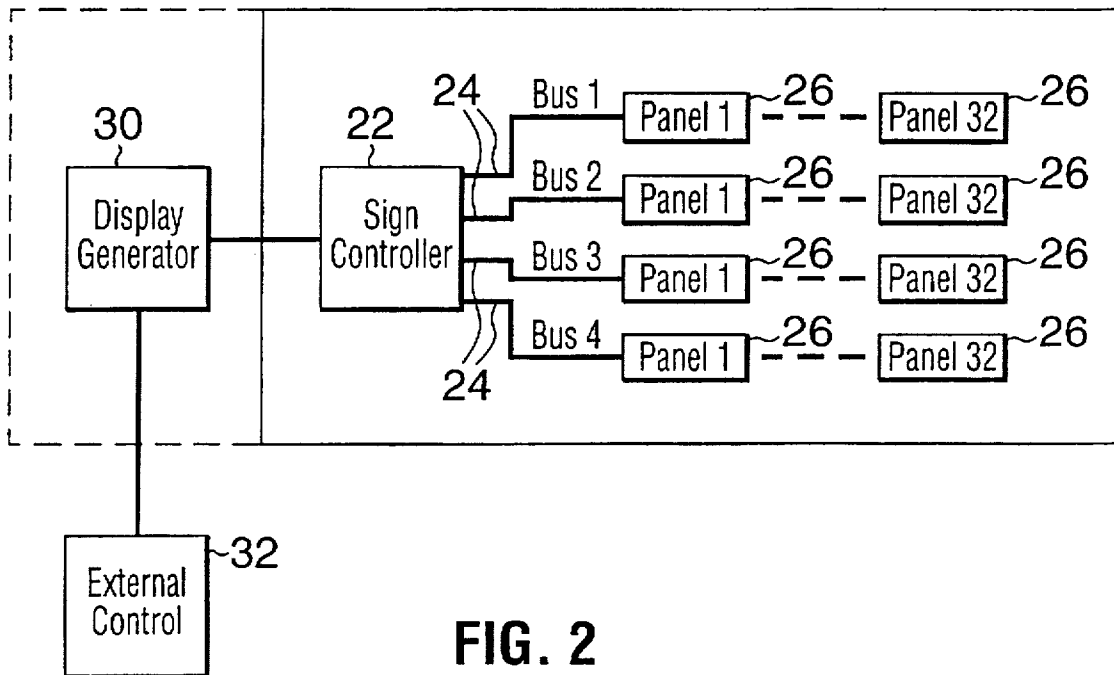


FIG. 2

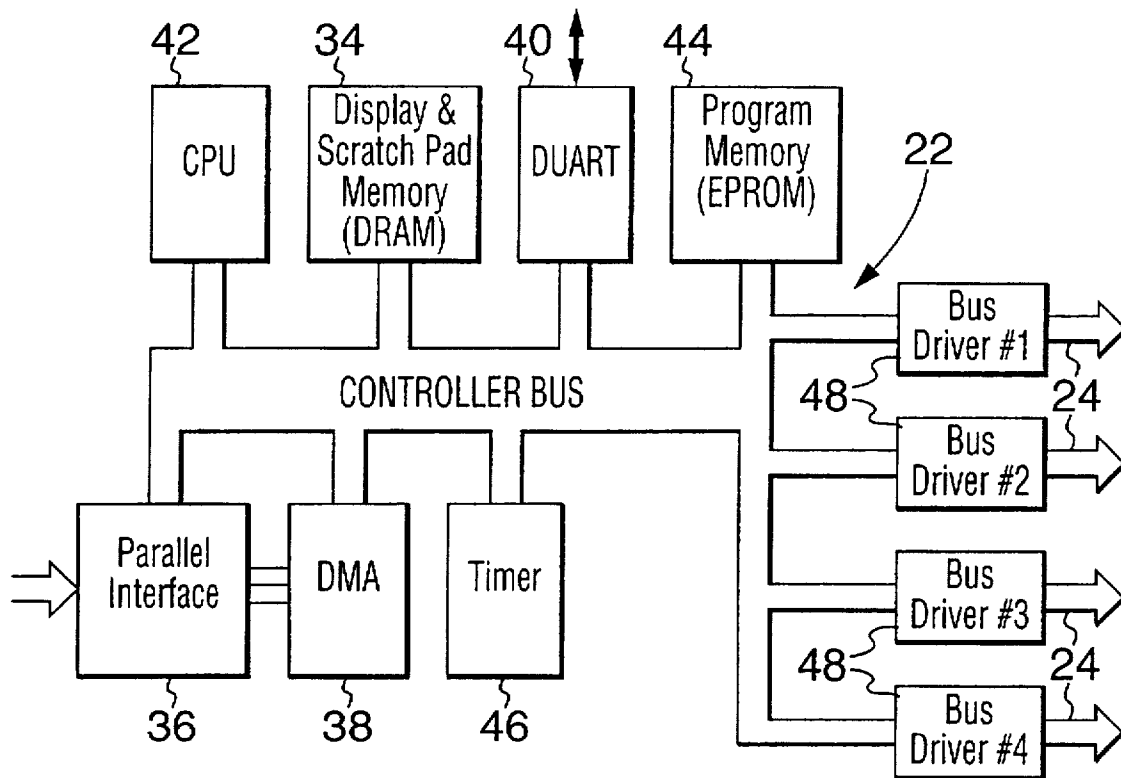


FIG. 3

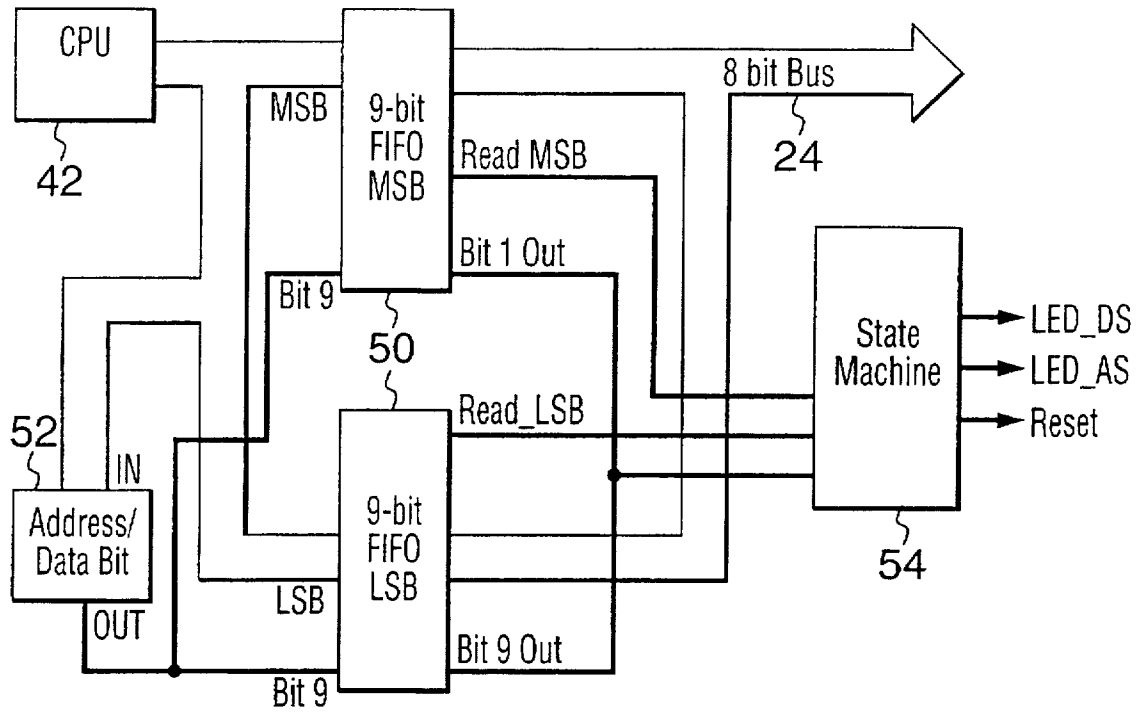


FIG. 4

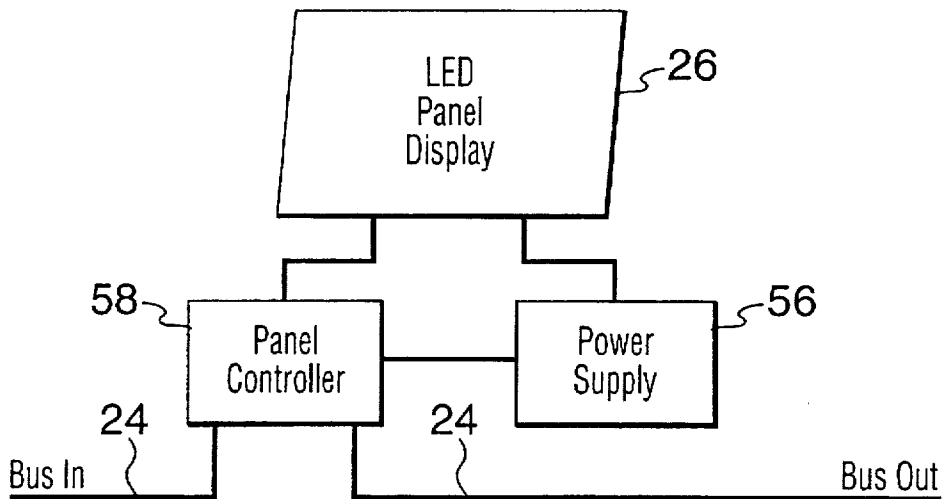


FIG. 5

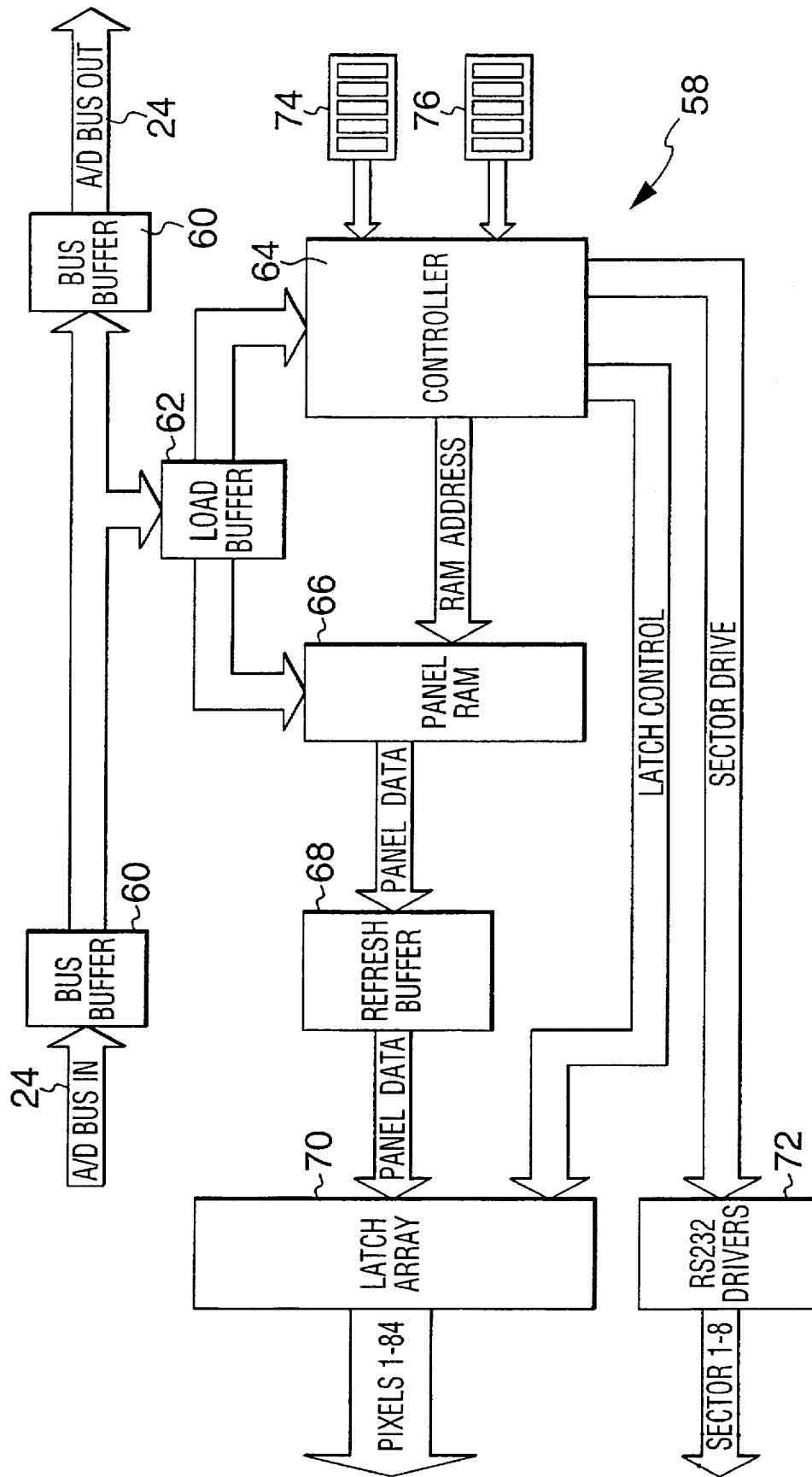


FIG. 6

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