Dr. Gabriel Robins
Professor of Computer Science

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Vitae:

Birth place: New York City, NY

Citizenship: USA

Education

- Ph.D. in Computer Science, UCLA, 1992
- M.S.E. in Computer Science, Princeton University, 1985
- B.S. in Mathematics and Computer Science, UCLA, 1983
- A.A., Los Angeles Valley College, 1981

Honors and Awards

1. Society for Industrial and Applied Mathematics (SIAM) Outstanding Paper Prize, 2007

2. Packard Foundation Fellowship, 1995-2001 ($550,000)

3. National Science Foundation Young Investigator Award, 1994-1999 ($312,500)

4. Best Presentation Award, IEEE International Conference on Localization and Global Navigation Satellite Systems, Torino, Italy, 2013

5. Member of the Army Science Board, U.S. Army, 1998-2001

6. Member of the Defense Science Study Group, U.S. Department of Defense, 1994-1995

7. Member of the Navy Future Study (of the National Academy of Sciences), 1996-1997

8. Walter N. Munster Endowed Chair, 1997-2002

9. Two-year early promotion to Associate Professor (with tenure), 1996

10. Promotion to Full Professor (with tenure), 2002

11. Faculty Mentor Award, UVa School of Engineering, 1997

12. All-University Outstanding Teaching Award, University of Virginia, 1994-1995

13. University Teaching Fellowship, University of Virginia, 1995-1996

14. Faculty Appreciation Award, Virginia Engineering Foundation, 1998

15. Award for "Tireless Dedication to Improving the Department in All Aspects", Department of Computer Science, 1998

16. Web Team Award - "In Appreciation for Founding and Leading the Department's Excellent Web Team for 6 Years", 2001

17. Member of the Faculty Senate, University of Virginia, 1999-2002

18. Member of the School of Engineering Faculty Council, University of Virginia, 1999-2002

19. Associate Editor of IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2000-2005

20. Member of the Editorial Board of Research Letters in Electronics, 2007-present

21. Expert Witness for major software and intellectual property litigations, 1996-present

22. Invited paper in IEEE International Conference on Electronics, Circuits and Systems, 2004

23. Nomination for Virginia state-wide (SCHEV) Outstanding Faculty Award, 1995

24. Nomination for NSF Presidential Faculty Fellows Award, 1995-1996

25. National Science Foundation Research Initiation Award, 1993

26. Nomination for ACM Best Dissertation Award, 1992

27. IBM Graduate Fellowship, 1991-1992

28. Distinguished Paper Award, IEEE International Conference on Computer-Aided Design, 1990

29. Nomination for Best Paper Award, Asia and South Pacific Design Automation Conference, Hong Kong, January 1999.

30. Distinguished Teaching Award, UCLA, 1989

31. Chancellor's Distinguished Honor Award

32. University of California Regents Scholarship

33. Dean's list (10 times)

34. Top ranked by students at "Rate My Professor" (RateMyProfessor.com)

Research Interests

Algorithms, optimization, RFID, genomics, bioinformatics, big data, computational geometry, computer-aided design (CAD), VLSI, national security, and intellectual property.

Teaching Interests

Theory of computation, algorithms, problem solving, discrete math, VLSI CAD, data structures, and computational geometry.

Experience

1. Professor of Computer Science (with tenure), Department of Computer Science, University of Virginia (6/2002 to present).

2. Walter N. Munster (endowed chair) Associate Professor of Computer Science (with tenure), Department of Computer Science, University of Virginia (6/1997 to 6/2002).

3. Associate Professor (with tenure), Department of Computer Science, University of Virginia, (9/1996 to 6/1997).

4. Assistant Professor, Department of Computer Science, University of Virginia (9/1992 to 9/1996).

5. Co-Founder of Diorama Technologies, an RFID-based startup company that commercializes the RFID localization technology that was co-invented, published, and patented by Professor Gabriel Robins and his graduate student.

6. Member of the National Academy of Sciences study: Technology for the United States Navy and Marine Corps, 2000-2035 - Becoming a 21st Century Force, 1996-1997: our task for this DoD advisory panel was to provide the U.S. Navy with a comprehensive strategic vision through the year 2035.

7. DoD Advisor, Defense Science Study Group (DSSG), Institute for Defense Analysis (IDA) (11/93 to present): In the context of this advisory board to the U.S. Department of Defense, we met with U.S. Secretary of Defense Bill Perry, as well as with numerous military experts, to discuss science and technology issues. We also visited a number of military bases and installations.

8. Expert Witness, Cislo & Thomas, Los Angeles, CA (2007-present): Analyzed evidence, performed prior art searches, and testified in multi-party patent and intellectual property litigations. (Girafa.com Inc. v. Amazon Web Services LLC et al, case number 1:07-cv-00787, Delaware District Court, 2007-2010, I assisted Snap Technologies Inc.; Diopsys Inc. v. Konan Medical USA Inc., case number 2:15-cv-05882, New Jersey District Court, 2015-present, I assisted Konan Medical USA Inc.)

9. Expert Witness, Drinker Biddle & Reath, Chicago, Illinois (2017-present): Performed prior art searches and analyzed evidence in a multi-party patent and intellectual property litigation. (Papst Licensing GmbH v. Apple Inc. et al, case number 6:15-cv-1095-RWS, Eastern Texas District Court, I assisted Samsung, LG, Huawei, and ZTE)

10. Expert Witness, Wilmer Cutler Pickering Hale & Dorr LLP, Palo Alto, CA (2011-2012): Analyzed evidence, performed prior art searches, and prepared to testify in a major multi-party intellectual property litigation. (Intellectual Ventures v. Altera, Xilinx, et al, case number 1:10-cv-01065, Delaware District Court, 2010-2012, I assisted Altera Corp)

11. Expert Witness, Hogan and Lovell, New York, NY (2012-2013): Analyzed evidence and performed prior art searches in patent and software intellectual property litigations. (Motorola Solutions, Inc. v. Round Rock Research LLC, case number 1:12-cv-00309, Delaware District Court, 2012-2013, I assisted Motorola Solutions Inc.)

12. Expert Witness, Covington and Burling LLP, Redwood Shores, CA (2012): Analyzed evidence, performed prior art searches, and performed validity analyses RE technology patents and intellectual property litigations. (No formal declarations filed nor testimony given.)

13. Expert Witness, Federal Bureau of Investigation, San Diego, CA (2004-2006): Analyzed evidence in a major software and intellectual property litigation between two large public companies. (No formal declarations filed nor testimony given.)

14. Expert Witness, O'Melveny and Myers, San Francisco; Banner and Witcoff, Washington, D.C. (2005): Analyzed evidence, created patent claims charts, and helped draft patent invalidity filings and re-exam requests in a major patent litigation between two public companies. (Synopsys Inc. v. Magma Design Automation Inc., case number 3:04-cv-03923, California Northern District Court, 2005, I assisted Magma Design Automation Inc.)

15. Expert Witness, O'Melveny and Myers, San Francisco, CA (1996-2002): Analyzed evidence, performed prior art searches, and testified in a billion-dollar software infringement and intellectual property litigation between major public companies. (Cadence Design Systems

Inc. v. Avant! Corporation, case number **99-17648**, **United States Ninth Circuit**; case number S098266, Supreme Court of California, 1996-2002, I assisted Avant! Corporation)

16. Consultant, Morrison & Foerster LLP, San Francisco, CA (2002-2009): Analyzed evidence and performed prior art searches and validity analyses in intellectual property litigations. (No formal declarations filed nor testimony given.)

17. Consultant, Earl Industries, Newport News, VA, (2008-2011): Performed due-diligence analyses, conducted prior art searches, and evaluated technology patent portfolios during corporate buyouts. (No formal declarations filed nor testimony given.)

18. Consultant, BOSH Global Services, Newport News, VA, (2012-2014): Conducted prior art searches and evaluated IP and technology patents and applications. (No formal declarations filed nor testimony given.)

19. Expert Witness, Russ, August and Kabat, Los Angeles, CA (2016-present): Analyzed evidence, drafted reports, and testified in patent litigations.

20. Research Assistant, UCLA (9/89 to 7/92): I performed research in layout algorithms for performance-driven interconnection, circuit testing, computational geometry, and motion planning.

21. Teaching Assistant, UCLA (9/87 to 1/90): I developed and taught a graduate-level course in computational geometry, as well as an upper-division course in formal languages and automata theory.

22. Researcher, USC Information Sciences Institute (6/85 to 1/89): I helped develop the knowledge representation language NIKL. I also designed and implemented the ISI Grapher, a portable tool for displaying arbitrary graphs pictorially. My system generated considerable interest worldwide, and was marketed commercially.

23. Teaching Assistant, Princeton University (9/83 to 6/85): I taught an undergraduate programming course in Pascal using the Apple MacIntosh.

24. Consultant, Xerox Electro-Optical Systems (7/84 to 9/84): I designed and implemented critical Ethernet software, allowing VAX 11/780 computers to communicate with Xerox 1108 workstations.

25. Lab Supervisor, UCLA Mathematics Department (10/82 to 6/83): I supervised an undergraduate computing laboratory, helping students with Pascal and C.

26. Programmer, Carnation Research (9/79 to 9/82): I programmed an IBM mainframe computer in Fortran, and designed and implemented numerous scientific applications for food-products manufacturing.

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