# United States Patent [19]

## **Parrillo**

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[45] Date of Patent: Aug.

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4,951,100

# [54] HOT ELECTRON COLLECTOR FOR A LDD TRANSISTOR

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[73]	Assignee:	Motorola, Inc., Schaumburg, Ill.	

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[22] Filed: Jul. 3, 1989

[51]	Int. Cl. <sup>5</sup>	H01L 29/78
		357/233; 357/23.12;
		357/53; 357/59

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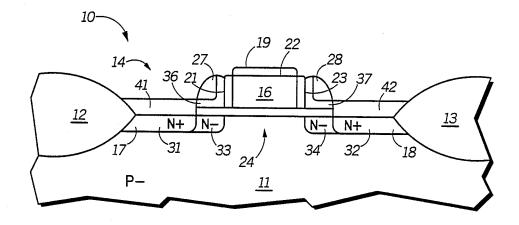
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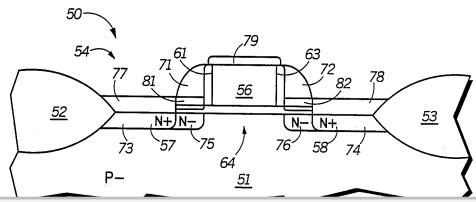
Primary Examiner—William D. Larkins Assistant Examiner—Donald L. Monin Attorney, Agent, or Firm—James L. Clingan, Jr.

#### [57] ABSTRACT

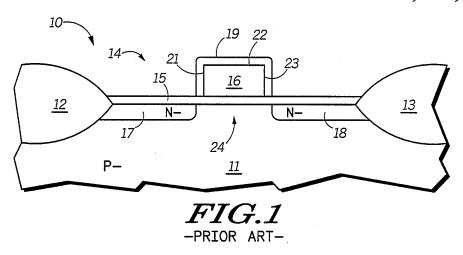
A lightly-doped drain (LDD) structure has conductive shield overlying the lightly-doped drain and source portions to collect and/or remove hot carriers which can otherwise cause instabilities such as gain degradation and threshold voltage shifts in short-channel MOS devices. The hot carriers eventually deteriorate the performance of the transistor to the point where the transistor provides insufficient performance. Thus, the lifetime of a transistor is affected by the degradation caused by the formation of hot carriers. The lifetime is increased by collecting the hot carriers in the conductive material over the lightly-doped source and drain.

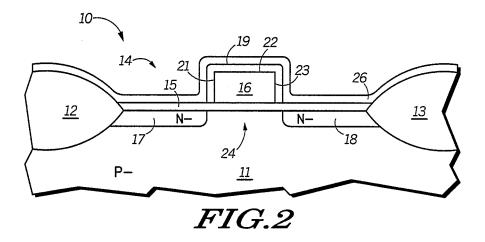
## 14 Claims, 4 Drawing Sheets

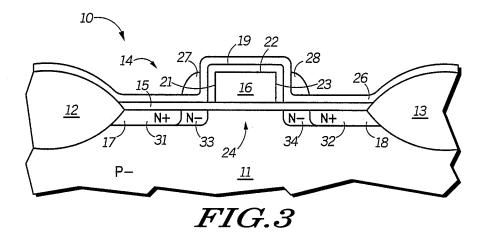


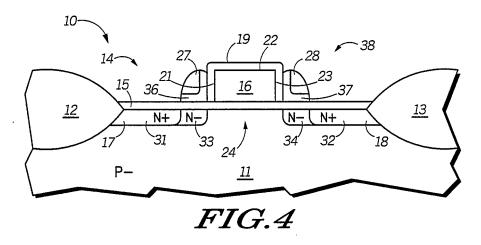


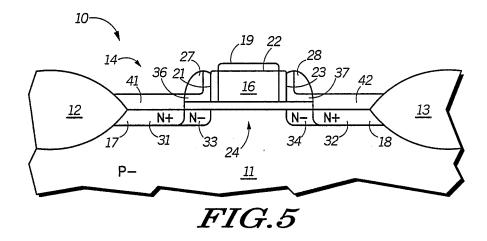


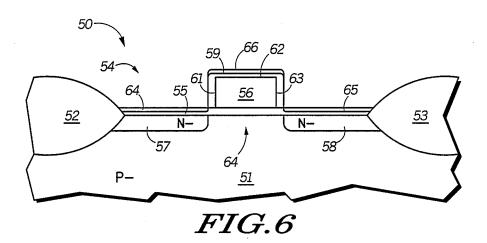


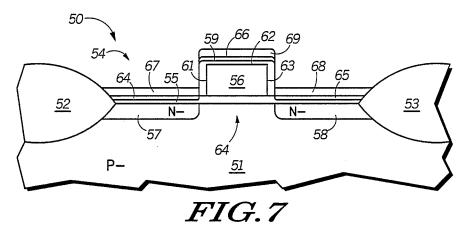


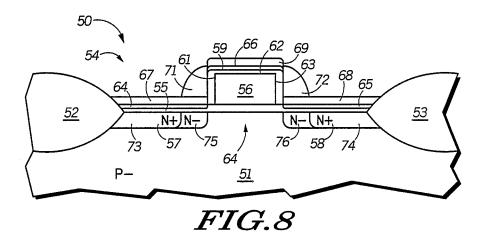


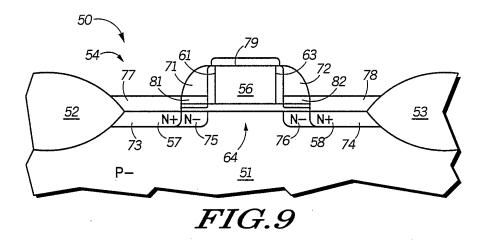


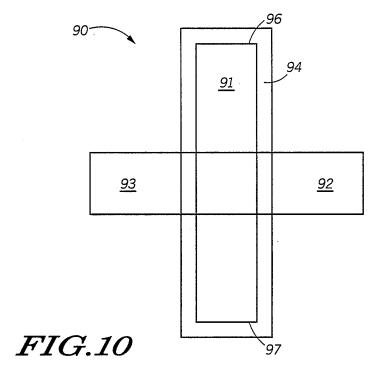


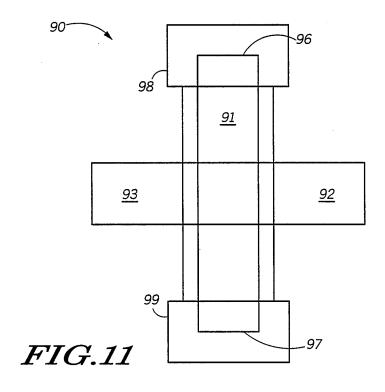












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