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# UNITED STATES PATENT AND TRADEMARK OFFICE

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### BEFORE THE PATENT TRIAL AND APPEAL BOARD

TAIWAN SEMICONDUCTOR MANUFACTURING COMPANY LTD.
Petitioner

v.

GODO KAISHA IP BRIDGE 1 Patent Owner.

Case IPR2017-01843

#### PETITIONER'S UPDATED EXHIBIT LIST



U.S. Patent 7,893,501 IPR2017-01843 Petitioner's Updated Exhibit List

Pursuant to the Board's Order dated December 29, 2017, Petitioner hereby submits the December 19, 2017 Conference Call Transcript as Exhibit 1227 and the infringement contentions dated February 1, 2017 for U.S. Patent 7,893,501 from *Godo Kaisha IP Bridge 1 v. Xilinx, Inc.*, Case No. 2:17-cv-00100 (E.D. Tex.) as Exhibit 1228. Petitioner also hereby submits an updated exhibit list.

Respectfully Submitted,

/ Michael Smith/

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## **CERTIFICATE OF SERVICE**

I hereby certify that, on January 9, 2018, I caused a true and correct copy of the foregoing materials:

- Petitioner's Updated Exhibit List
- Petitioner's List of Exhibits
- Exhibits 1227 and 1228

to be served via email on the following counsel of record as listed in Patent Owner's Mandatory Notices:

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> > Respectfully Submitted,

/Michael Smith/

Michael H. Smith Registration No. 71,190



# <u>PETITIONER'S LIST OF EXHIBITS FOR</u> <u>IPR2017-01843</u>

Exhibit	Description
1201	U.S. Patent No. 7,893,501
1202	Declaration of Stanley R. Shanfield, Ph.D. Regarding U.S. Patent No. 7,893,501, Claims 1, 4-5, 7, 9-11, 15-18, and 23-25 ("Shanfield Decl.")
1203	Applicant's Amendment and Response dated August 6, 2010
1204	U.S. Patent No. 5,960,270 to Misra et al. ("Misra")
1205	U.S. Patent No. 5,472,890 to Oda ("Oda")
1206	U.S. Patent No. 6,110,827 to Chien et al. ("Chien")
1207	U.S. Patent Publication No. 2002/0145156 to Igarashi et al. ("Igarashi")
1208	U.S. Patent No. 6,072,237 to Jang et al. ("Jang")
1209	J. Plummer et al., <i>Silicon VLSI Technology: Fundamentals</i> , <i>Practice, and Modeling</i> , (1 <sup>st</sup> ed. 2000)
1210	K. Maex et al, <i>Simply irresistible silicides</i> , Physics World, at 35-39 (Nov. 1995)
1211	J. Rabaey et al., <i>Digital Integrated Circuits</i> , at 40-44 (2d ed. 2003) ("Rabaey")
1212	U.S. Patent Application No. 2003/0011043 ("Roberts")
1213	U.S. Patent No. 6,383,906 to Wieczorek et al. ("Wieczorek")
1214	Kastenmeier et al., "Remote plasma etching of silicon nitride and silicon dioxide using NF <sub>3</sub> /O <sub>2</sub> gas mixtures," J. Vac. Sci. Technol. A 16(4), Jul/Aug 1998 ("Kastenmeier")



Exhibit	Description
1215	U.S. Patent No. 6,444,566 to Tsai et al. ("Tsai")
1216	U.S. Patent No. 6,326,301 to Venkatesan et al. ("Venkatesan")
1217	U.S. Patent No. 6,228,777 ("Arafa")
1218	U.S. Patent No. 6,509,234
1219	U.S. Patent No. 5,726,479
1220	U.S. Patent No. 6,512,266
1221	U.S. Patent No. 6,806,584
1222	Notice of Allowance dated October 15, 2010
1223	Interaction of Transition Metals with Silicon (100): The Ni-Si, Co-Si and Au/Si(100) Systems, by Steven Naftel, Graduate Program in Chemistry, The University of Western Ontario, July 1999 (Naftel)
1224	U.S. Patent No. 5,286,344 to Blalock et al. ("Blalock")
1225	W.O. Publication No. 2002/043151 with certified English translation ("Shimizu")
1226	Mizuno, T. et al., "Hot-Carrier Injection Suppression Due to the Nitride-Oxide LDD Spacer Structure", IEEE Trans. Electron Dev., Vol. 38, No. 3, March, 1991 ("Mizuno")
1227	December 19, 2017 Conference Call Transcript
1228	Infringement contentions dated February 1, 2017 for U.S. Patent 7,893,501 from <i>Godo Kaisha IP Bridge 1 v. Xilinx, Inc.</i> , Case No. Case No. 2:17-cv-00100 (E.D. Tex.)

