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(54) SEMICONDUCTOR DEVICE UTILIZING DUMMY FEATURES TO FORM UNIFORM SIDEWALL STRUCTURES

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(30) Foreign Application Priority Data

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(51)	Int. Cl. ⁷		H01L 23/52
(52)	IIS CL	257/	365. 257/774

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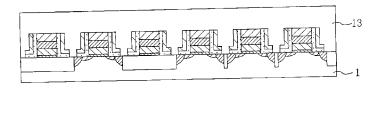
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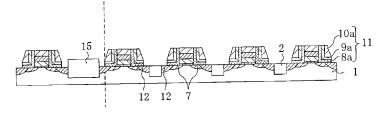
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(57) ABSTRACT

An inventive semiconductor device includes: a substrate; a plurality of first projections each including at least a gate electrode and formed on the substrate; and a plurality of second projections formed on the substrate. When a contour surface constituted by the uppermost face of the substrate and by side and upper faces of the first and second projections is measured for every partial area per unit area of the substrate, the maximum partial area of the contour surface is 1.6 or less times larger than the minimum partial area of the contour surface.

12 Claims, 11 Drawing Sheets





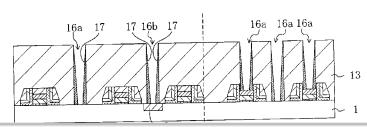




FIG. 1 sidewall insulating film thickness of sidewal 1.20 lm in sparse region 1.00 0.80 0.60 ratio of the thickness of dense region to insulating fi 0.40 0.20 0.00 2.6 2.2 1.8 2.0 1. 2 1.6 1.0 1.4 ratio of surface area per unit area in dense region to surface area per unit area in sparse region



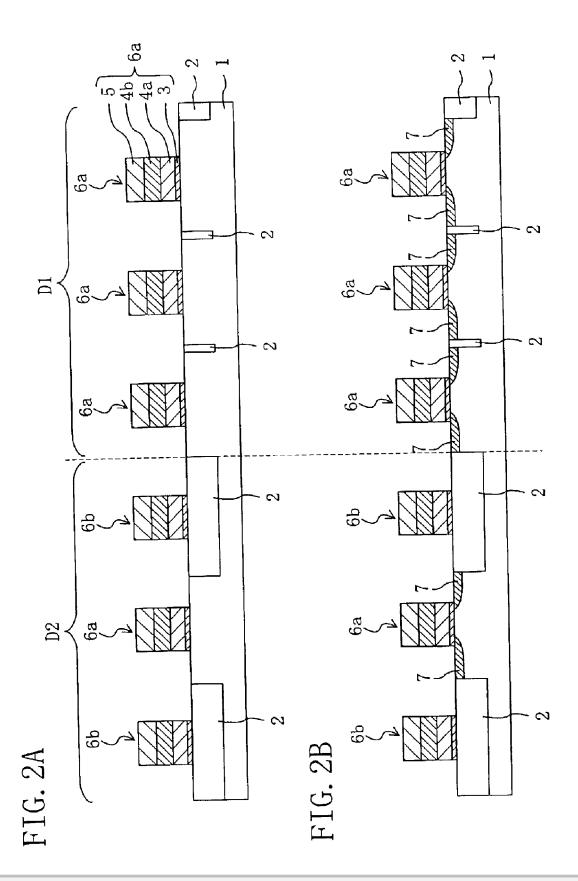
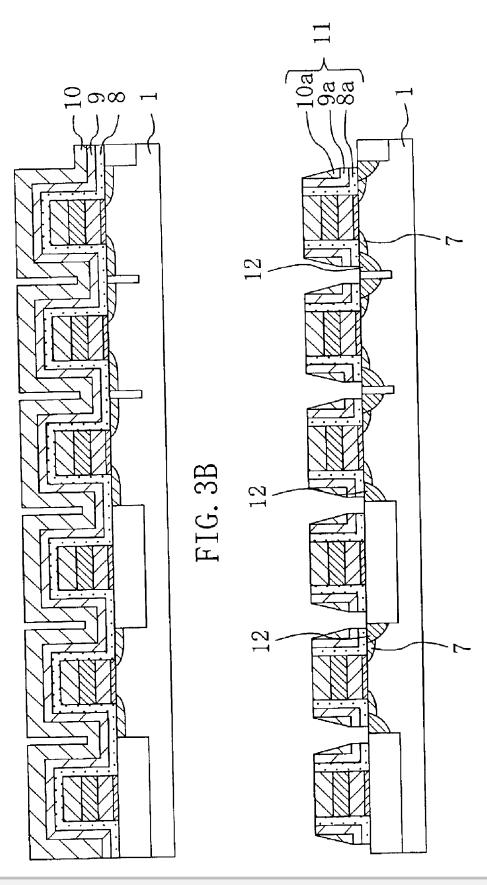
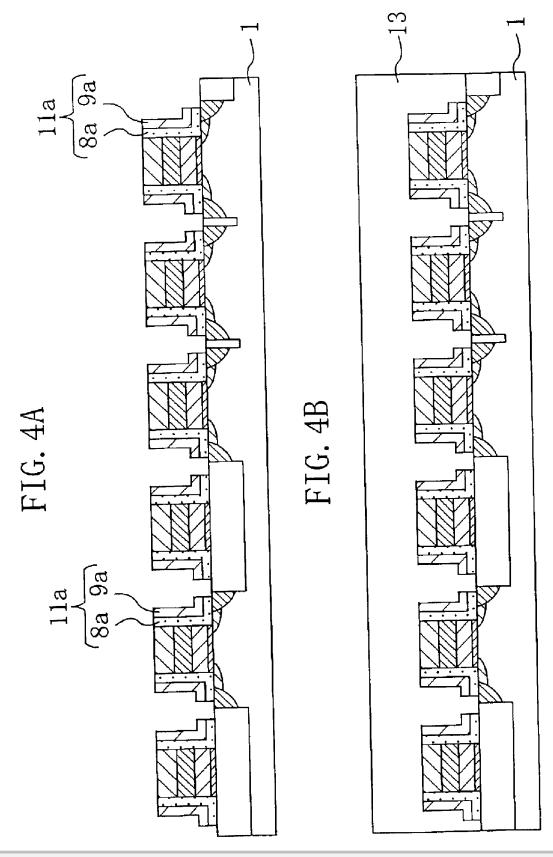


FIG. 3A





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