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United States Patent [19]

Agata et al.

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[54]	SEMICONDUCTOR DEVICE HAVING AT
	LEAST ONE SYMMETRICAL PAIR OF
	MOSFETS

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U.S. Cl. 257/369; 257/390; [52] 257/401; 257/618; 257/653; 257/773

[58] Field of Search 257/369, 390, 401, 618, 257/653, 773

[56]

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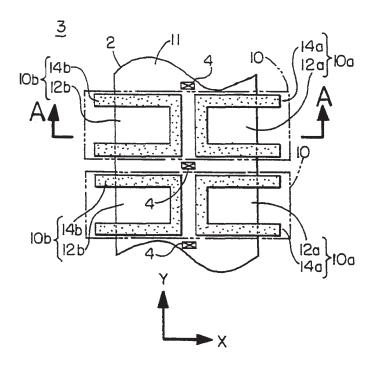
Search Report for European Appl. 93104940.7 mailed Oct. 4, 1993.

Primary Examiner—Edward Wojciechowicz Attorney, Agent, or Firm-Renner, Otto, Boisselle & Sklar

[57] ABSTRACT

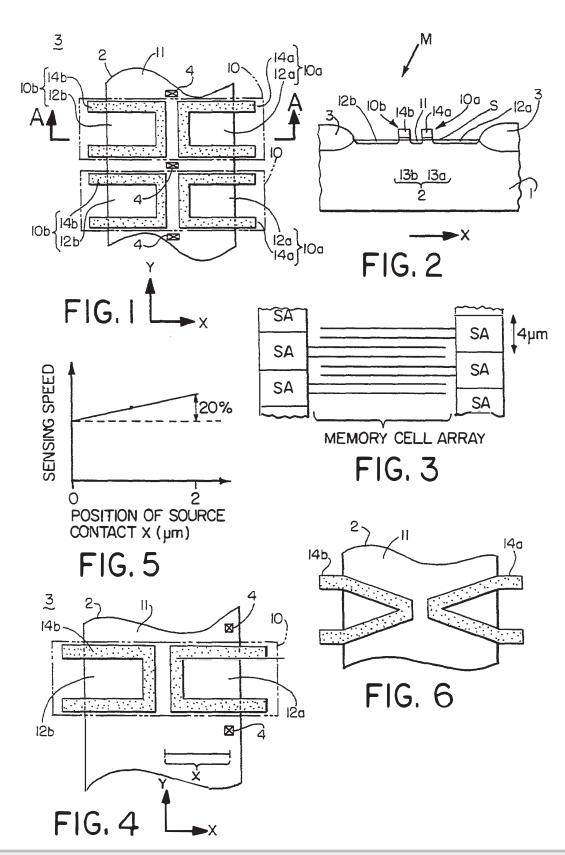
A semiconductor device having at least one symmetrical pair of MOSFETs is provided. The device includes a semiconductor layer having an upper surface, an active region formed in the upper surface, an isolation region formed in the upper surface and enclosing the active region, and a pair of MOSFETs formed in the active region, wherein the pair of MOSFETs are symmetrical with respect to a first symmetric plane substantially vertical to the upper surface and also with respect to a second symmetric plane vertical both to the upper surface and to the first symmetric plane, each of the pair of MOSFETs includes a source region, a drain region, and a channel region formed in an upper surface of the active region, the source region is shared by the pair of MOSFETs, and the drain region is spatially isolated from the source region by the channel region.

30 Claims, 4 Drawing Sheets



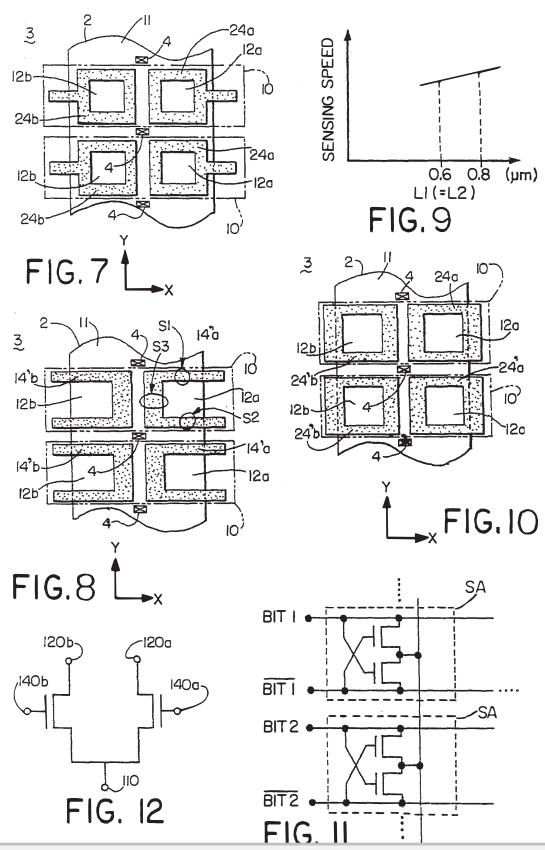
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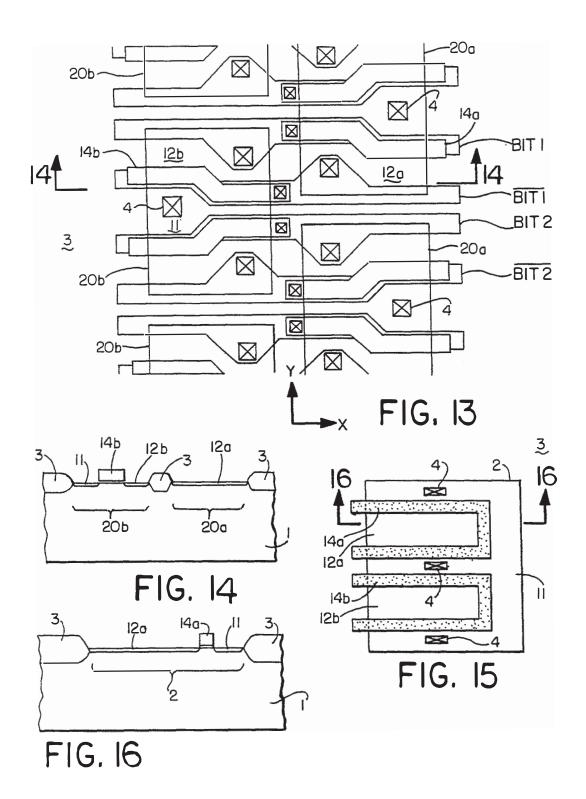


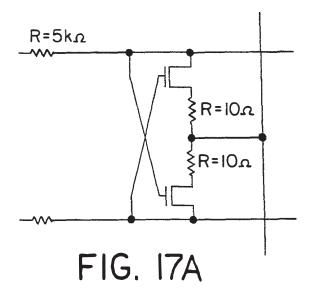


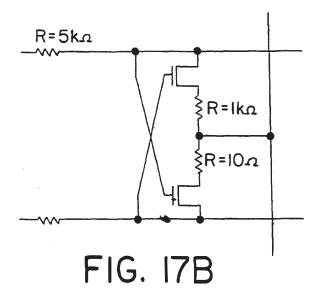


Feb. 14, 1995









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